Understanding structure of parameters in neural networks

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Abstract

1. Introduction

Main criticism of neural networks is in the lack of interpretability of their results. This is serious issue, as it hinders process of knowledge injection, and makes difficult to predict their behaviour in the new conditions (not presented during training). This issue is the consequence of superficial understanding of computation underlaying neural networks. We do attempt in direction of understanding computation behind neural networks.

We show that learnt parameters of neural networks have very rich low-dimensional linear structure. We are not able to explain reason for its presents. However, such a structure has a far-reach consequences in (i) understanding computation of neural networks, (ii) regularization of neural networks, (iii) optimization, (iv) accelerating testing, (v) re-parametrization. We describe how such structure can be extracted and visualized. We show it for state-of-the-art object recognition models (~ 60 mln. learnable parameters). Moreover, we explore consequences of this low-rank linear structure for simpler models.

2. Related Work

Our low-rank approximations get inspired by work of Denil et. al (Denil et al., 2013) on redundancies in neural networks. They shown that based on small fraction of weights (e.g. $\sim 5\%$), one can accurately predict rest of weights. This indicates that neural networks are heavily over-parametrized. All the methods

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presented here focus on exploiting linear structure of such over-parametrization.

3. Low Rank Approximations

In this section, we give theoretical background on lowrank approximations. First, we discuss simplest setting, which is for matrices (two dimensional tensors). Further, we move to approximation of 4-dimensional tensors with 2 convolutional (spacial) dimensions.

3.1. Matrix Low Rank Approximation

Let's consider input $X \in \mathbb{R}^{n \times m}$, and matrix of weights $W \in \mathbb{R}^{m \times k}$. W might have a low-rank (many eigenvalues close to zero), and matrix multiplication of X with W might be very close to the matrix multiplication with approximation of W.

Every matrix can be expressed using singular value decomposition:

$$W = USV^T$$
, where $U \in \mathbb{R}^{m \times m}$, $S \in \mathbb{R}^{m \times k}$, $V \in \mathbb{R}^{k \times k}$

S is has eigenvalues on the diagonal, and apart from it has zeros. W can be approximated by using t most significant eigenvalues from S. We can write approximation as

$$\hat{W} = \hat{U}\hat{S}\hat{V}^T$$
, where $\hat{U} \in \mathbb{R}^{m \times t}, \hat{S} \in \mathbb{R}^{t \times t}, \hat{V} \in \mathbb{R}^{t \times k}$

3.2. Tensor Low Rank Approximations

In typical object recognition architectures, the convolutional tensors resulting from the training exhibit strong redundancy and regularity across all its dimensions. A particularly simple way to exploit such regularity is to linearly compress the tensors, which amounts to finding low-rank approximations.

Convolution kernels can be described as a 4-dimensional tensors. Let $W \in \mathbb{R}^{C \times X \times Y \times F}$ be convolutional kernel. C is input number of feature maps (or colors), X, Y are special dimensions over which we compute convolution, and F is the target number of feature maps. Let $I \in \mathbb{R}^{C \times N \times M}$ denote an input signal. A generic convolutional layer is defined as

$$I * W(f, x, y) = \sum_{c=1}^{C} \sum_{x'=-X/2}^{X/2} \sum_{y'=-Y/2}^{Y/2} I(c, x - x', y - y') W(c, x', y', f)$$

Low-rank approximation of tensors is very similar to low-rank approximation of matrices. However, first we have to choose 2 dimensions with respect to which we are approximating, and treat rest of tensor as it would be a matrix.

3.2.1. Monochromatic filters

For generic convolution there are connections between all output feature maps, and all input feature maps. However, one could imagine that this connections in trained network in some basis could become sparse.

4. Consequences

4.1. Regularization

4.2. Speeding up testing

Training large neural networks (NN) takes weeks, or even months. This can hinder research, and there have been extensive effort devoted to speed up training procedure. However, resource-wise from perspective of companies executing neural networks on internet-scale data (e.g. annotating images), this is not the main cost. Major cost is in the final stage, where network is evaluated on the target data, which is present in quantities of billions. We focus here on speeding up evaluation of trained NN, which directly maps to the cost of executing NN on internet-scale data.

5. Numerical Experiments

5.1. MNIST

5.2. Imagenet

5.2.1. First layer analysis

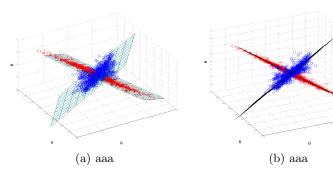


Figure 1. Experiment results

5.2.2. Further layers analysis

6. Discussion

References

Denil, Misha, Shakibi, Babak, Dinh, Laurent, Ranzato, Marc'Aurelio, and de Freitas, Nando. Predicting parameters in deep learning. arXiv preprint arXiv:1306.0543, 2013.

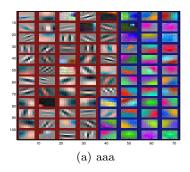


Figure 2. First layer filters grouped by subspace cluster

Le, Q. V., Ngiam, J., Chen, Z., Chia, D., Koh, P. W., and Ng, A. Y. Tiled convolutional neural networks. In Advances in Neural Information Processing Systems, 2010.

Le, Quoc V, Ranzato, Marc'Aurelio, Monga, Rajat, Devin, Matthieu, Chen, Kai, Corrado, Greg S, Dean, Jeff, and Ng, Andrew Y. Building highlevel features using large scale unsupervised learning. arXiv preprint arXiv:1112.6209, 2011.

Lowe, David G. Object recognition from local scaleinvariant features. In *Computer vision*, 1999. The proceedings of the seventh IEEE international conference on, volume 2, pp. 1150–1157. Ieee, 1999.

Mathieu, Michael, Henaff, Mikael, and LeCun, Yann. Fast training of convolutional networks through ffts. arXiv preprint arXiv:1312.5851, 2013.

Vanhoucke, Vincent, Senior, Andrew, and Mao, Mark Z. Improving the speed of neural networks on cpus. In *Proc. Deep Learning and Unsupervised Feature Learning NIPS Workshop*, 2011.

Zeiler, Matthew D, Taylor, Graham W, and Fergus, Rob. Adaptive deconvolutional networks for mid and high level feature learning. In *Computer Vision (ICCV)*, 2011 IEEE International Conference on, pp. 2018–2025. IEEE, 2011.