

# CS3100

NP-Hardness &  
(more) reductions

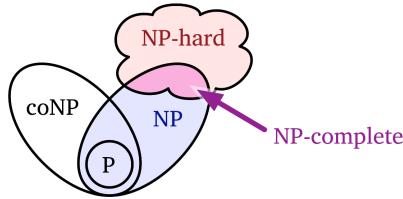


## Announcements

- Next HW - due on paper  
next Wednesday
- HW after will be  
oral grading → Friday Nov 17
- Office hours Friday:  
10-11 am  
12-1 pm
- No class Nov. 27  
look for reading assignment

Recall :

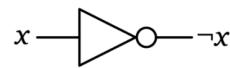
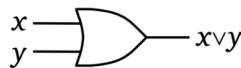
A problem is NP-Complete if it is both:



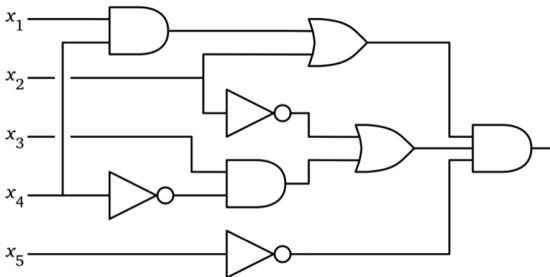
- In NP: A "yes" answer  
can be checked in  
polynomial time
- NP-Hard :  
via reductions

Thm: (Cook-Levine)

~~Circuit SAT~~ Circuit SAT is NP-Complete



An AND gate, an OR gate, and a NOT gate.

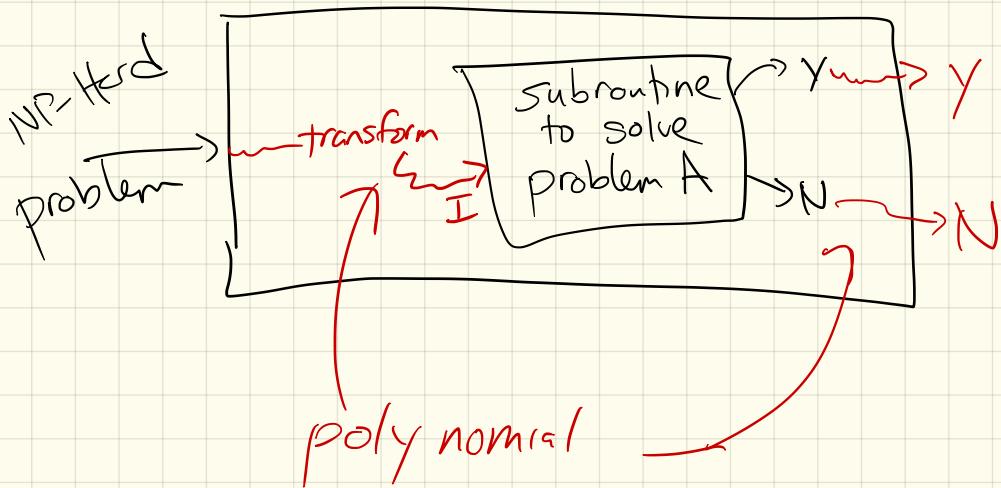


A boolean circuit. inputs enter from the left, and the output leaves to the right.

"Proof": We can turn any Turing machine into a circuit.

To prove any other problem A is NP-Hard, we'll use a reduction:

Reduce a known NP-hard problem to A.



Thm: SAT is NP-Complete

↪ logical sentences

$$(a \wedge b) \vee (c \wedge \bar{d}) \wedge (a \Rightarrow (b \wedge c)) \dots$$

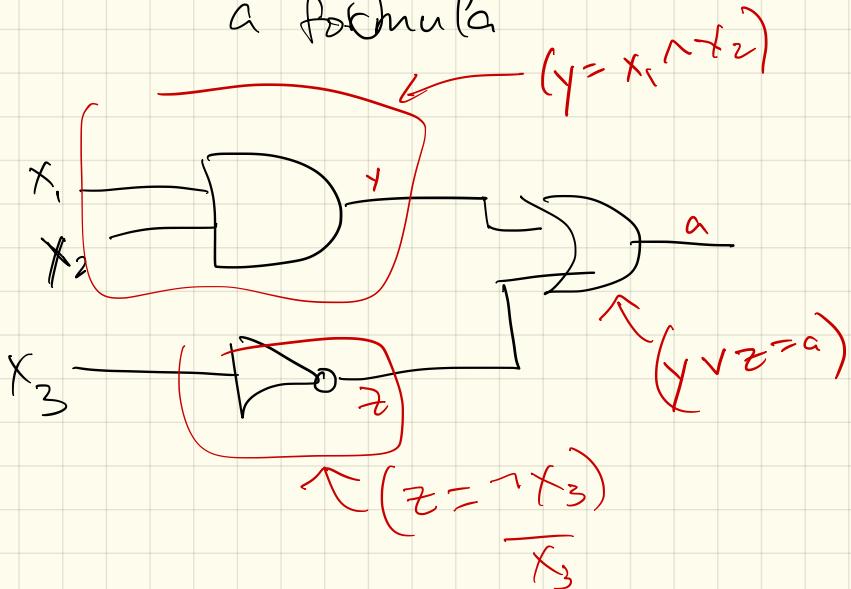
Pf:

• In NP:

Given a set of boolean inputs, linear time to compute output

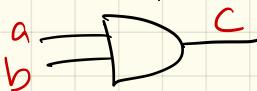
• NP-Hard: reduce CIRCUITSAT to SAT:

For each gate, can write a formula



More carefully:

1) For any gate, can transform:  
     $\wedge$  1 clause per gate



$$\therefore (c = a \wedge b)$$

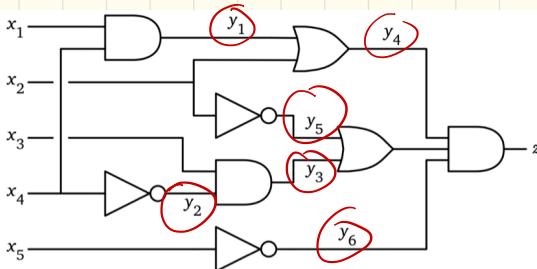


$$\therefore (c = a \vee b)$$



$$\therefore (d = \overline{a})$$

2) "And" these together,  
+ want final output  
true:



$$(y_1 = x_1 \wedge x_4) \wedge (y_2 = \overline{x_4}) \wedge (y_3 = x_3 \wedge y_2) \wedge (y_4 = y_1 \vee x_2) \wedge \\ (y_5 = \overline{x_2}) \wedge (y_6 = \overline{x_5}) \wedge (y_7 = y_3 \vee y_5) \wedge (z = y_4 \wedge y_7 \wedge y_6) \wedge z$$

Is this poly-size?

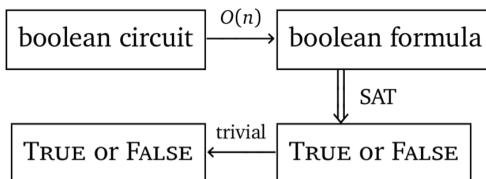
Given  $n$  inputs +  $m$  gates:

Variables:  $\begin{cases} 1 \text{ per input} \\ 1 \text{ per gate} \end{cases} \} n+m$

Clauses: 1 per gate

Size of SAT formula:  
 $m+n+m = O(m+n)$

End reduction:



$$T_{CSAT}(n) \leq O(n) + T_{SAT}(O(n)) \implies T_{SAT}(n) \geq T_{CSAT}(\Omega(n)) - O(n)$$

(Here, "n" is total input size)

Next: 3SAT :

a restricted version of SAT

Dfn: Conjunctive Normal Form (CNF)

$$\overbrace{(a \vee b \vee c \vee d) \wedge (b \vee \bar{c} \vee \bar{d}) \wedge (\bar{a} \vee c \vee d) \wedge (a \vee \bar{b})}^{\text{clause}} \quad \downarrow$$

↑ ↑ ↑ ↑      ↑  
"OR"s            "and"

3SAT: SAT restricted to be  
CNF & exactly 3 literals  
per clause

$$(a \vee b \vee c) \wedge (\bar{a} \vee d \vee \bar{x}) \wedge \dots$$

$\underbrace{\quad\quad\quad}_{3 \text{ literals}}$

Thm: 3SAT is NP-Hard

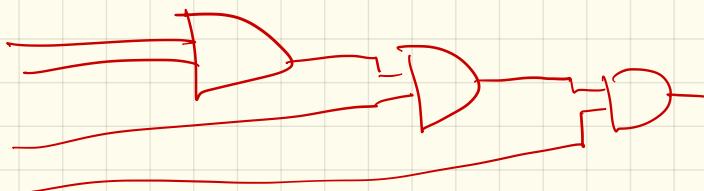
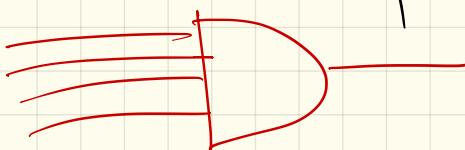
Pf: Reduce circuitSAT to 3SAT.

Need to show any circuit can be transformed to CNF form

(so last reduction fails)

Steps:

① Rewrite so each gate has 2 inputs:



② Write formula, like in SAT.  
3 types:

$$\boxed{y = a \vee b \\ y = a \wedge b \\ y = \bar{a}}$$

③ Now, change to CNF:  
go back to truth tables

$$a = b \wedge c \rightarrow (a \vee \bar{b} \vee \bar{c}) \wedge (\bar{a} \vee b) \wedge (\bar{a} \vee c)$$

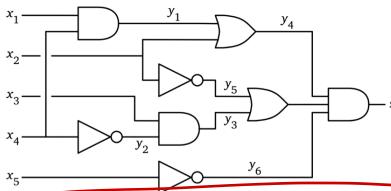
$$a = b \vee c \rightarrow (\bar{a} \vee b \vee c) \wedge (a \vee \bar{b}) \wedge (a \vee \bar{c})$$

$$a = \bar{b} \rightarrow (a \vee b) \wedge (\bar{a} \vee \bar{b})$$

④ Now, need 3 per clause!

$$\begin{aligned} a &\rightarrow \underbrace{(a \vee x \vee y) \wedge (a \vee \bar{x} \vee y)}_{1} \wedge \underbrace{(a \vee x \vee \bar{y}) \wedge (a \vee \bar{x} \vee \bar{y})}_{3} \\ a \vee b &\rightarrow \underbrace{(a \vee b \vee x) \wedge (a \vee b \vee \bar{x})}_{\cup} \end{aligned}$$

Note : Bigger!



$$(y_1 = x_1 \wedge x_4) \wedge (y_2 = \bar{x}_4) \wedge (y_3 = x_3 \wedge x_2) \wedge (y_4 = y_1 \vee x_2) \wedge \\ (y_5 = \bar{x}_2) \wedge (y_6 = \bar{x}_3) \wedge (y_7 = y_3 \vee y_5) \wedge (z = y_4 \wedge y_7 \wedge y_6) \wedge z$$

A boolean circuit with gate variables added, and an equivalent boolean formula.

$$\begin{aligned}
& (y_1 \vee \bar{x}_1 \vee \bar{x}_4) \wedge (\bar{y}_1 \vee x_1 \vee z_1) \wedge (\bar{y}_1 \vee x_1 \vee \bar{z}_1) \wedge (\bar{y}_1 \vee x_4 \vee z_2) \wedge (\bar{y}_1 \vee x_4 \vee \bar{z}_2) \\
& \wedge (y_2 \vee x_4 \vee z_3) \wedge (y_2 \vee x_4 \vee \bar{z}_3) \wedge (\bar{y}_2 \vee \bar{x}_4 \vee z_4) \wedge (\bar{y}_2 \vee \bar{x}_4 \vee \bar{z}_4) \\
& \wedge (y_3 \vee \bar{x}_3 \vee \bar{y}_2) \wedge (\bar{y}_3 \vee x_3 \vee z_5) \wedge (\bar{y}_3 \vee x_3 \vee \bar{z}_5) \wedge (\bar{y}_3 \vee y_2 \vee z_6) \wedge (\bar{y}_3 \vee y_2 \vee \bar{z}_6) \\
& \wedge (\bar{y}_4 \vee y_1 \vee x_2) \wedge (y_4 \vee \bar{x}_2 \vee z_7) \wedge (y_4 \vee \bar{x}_2 \vee \bar{z}_7) \wedge (y_4 \vee \bar{y}_1 \vee z_8) \wedge (y_4 \vee \bar{y}_1 \vee \bar{z}_8) \\
& \wedge (y_5 \vee x_2 \vee z_9) \wedge (y_5 \vee x_2 \vee \bar{z}_9) \wedge (\bar{y}_5 \vee \bar{x}_2 \vee z_{10}) \wedge (\bar{y}_5 \vee \bar{x}_2 \vee \bar{z}_{10}) \\
& \wedge (y_6 \vee x_5 \vee z_{11}) \wedge (y_6 \vee x_5 \vee \bar{z}_{11}) \wedge (\bar{y}_6 \vee \bar{x}_5 \vee z_{12}) \wedge (\bar{y}_6 \vee \bar{x}_5 \vee \bar{z}_{12}) \\
& \wedge (\bar{y}_7 \vee y_3 \vee y_5) \wedge (y_7 \vee \bar{y}_3 \vee z_{13}) \wedge (y_7 \vee \bar{y}_3 \vee \bar{z}_{13}) \wedge (y_7 \vee \bar{y}_5 \vee z_{14}) \wedge (y_7 \vee \bar{y}_5 \vee \bar{z}_{14}) \\
& \wedge (y_8 \vee \bar{y}_4 \vee y_7) \wedge (\bar{y}_8 \vee y_4 \vee z_{15}) \wedge (\bar{y}_8 \vee y_4 \vee \bar{z}_{15}) \wedge (\bar{y}_8 \vee y_7 \vee z_{16}) \wedge (\bar{y}_8 \vee y_7 \vee \bar{z}_{16}) \\
& \wedge (y_9 \vee \bar{y}_8 \vee \bar{y}_6) \wedge (\bar{y}_9 \vee y_8 \vee z_{17}) \wedge (\bar{y}_9 \vee y_8 \vee \bar{z}_{17}) \wedge (\bar{y}_9 \vee y_6 \vee z_{18}) \wedge (\bar{y}_9 \vee y_6 \vee \bar{z}_{18}) \\
& \wedge (y_9 \vee z_{19} \vee z_{20}) \wedge (y_9 \vee \bar{z}_{19} \vee z_{20}) \wedge (y_9 \vee z_{19} \vee \bar{z}_{20}) \wedge (y_9 \vee \bar{z}_{19} \vee \bar{z}_{20})
\end{aligned}$$

How big? If exponential, no good

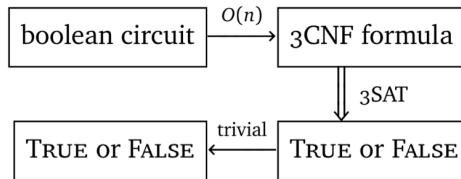
Still polynomial:

For each gate:

turned into  $\leq 3$  clauses  
(wrong sizes)  
↪  $\leq 4$  clause

$\Rightarrow \leq 12$  clauses per gate

So:  $\Rightarrow O(mn)$  size

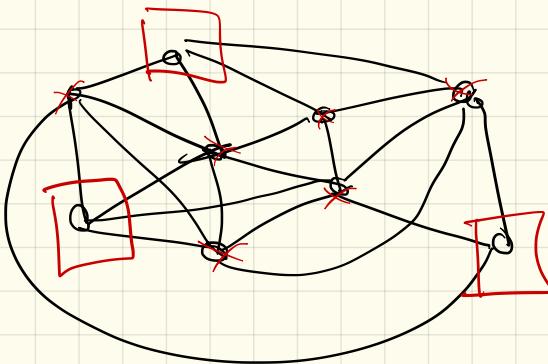


$$T_{\text{CSAT}}(n) \leq O(n) + T_{\text{3SAT}}(O(n)) \implies T_{\text{3SAT}}(n) \geq T_{\text{CSAT}}(\Omega(n)) - O(n)$$

Next Problem:

Independent Set:

A set of vertices in a graph with no edges between them:



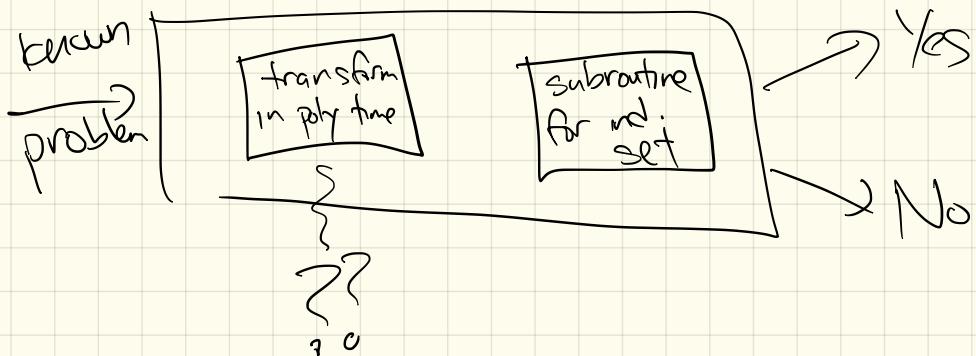
Decision version:

Given  $G$  &  $\#k$ , is there an indep set of size  $\geq k$ ?

In NP: Given  $k$  vertices, check if any edges b/t them.

Challenge: No booleans!

But reduction needs to  
take known NP-hard  
problem + build a  
graph!



We'll use 3SAT

(but stop and marvel  
a bit first...)

## Reduction:

Input is 3CNF boolean formula

$$(a \vee b \vee c) \wedge (b \vee \bar{c} \vee \bar{d}) \wedge (\bar{a} \vee c \vee d) \wedge (a \vee \bar{b} \vee \bar{d})$$



① Make a vertex for each literal in each clause

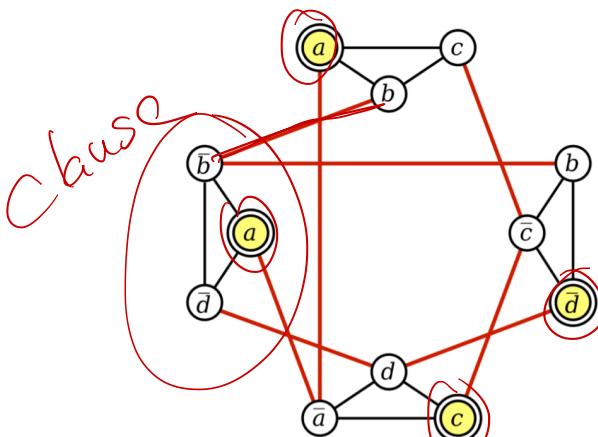
② Connect two vertices if:

- they are in some clause

- they are a variable & its inverse

# Example :

$$(a \vee b \vee c) \wedge (b \vee \bar{c} \vee \bar{d}) \wedge (\bar{a} \vee c \vee d) \wedge (\bar{a} \vee \bar{b} \vee \bar{d})$$



A graph derived from a 3CNF formula, and an independent set of size 4.

Claim:

formula is Satisfiable



$G$  has independent set  
of size  $n$  ( $\Leftarrow \# \text{ input variables}$ )  
~~variables~~  
clauses

p.f.:  $\Rightarrow$  Say have a satisfying assignment.

Each clause has at least 1 true variable.

Choose the corresponding vertex in  $G$  to be in an independent set:

1 per "triangle" (clause)

know no 2 are connected,  
since satisfying assignment

means  $x$  is true

(so  $\bar{x}$  won't be)

So, must have indep set of size exactly = # of clauses.

$\Leftarrow$ : Start w/ indep set in  $G$ , of size  $m$ .

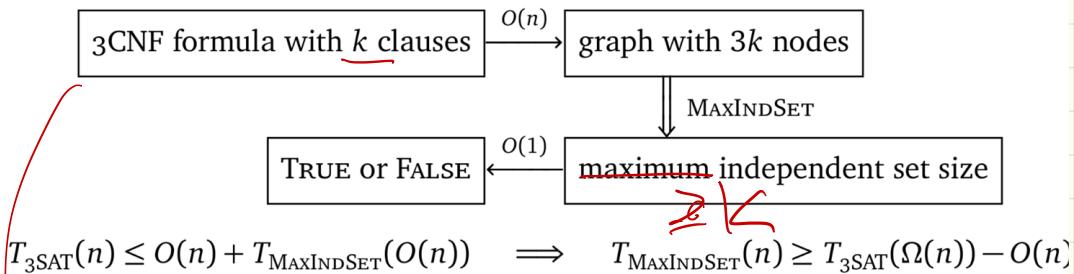
$G$  has  $\cancel{X}$ 's : means indep set choose  $\leq 1$  per triangle.

$\Rightarrow$  exactly 1 per  $\Delta$ .

Since indep set, never choose both  $X + \bar{X}$  in different clauses.

Build sat. assignment by marking all vertices in indep. set as true.  
(others don't matter)

So :



$$\xrightarrow{\text{# clauses} + \text{# inputs}} O(k+n)$$

Know : { 3SAT  
Circuit SAT  
Independent set of size  $k$   
are NP-hard