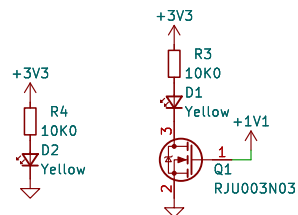
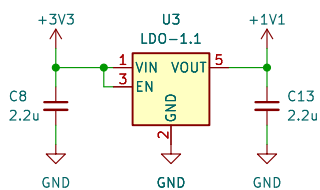
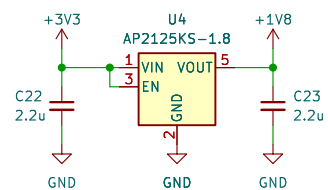


Pin	SD	SPI
1	DAT2	X
2	CD/DAT3	CS
3	CMD	DI
4	VDD	VDD
5	CLK	SCLK
6	VSS	VSS
7	DAT0	DO
8	DAT1	X



**P42 Electronics Design**  
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**Title: P42-Pico-M.2**

Size: B  
KiCad E.D.A. 8.0.8

Date: 2025-03-11

Rev: 3.0  
Id: 1/2

