

# Synergistic Processing Element(SPE)

## Synergistic Processing Unit(SPU)

Synergistic  
Execution  
Unit(SXU)

SLS

SSC

Locale Storage(LS)

## Memory Flow Controller(MFC)

Synergistic Memory  
Management(SMM)

Channels and  
SPU Command Queue

Direct Memory Access  
Controller (DMAC)

MMIO Registers and  
Proxy Command Queue

Element Interconnect Bus

