Fig. 3 PSG REGISTER ARRAY

REGIS	TER	B7	В6	85	84	В3	B2	B1	во
R0		8-BIT Fine Tune A							
R1	Channel A Tone Period	4-BIT Coarse Tune A							
R2		8-BIT Fine Tune B							
R3	Channel B Tone Period	4-BIT Coarse Tune B							
R4	0	8-BIT Fine Tune C							
R5	Channel C Tone Period	4-BIT Coarse Tune C							
R6	Noise Period	5-BIT Period Control							
	Enable	IN/OUT Noise				Tone			
R7		IOB:	IOA 2	С	В	A	С	В	Α
R10	Channel A Amplitude				м	L3	L.2	L1	LO
R11	Channel B Amplitude				м	L3	L2	L1	L0
R12	Channel C Amplitude				М	L3	L2	L1	LO
R13	8-BIT Fine Tune E								
R14	Envelope Period	8-BIT Coarse Tune E							
R15	Envelope Shape/Cycle	CONT. ATT. ALT. HOLE							HOLD
R16	I/O Port A Data Store	8-BIT PARALLEL I/O on Port A							
R17	I/O Port B Data Store	8-BIT PARALLEL I/O Port B							

1 octal numbers!