PCB 교육 정리

Pspice Simulation

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  - parametric sweep
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  - Digital signal simulation
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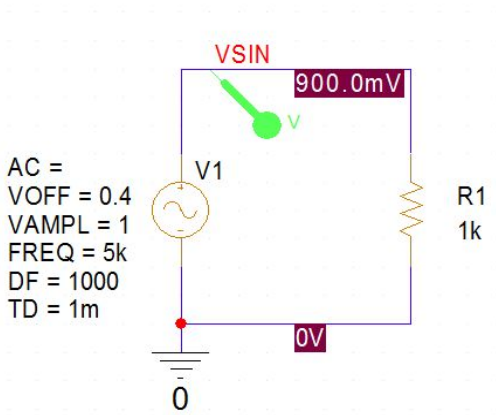
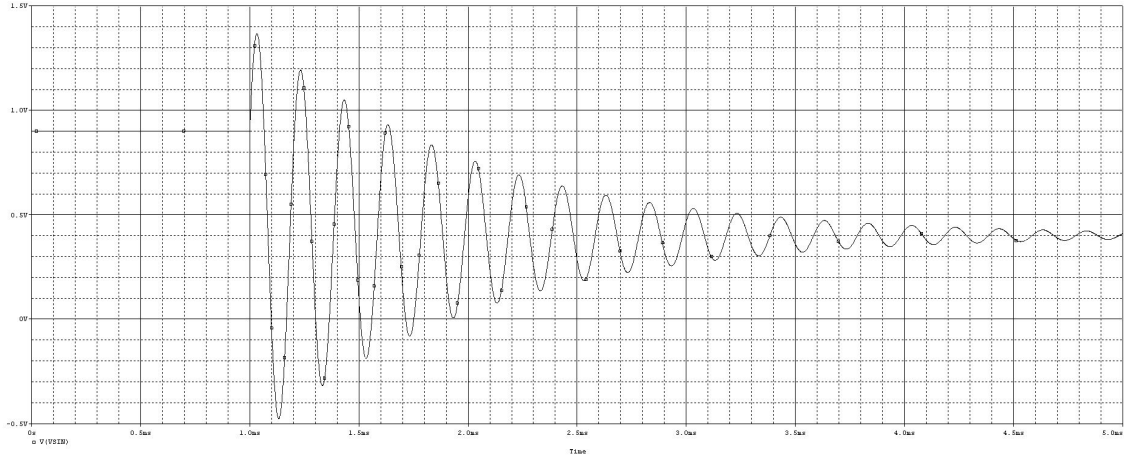
Voltage source

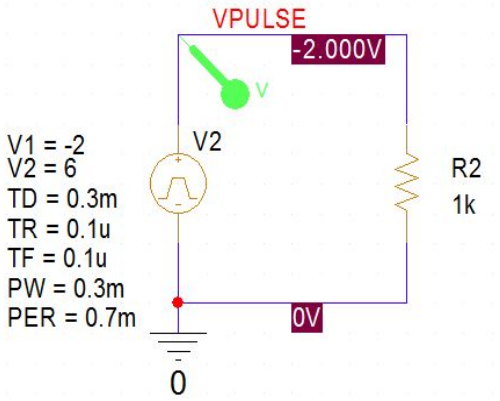
VSIN

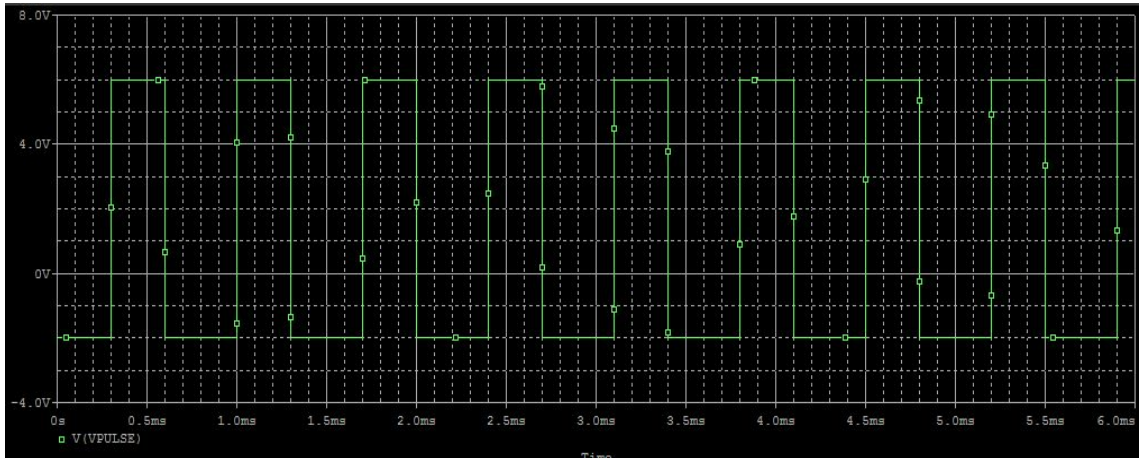




Voltage source

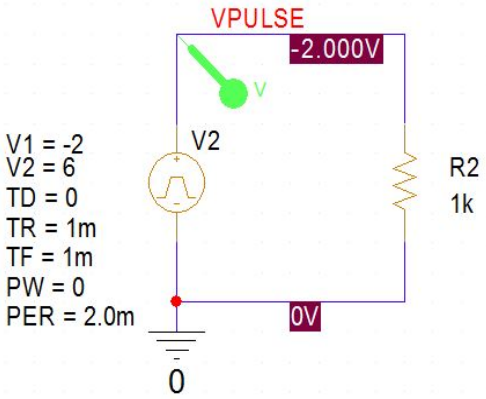
VPULSE

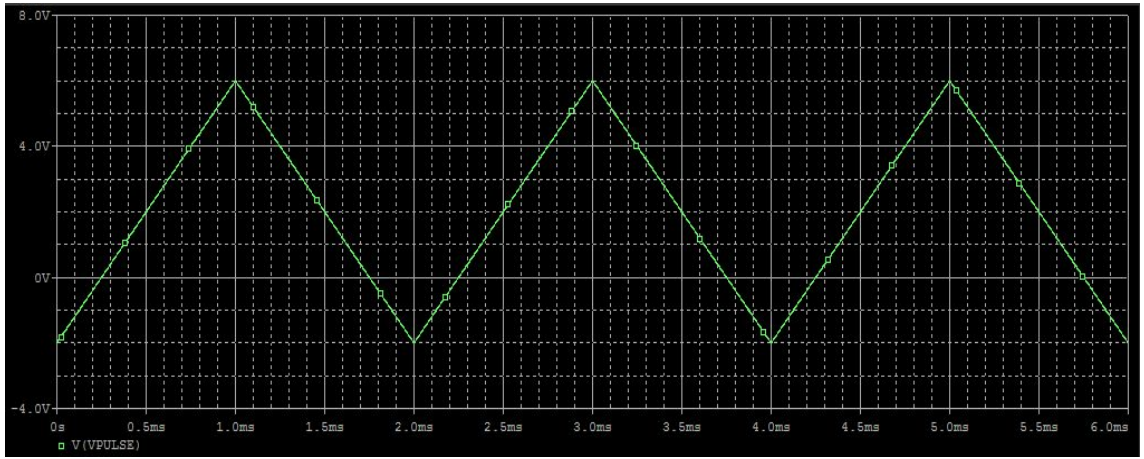




Voltage source

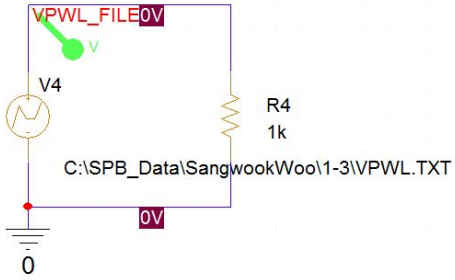
삼각파



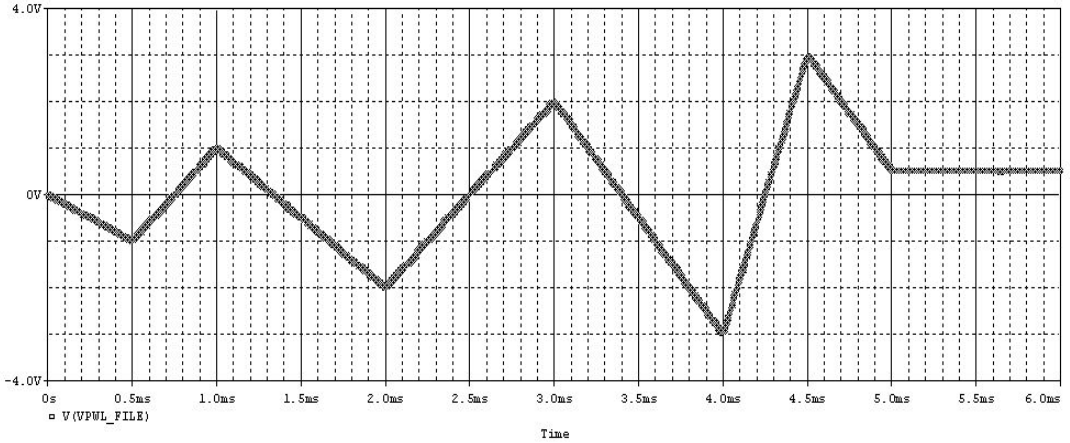


Voltage source

삼각파



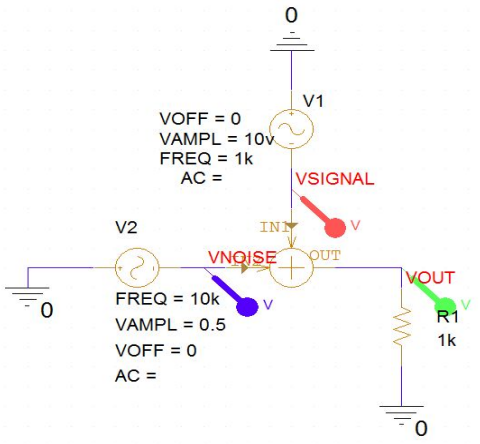
0	0
0.5m	−1
1m	1
2m	−2
3m	2
4m	−3
4.5m	3
5m	0.5

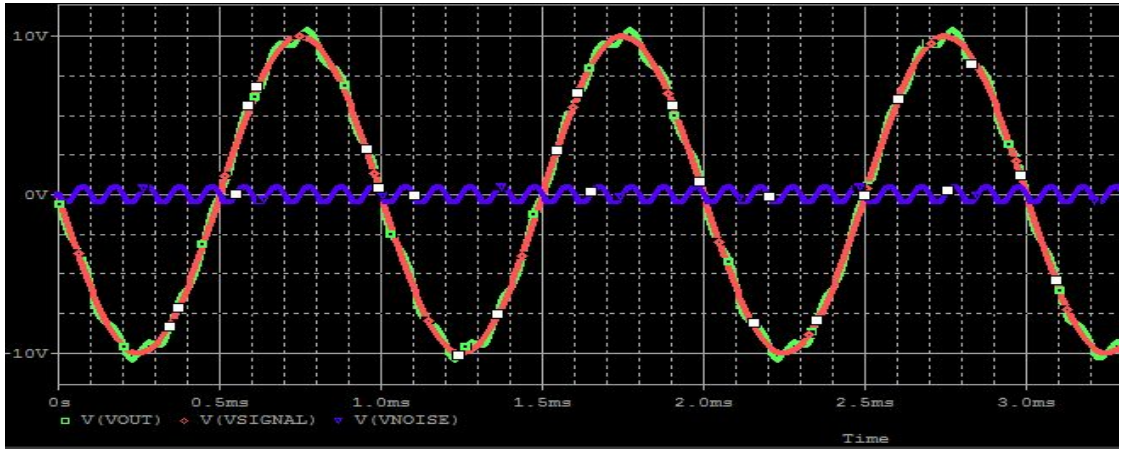


text file을 각 초에 맞게 값을 지정하여 활용한다.

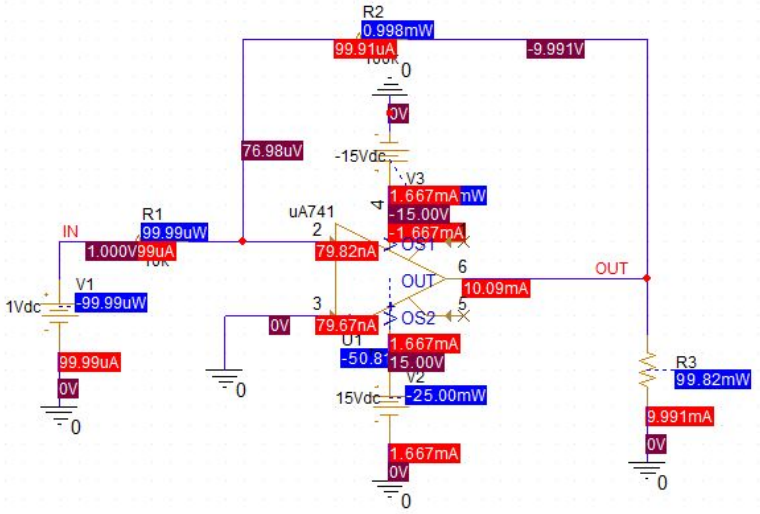
Voltage source

과제 1 : 신호 합성



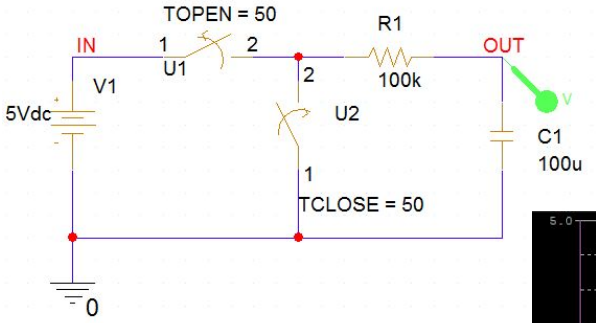


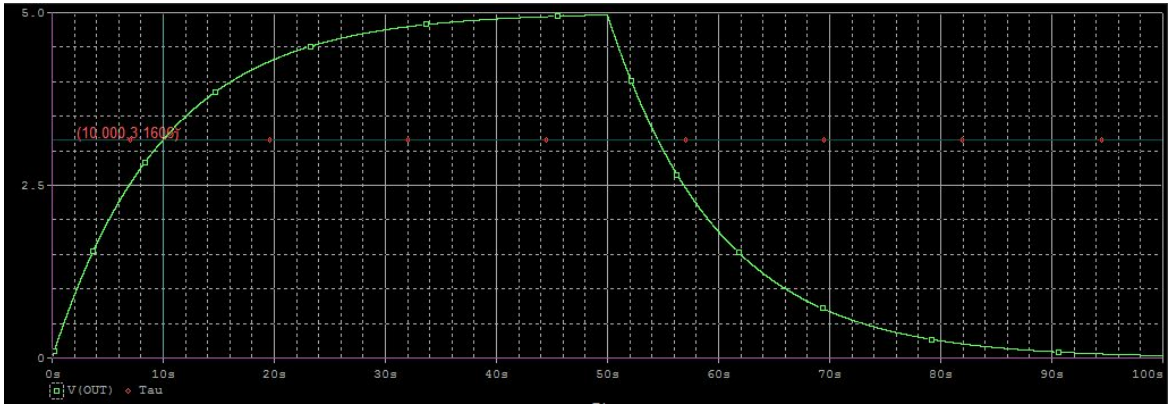
Bias point




```
*****   SMALL-SIGNAL CHARACTERISTICS

  V(OUT)/V_V1 = -9.999E+00

  INPUT RESISTANCE AT V_V1 = 1.000E+04

  OUTPUT RESISTANCE AT V(OUT) = 8.451E-03


     JOB CONCLUDED
```

Simulation

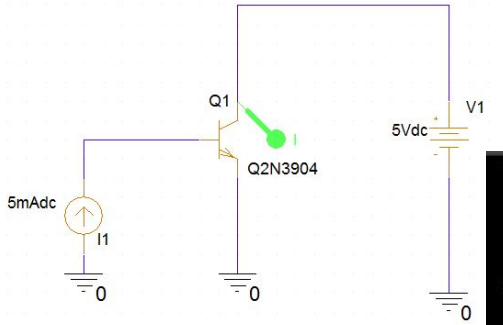

TOPEN = 50
IN
1
U1
2
R1
OUT
V1
2
100k
5Vdc
U2
C1
100u
1
TCLOSE = 50
0

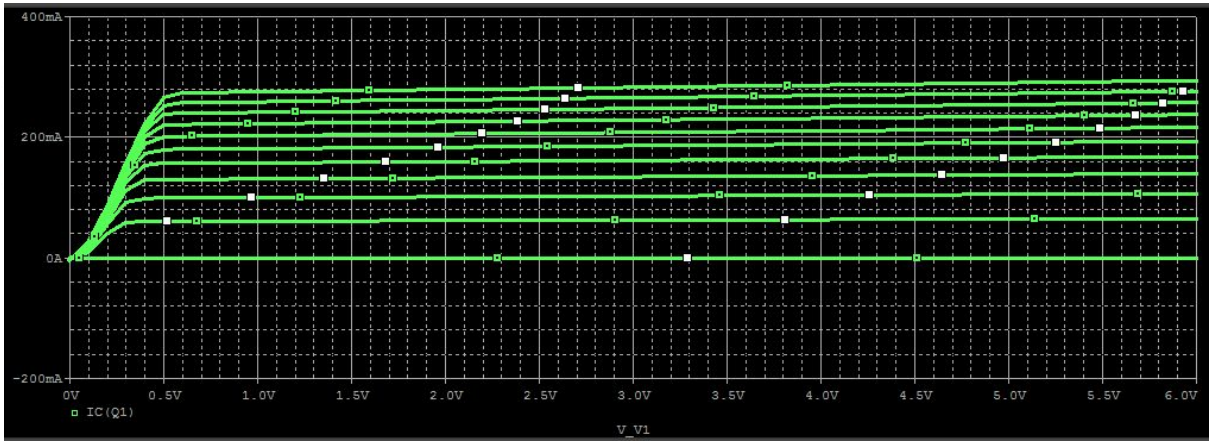

5.0
2.5
0
(10.000,3.1600)
0s
10s
20s
30s
40s
50s
60s
70s
80s
90s
100s
V(OUT)
Tau

Simulation

parametric sweep

VI 특성 곡선 확인

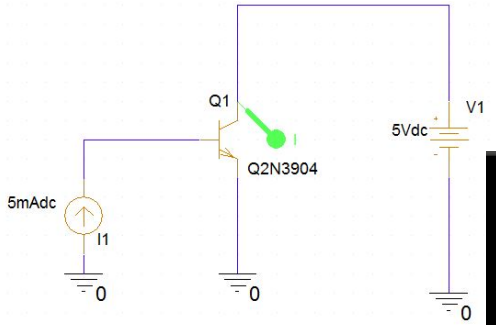


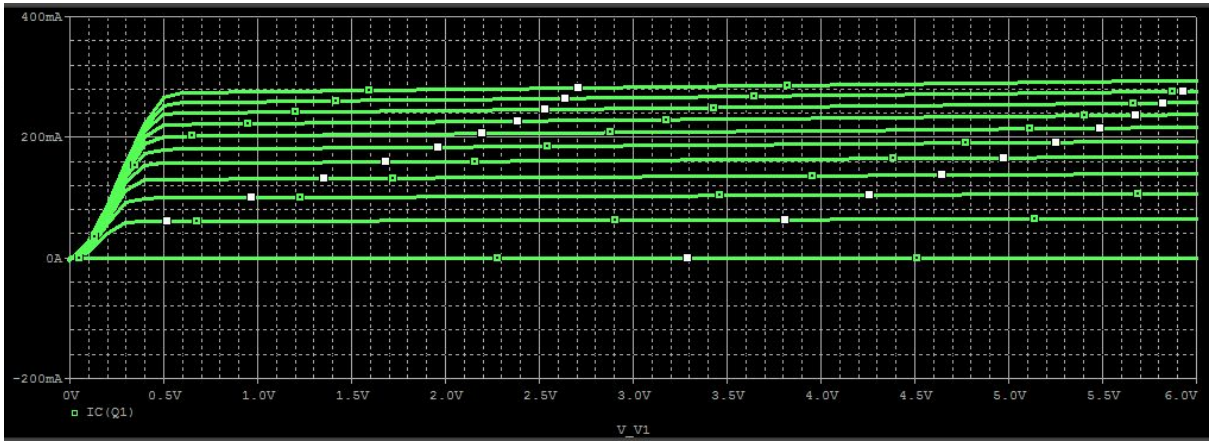


Simulation

DC sweep with secondary sweep

VI 특성 곡선 확인

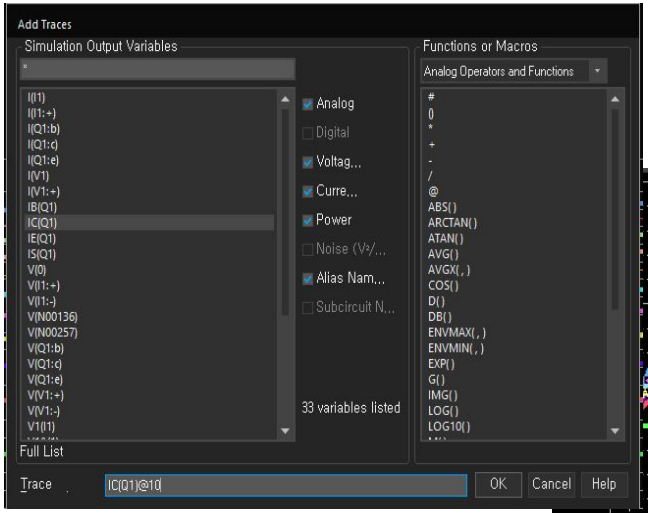


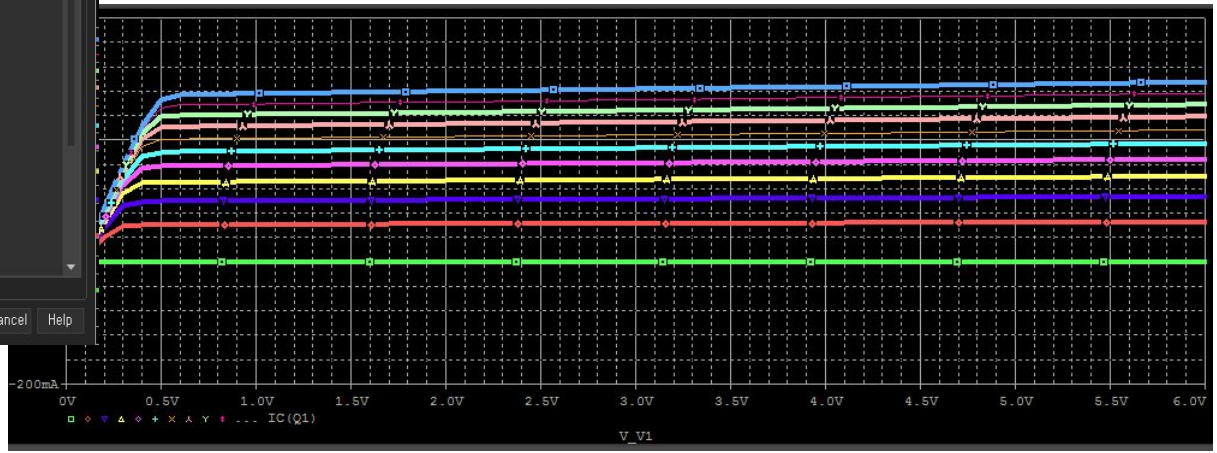


Simulation

parametric sweep

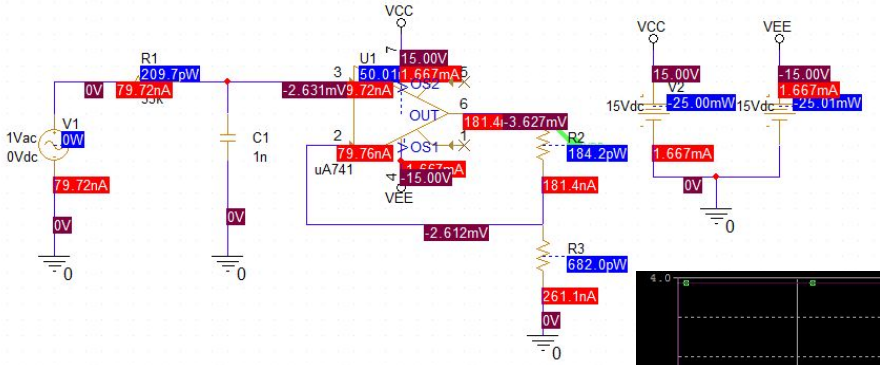
VI 특성 곡선 확인

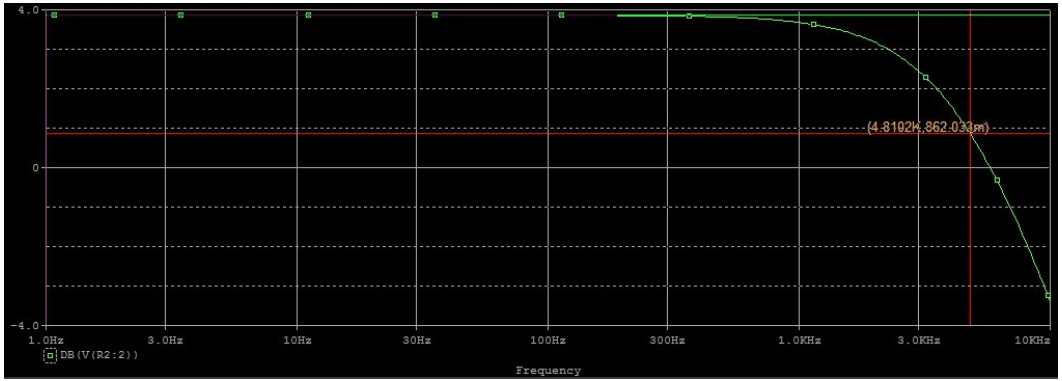




Simulation

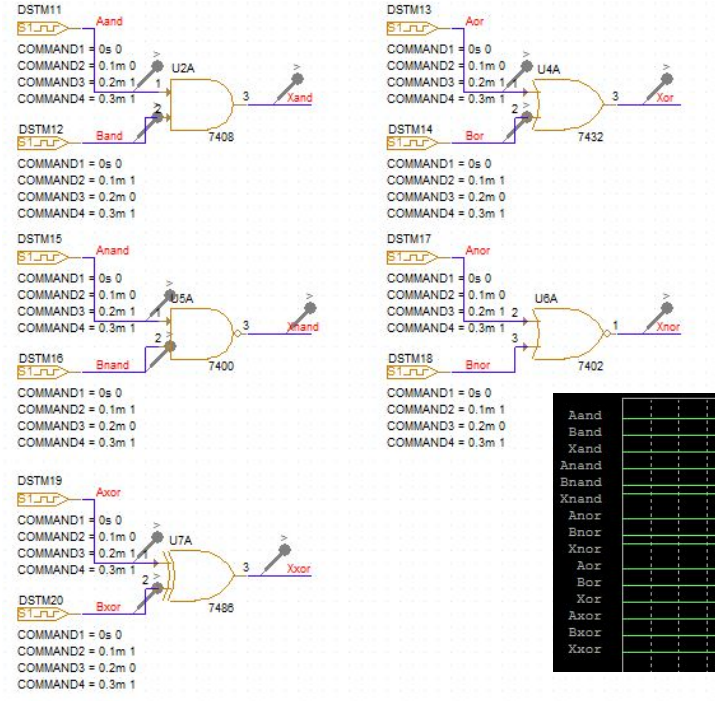
AC SWEEP

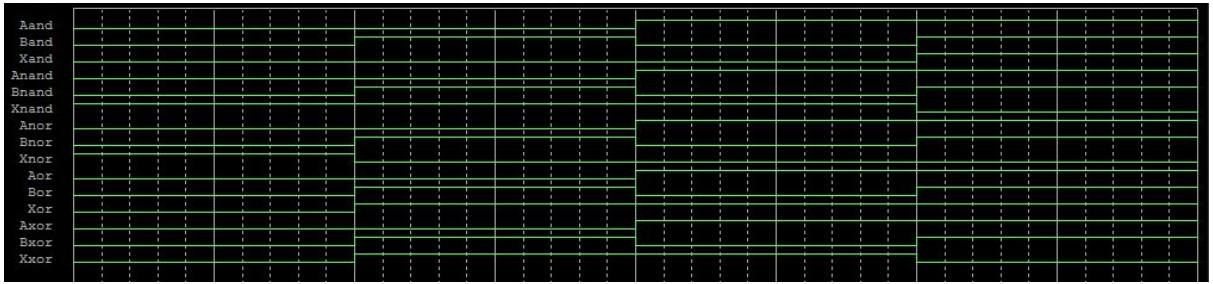




Simulation

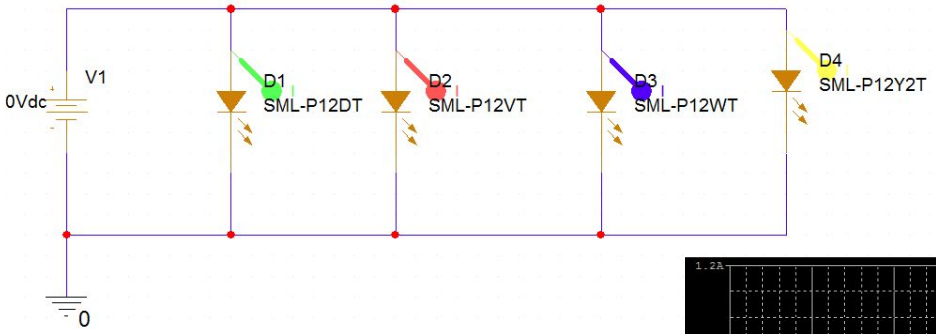
Digital

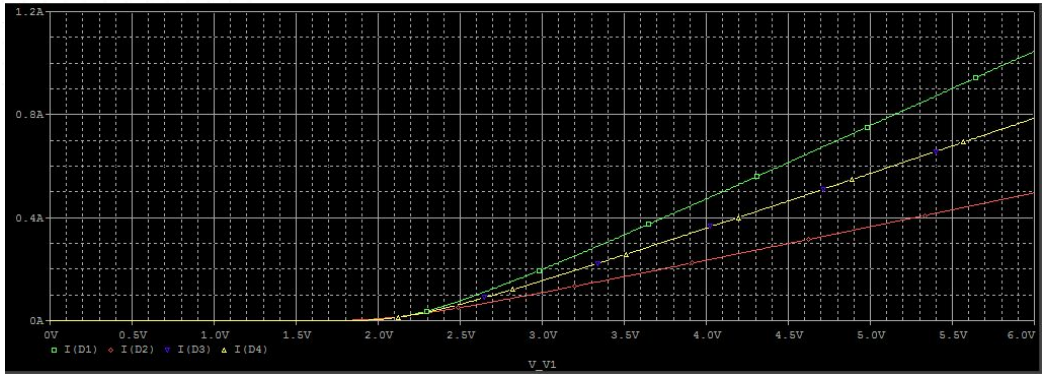




Simulation

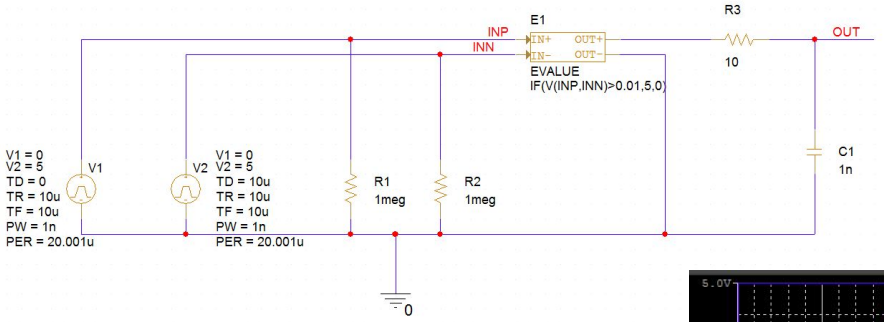
module import

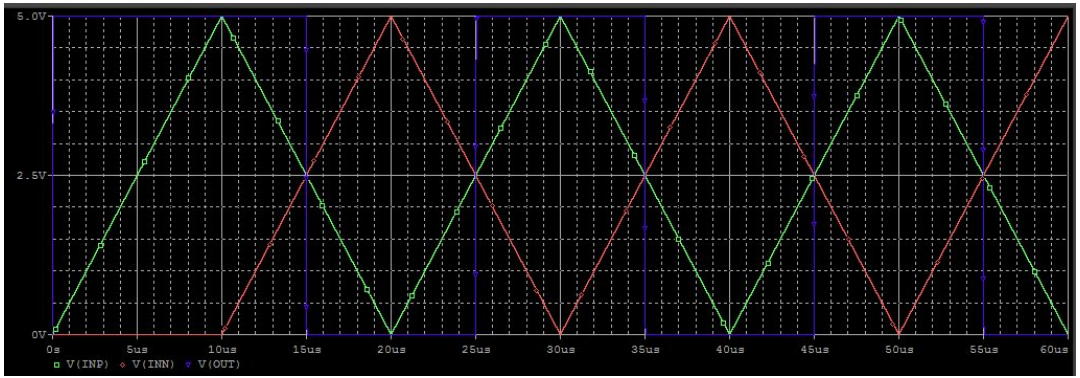




Simulation

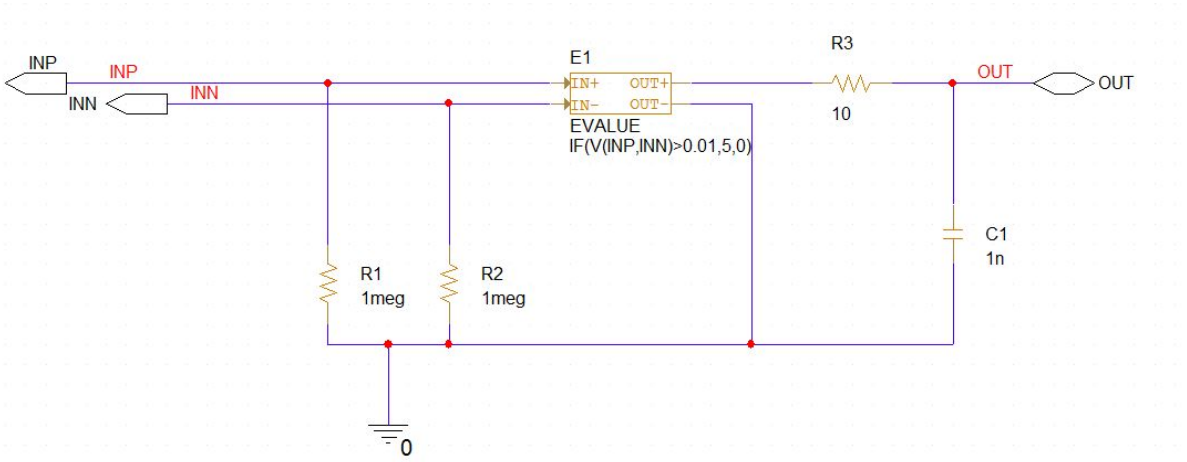
module 만들기
1. module simulation





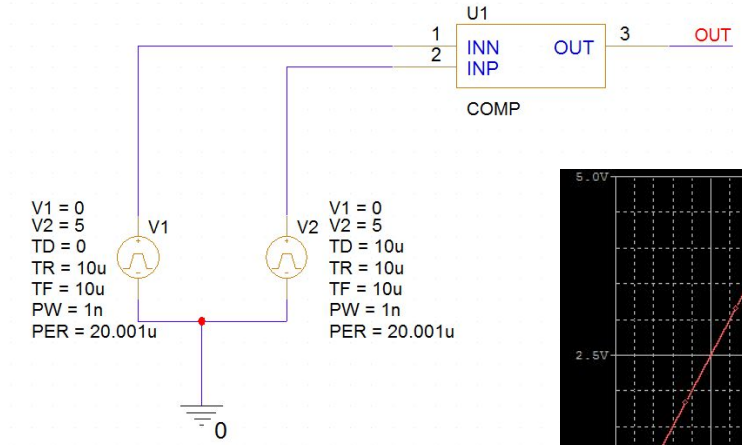
Simulation

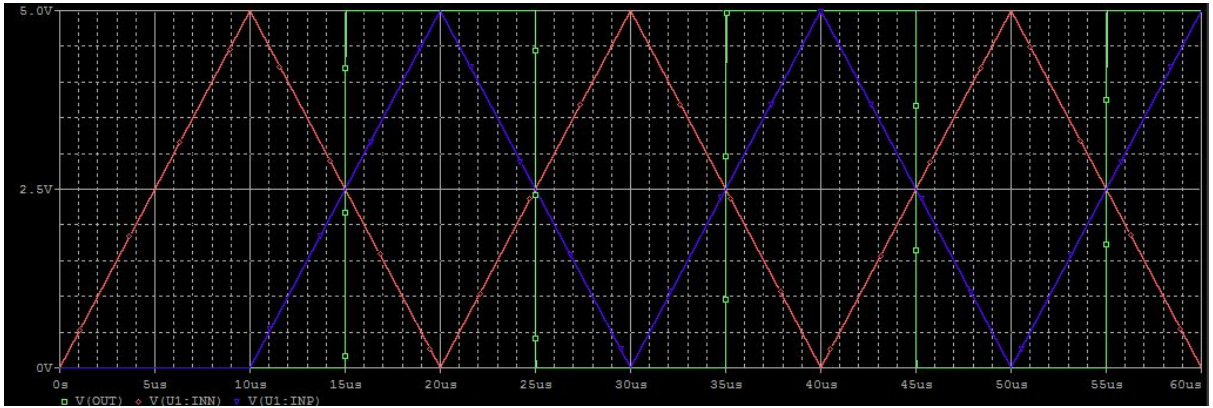
module 만들기
2. module port 설정



Simulation

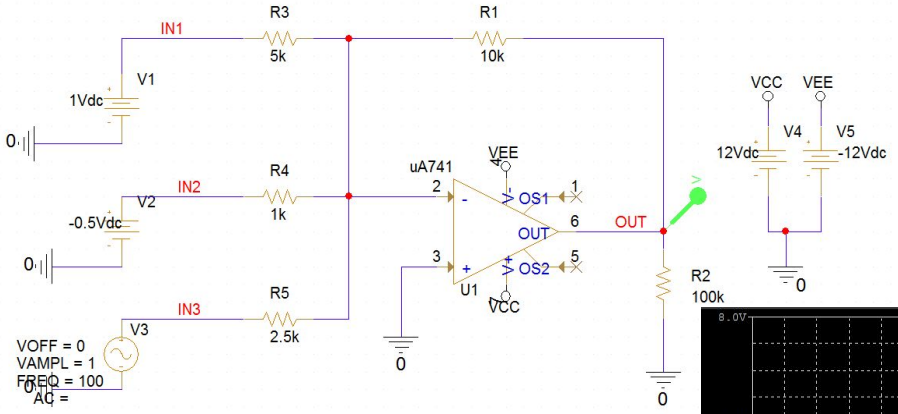
module 만들기
3. 만든 module simulation

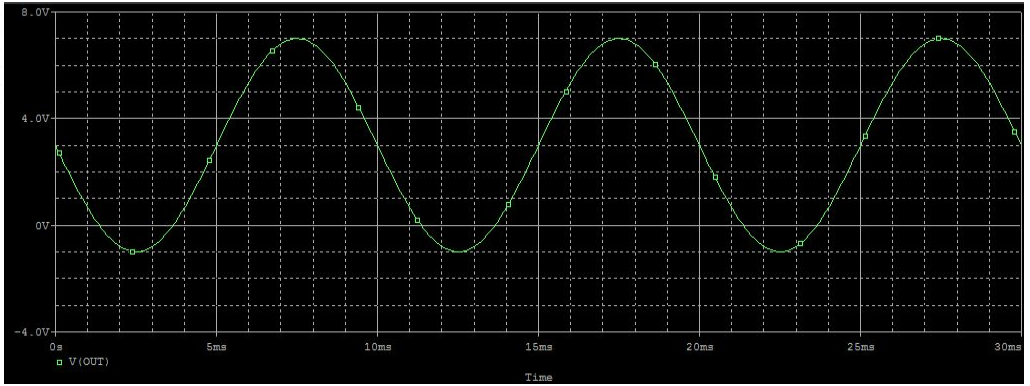




Simulation

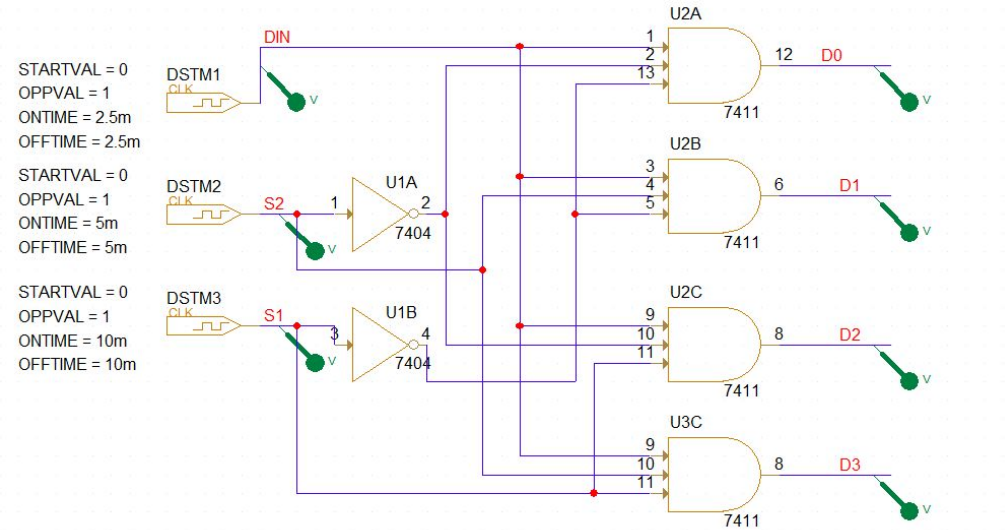
1번 과제

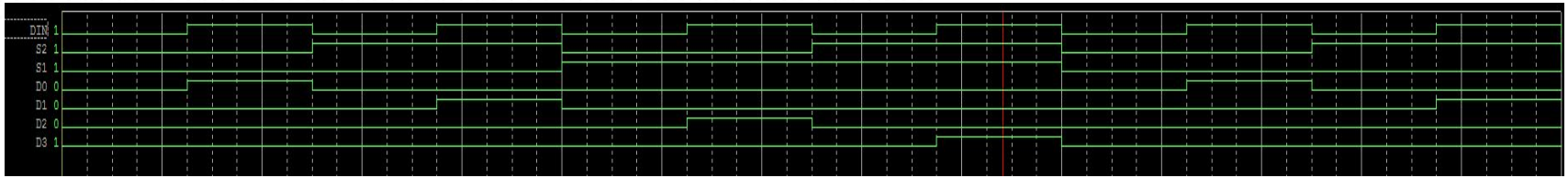




Simulation

2번 과제





Simulation

3번 과제