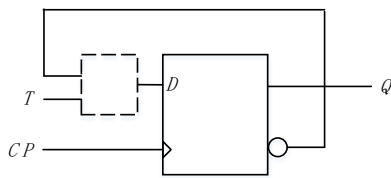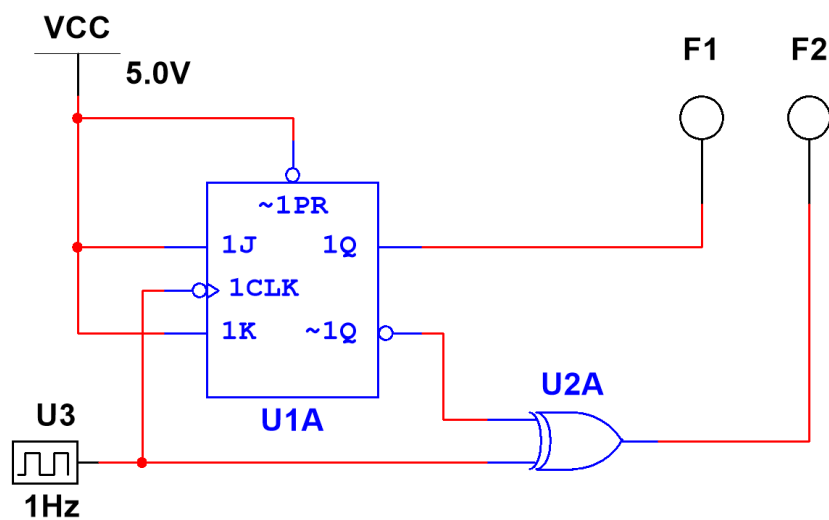**Chapter Five: Flip-Flop**

1. **True or False.** To convert a D flip-flop to a T flip-flop, the gate that is connected with the

D should be an （ AND gate ） .

2. Draw the waveforms.

Analyze the synchronous sequential logic circuit shown in the following figure which

contains a JK flip-flop and one XOR gate.

Suppose the initial state of F1 is 1, please sketch the output waveforms.

CLK

F1

F2