**Chapter Three: Digital Circuit**

1. Given VOHmin = 3.7V, VOLmax = 0.2V, VILmax = 0.9V, VIHmin = 2.0V, then High-state DC noise margin is ( 1.7 )V.

2. The three-state buffer’s output is logic 1, Hi-z ( or Open or floating ), and logic 0.

3. Which of the following statements is not correct? （ D ）

(A) The larger the DC noise margin of a logic device, the stronger its immunity to noise.

(B) The unused CMOS inputs are generally not allowed to be unconnected.

(C) The three-state buffer’s output can be one of high-level, low-level, and high-impedance states.

(D) The output of any CMOS logic gate doesn’t allow direct connection with other outputs.(Wired logic)

4. **True or False:** The output of the circuit shown in Fig.1 is ( (A+B)C ).



Fig. 1

**Solution**: False. ((A+B)C)’