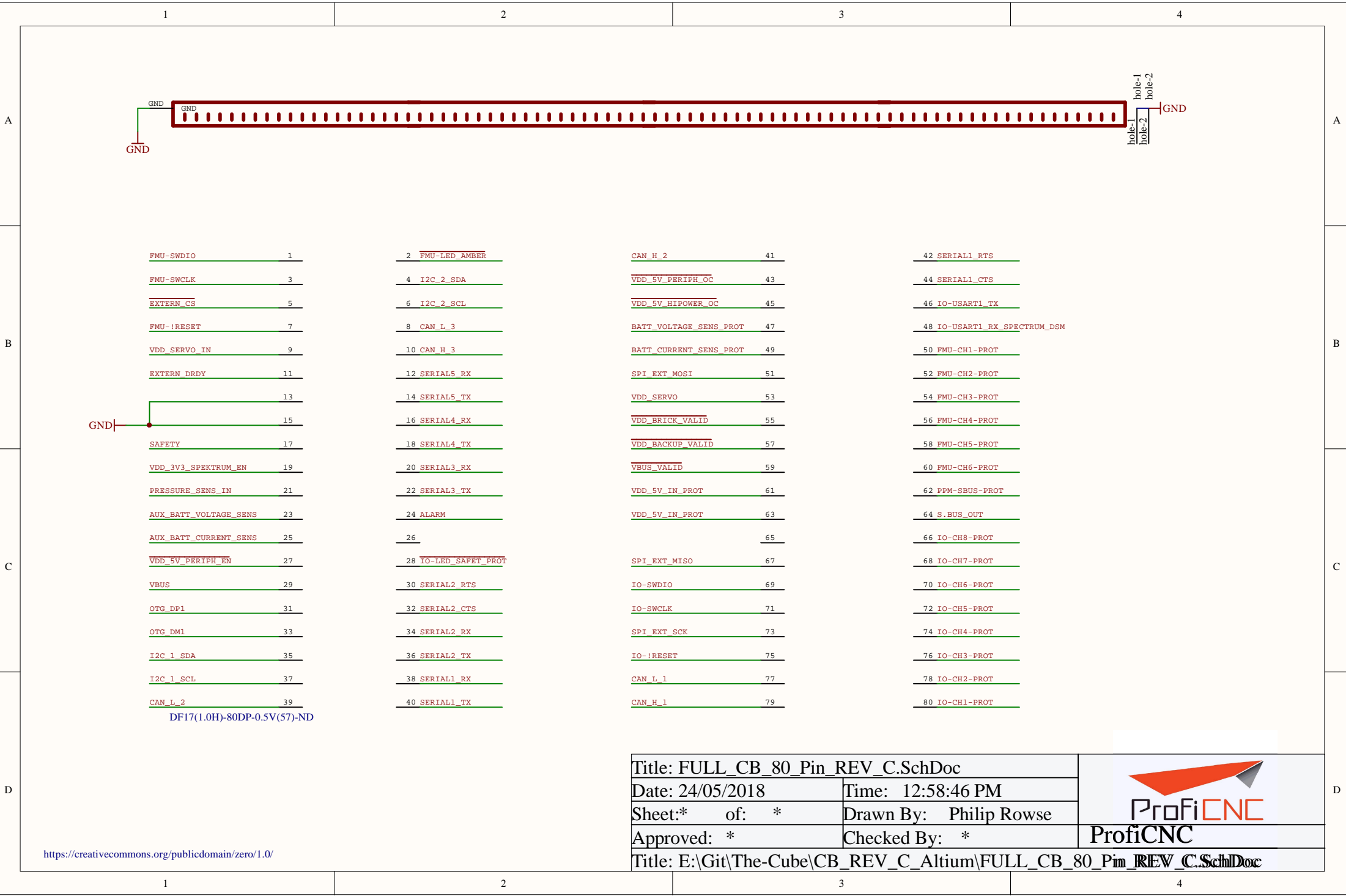

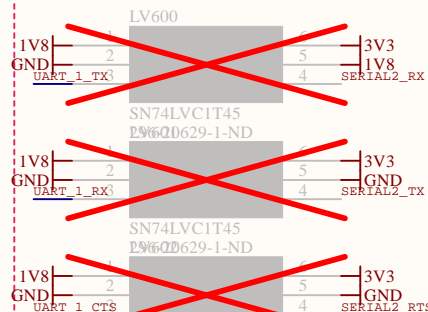


7-808

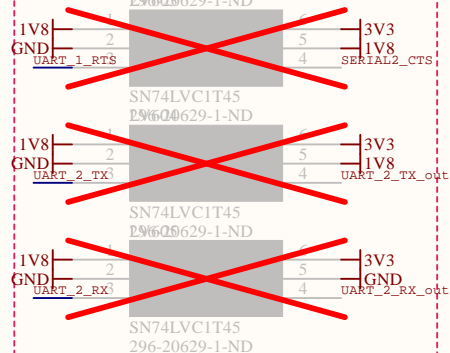


Title: FULL_CB_80_Pin_REV_C.SchDoc		 ProFiCNC
Date: 24/05/2018	Time: 12:58:46 PM	
Sheet:* of: *	Drawn By: Philip Rowse	
Approved: *	Checked By: *	
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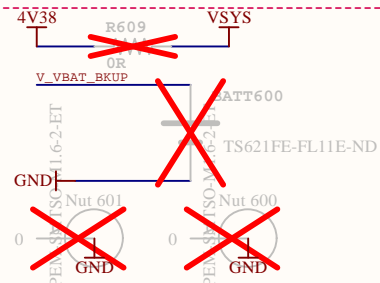
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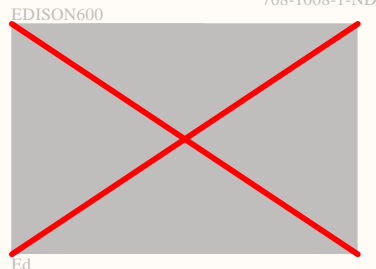
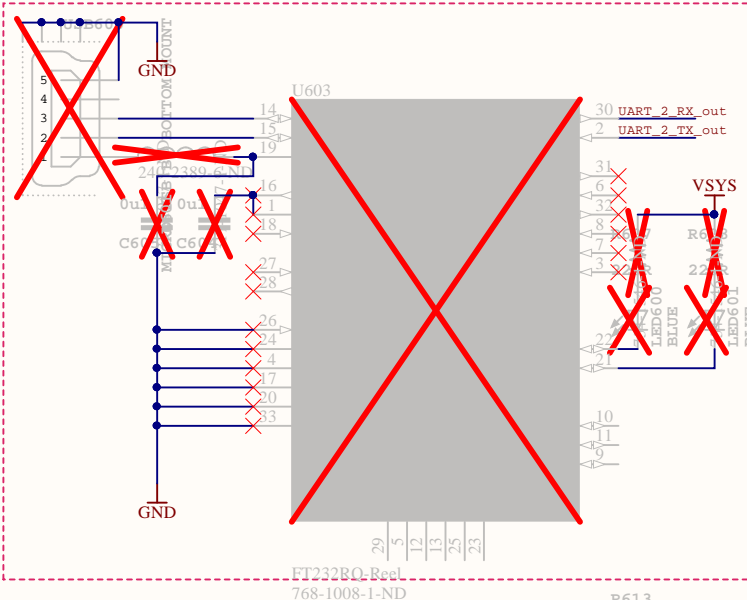
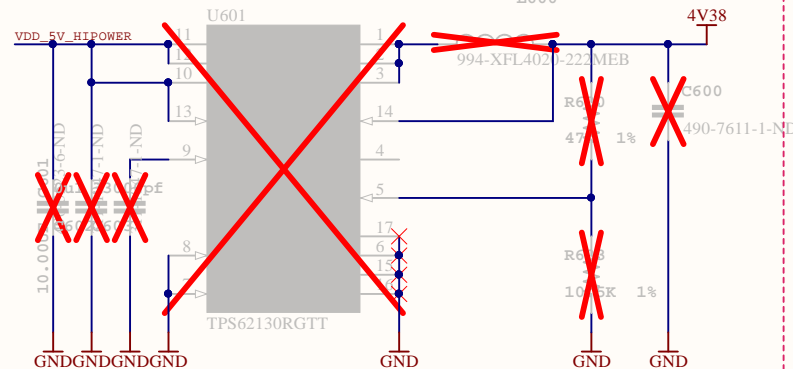


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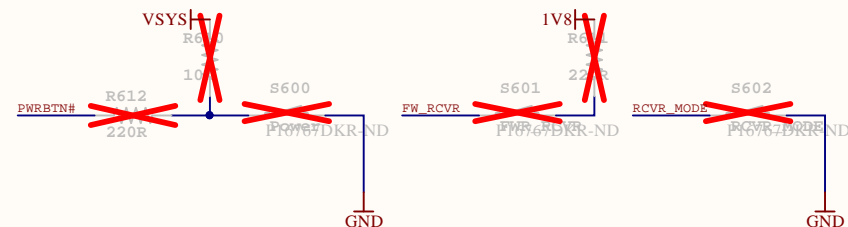
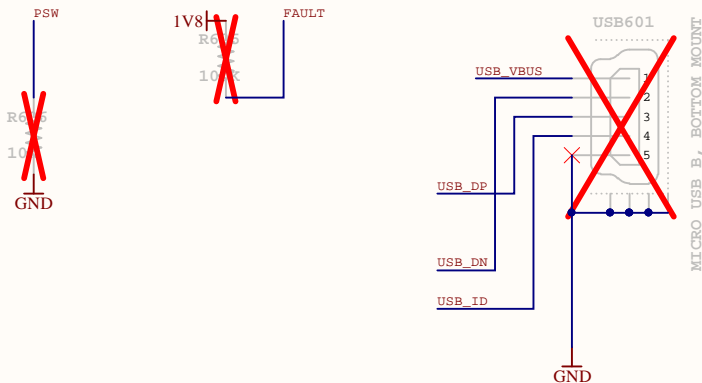
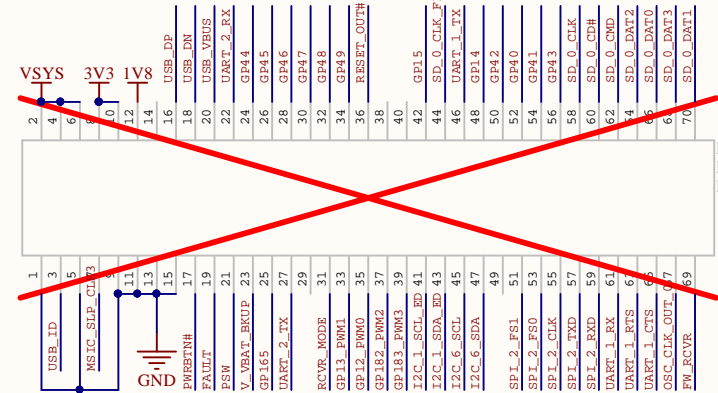
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RESET_OUT# R613 10K 1V8



Title: FULL_CB_INTEL_REV_C.SchDoc

Date: 24/05/2018

Time: 12:58:46 PM

Sheet: * of: *

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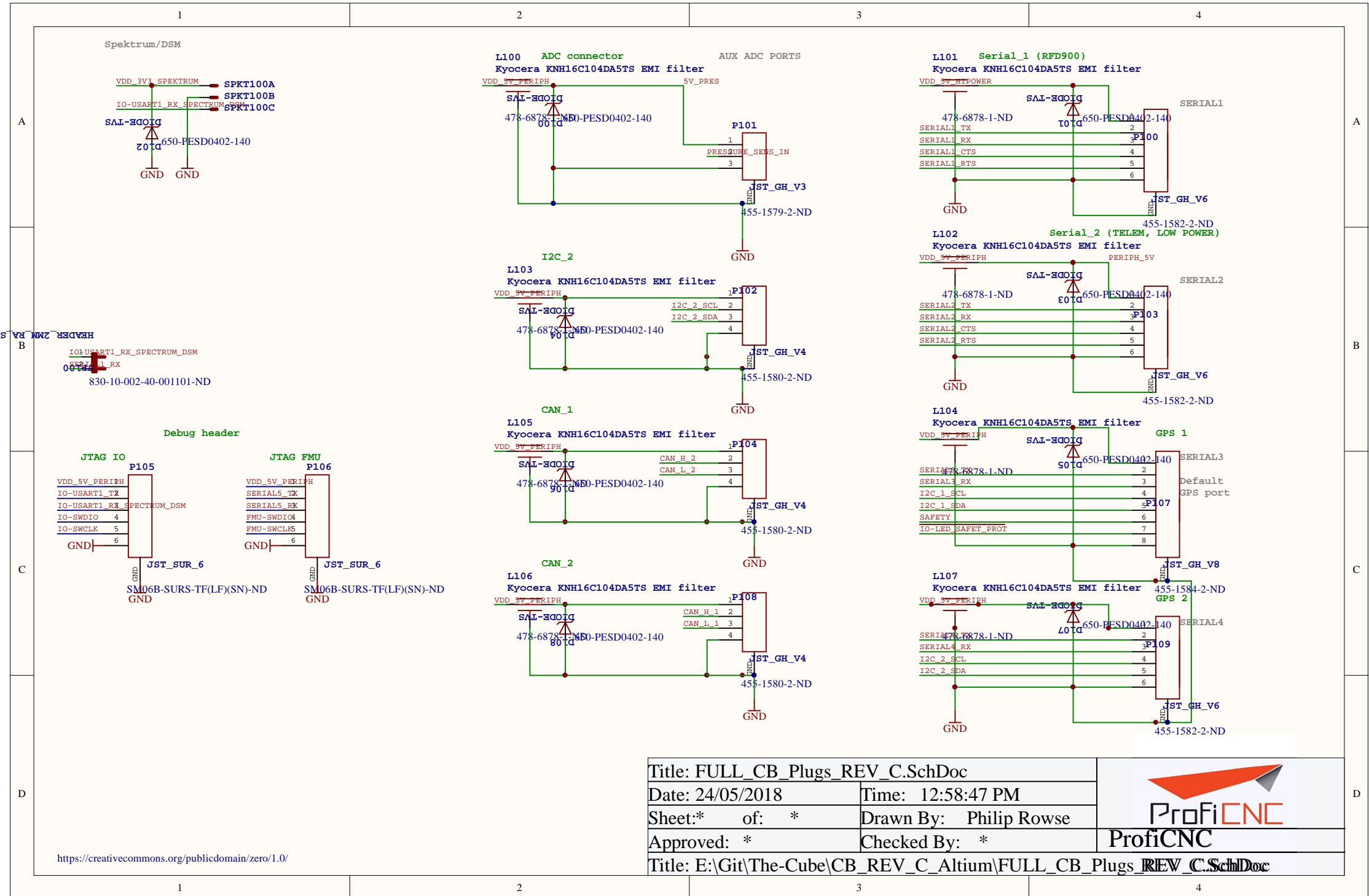
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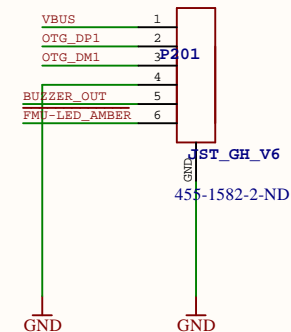
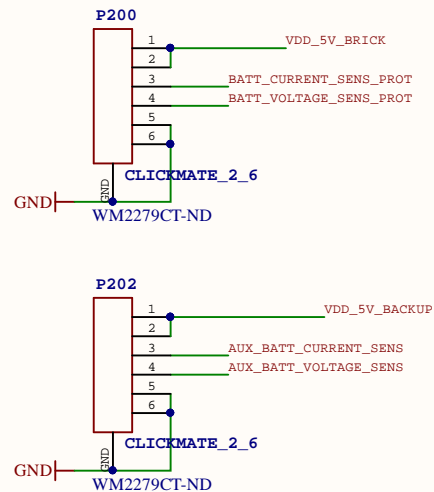
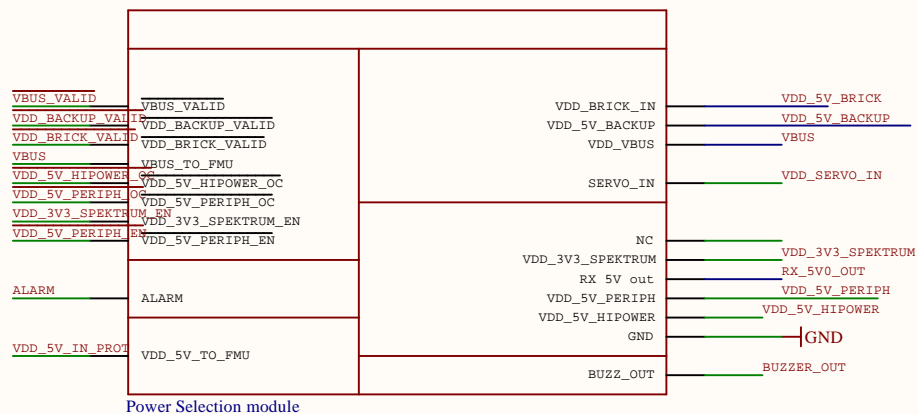
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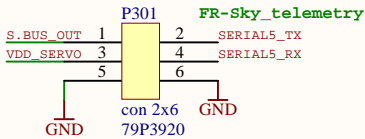
ProFiCNC

ProFiCNC

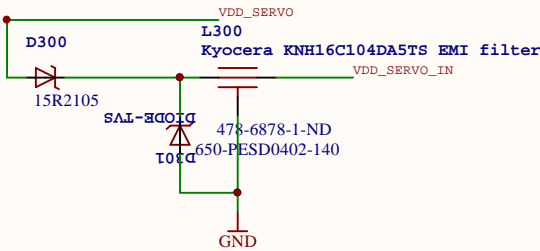




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Date: 24/05/2018	Time: 12:58:47 PM	
Sheet:* of: *	Drawn By: Philip Rowse	
Approved: *	Checked By: *	
Title: E:\Git\The-Cube\CB_REV_C_Altium\FULL_CB_Power_REV_C.SchDoc		



FMU-CH1-PROT	1	P304
FMU-CH2-PROT	2	FMU-CH6
FMU-CH3-PROT	3	FMU-CH5
FMU-CH4-PROT	4	FMU-CH4
FMU-CH5-PROT	5	FMU-CH3
FMU-CH6-PROT	6	FMU-CH2
IO-CH1-PROT	7	FMU-CH1
IO-CH2-PROT	8	IO-CH1
IO-CH3-PROT	9	IO-CH2
IO-CH4-PROT	10	IO-CH3
IO-CH5-PROT	11	IO-CH4
IO-CH6-PROT	12	IO-CH5
IO-CH7-PROT	13	IO-CH6
IO-CH8-PROT	14	IO-CH7
PPM-SBUS-PROT	15	IO-CH8
		PPM In
GND	GND	GND
VDD_SERVO	SERVO VCC	SERVO VCC
RX_5V0_OUT	RX 5V	RX 5V
		PH_2 Servo header



1

2

3

4

A

A

B

B

C

C

D


D

~~S1TX~~
~~SERIAL1_TX~~
~~PAD.04~~
~~SERIAL1_RX~~
~~PAD.04~~
~~S2TX~~
~~SERIAL2_TX~~
~~PAD.04~~
~~S2RX~~
~~SERIAL2_RX~~
~~PAD.04~~
~~S3TX~~
~~SERIAL3_TX~~
~~PAD.04~~
~~S3RX~~
~~SERIAL3_RX~~
~~PAD.04~~
~~S4TX~~
~~SERIAL4_TX~~
~~PAD.04~~
~~S4RX~~
~~SERIAL4_RX~~
~~PAD.04~~
~~S5_TX~~
~~SERIAL5_TX~~
~~PAD.04~~
~~S5_RX~~
~~SERIAL5_RX~~
~~PAD.04~~

~~ADC_PR~~
~~PRESSURE_SENS_IN~~
~~PAD.04~~

~~D_RDY1~~
~~EXTERNAL_DRDY~~
~~SPI_CS0CK1~~
~~SPI_EXT_CSCK~~
~~PAD.04~~
~~SPI_MISO1~~
~~SPI_EXT_MISO~~
~~PAD.04~~
~~SPI_MOSI1~~
~~SPI_EXT_MOSI~~
~~PAD.04~~
~~SPI_CS1~~
~~EXTERNAL_CS~~
~~PAD.04~~

~~×~~
~~×~~ Fiducials for machine
vision alignment.

Title: FULL_CB_tp_REV_C.SchDoc		
Date: 24/05/2018	Time: 12:58:47 PM	
Sheet:* of: *	Drawn By: Philip Rowse	
Approved: *	Checked By: *	
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1

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