Model Advisor Report – SWC_TCF.slx

Simulink version: 10.1

System: SWC_TCF

Treat as Referenced Model: off

Model version: 1.565

Current run: 2021/12/21 13:10:56

Run Summary

Pass	Fail	Warning	Not Run	Total
✅ 231	❌ 0	⚠️ 133	🗒️ 697	1061

📁 By Task

📁 Simulink Code Inspector compatibility checks   ✅0 ❌0 ⚠️0 🗒️69

_____

🗒️ Check code generation settings

Not Run

_____

🗒️ Check data import and export settings

Not Run

_____

🗒️ Check diagnostic settings

Not Run

_____

🗒️ Check hardware implementation settings

Not Run

_____

🗒️ Check math and data types settings

Not Run

_____

🗒️ Check solver settings

Not Run

_____

▦ Check for unconnected objects in the model

Not Run

_____

▦ Check system target file setting

Not Run

_____

▦ Check function specification setting

Not Run

_____

▦ Check for usage of fixed-point instrumentation

Not Run

_____

▦ Check for unsupported blocks

Not Run

_____

▦ Check storage class for workspace variables

Not Run

_____

▦ Check GetSet storage class for workspace variables

Not Run

_____

▦ Check for sample times in the model

Not Run

_____

▦ Check usage of Sources blocks

Not Run

_____

▦ Check usage of Signal Routing blocks

Not Run

_____

▦ Check usage of Math Operations blocks

Not Run

_____

⊟ Check usage of Signal Attributes blocks
    Not Run

_____

⊟ Check usage of Logical and Bit Operations blocks
    Not Run

_____

⊟ Check usage of Lookup Tables blocks
    Not Run

_____

⊟ Check usage of User-Defined Function blocks
    Not Run

_____

⊟ Check usage of Ports and Subsystems blocks
    Not Run

_____

⊟ Check usage of Discontinuities blocks
    Not Run

_____

⊟ Check usage of Sinks blocks
    Not Run

_____

⊟ Check usage of Discrete blocks
    Not Run

_____

⊟ Check usage of root Outport blocks
    Not Run

_____

⊟ Check for unsupported Signal Conversion blocks automatically inserted at signals entering block input ports
    Not Run

_____

Check usage of buses
Not Run

_____

Check for usage of synthesized local data stores
Not Run

_____

Check usage of global data stores
Not Run

_____

Check global data stores' name shadow
Not Run

_____

Check for root Outport blocks being conditionally assigned
Not Run

_____

Check conditional input branch execution setting
Not Run

_____

Check usage of Stateflow blocks
Not Run

_____

Check for Stateflow machine data
Not Run

_____

Check for Stateflow machine events
Not Run

_____

Check usage of Stateflow charts
Not Run

_____

Check usage of Stateflow data
Not Run

_____

▤ Check usage of Stateflow events

Not Run

_____

▤ Check usage of Stateflow states

Not Run

_____

▤ Check usage of Stateflow junctions

Not Run

_____

▤ Check usage of Stateflow transitions

Not Run

_____

▤ Check usage of Stateflow graphical functions

Not Run

_____

▤ Check usage of Stateflow truth tables

Not Run

_____

▤ Check Loop unrolling threshold setting

Not Run

_____

▤ Check destinations of If and Switchcase blocks

Not Run

_____

▤ Check for root Outport blocks that have non-auto storage class

Not Run

_____

▤ Check for Terminator blocks connected to Model Reference block outports

Not Run

_____

▤ Check for unsupported propagation of initial condition values
Not Run

_____

▤ Check data type replacement names
Not Run

_____

▤ Check usage of MATLAB Function Blocks
Not Run

_____

▤ Check usage of Data in MATLAB Functions
Not Run

_____

▤ Check usage of Code in MATLAB Functions
Not Run

_____

▤ Check MATLAB Code Analyzer messages
Not Run

_____

▤ Check for multiple sample times in model used as a model reference target
Not Run

_____

▤ Check Treat each discrete rate as a separate task setting
Not Run

_____

▤ Check model for commented out blocks
Not Run

_____

▤ Check model for instrumented signals
Not Run

_____

▤ Check model for void_void subsystems that use the same function name
Not Run

_____

📋 Check n-D Lookup Table blocks for incompatible breakpoint data type

Not Run

_____

📋 Check model for reusable subsystems that use the same function interfaces

Not Run

_____

📋 Check for usage of shared synthesized local data stores

Not Run

_____

📋 Check the code generation folder structure for the model

Not Run

_____

📋 Check for unsupported Code Mapping settings

Not Run

_____

📋 Check model for compiled and graphical block sorted order

Not Run

_____

📋 Check usage of String blocks

Not Run

_____

📋 Check usage of shared utilities

Not Run

_____

📋 Check model arguments for storage classes

Not Run

_____

📋 Check usage of Stateflow MATLAB action language

Not Run

📁 Modeling Standards for DO-178C/DO-331 ✅0 ❌0 ⚠0 ▤114

▤ Display model version information
  Not Run

📁 High-Integrity Systems ✅0 ❌0 ⚠0 ▤89

📁 *Simulink* ✅*0* ❌*0* ⚠*0* ▤*28*

▤ Check usage of Abs blocks
  Not Run

▤ Check usage of Math Function blocks (rem and reciprocal functions)
  Not Run

▤ Check usage of Math Function blocks (log and log10 functions)
  Not Run

▤ Check usage of While Iterator blocks
  Not Run

▤ Check usage of For and While Iterator subsystems
  Not Run

▤ Check usage of For Iterator blocks
  Not Run

▤ Check usage of If blocks and If Action Subsystem blocks
  Not Run

▤ Check usage of Switch Case blocks and Switch Case Action Subsystem blocks
  Not Run

_____

⊞ Check usage of conditionally executed subsystems

Not Run

_____

⊞ Check usage of Merge blocks

Not Run

_____

⊞ Check Relational Operator blocks equating floating-point types

Not Run

_____

⊞ Check usage of Relational Operator blocks

Not Run

_____

⊞ Check usage of Logical Operator blocks

Not Run

_____

⊞ Check usage of bit operation blocks

Not Run

_____

⊞ Check for blocks not recommended for C/C++ production code deployment

Not Run

_____

⊞ Check for inconsistent vector indexing methods

Not Run

_____

⊞ Check data types for blocks with index signals

Not Run

_____

⊞ Check usage of variant blocks

Not Run

_____

▤ Check usage of lookup table blocks
     Not Run

_____

▤ Check usage of Signal Routing blocks
     Not Run

_____

▤ Check for root Inports with missing properties
     Not Run

_____

▤ Check for root Inports with missing range definitions
     Not Run

_____

▤ Check for root Outports with missing range definitions
     Not Run

_____

▤ Check usage of Assignment blocks
     Not Run

_____

▤ Check global variables in graphical functions
     Not Run

_____

▤ Check usage of Gain blocks
     Not Run

_____

▤ Check for length of user-defined object names
     Not Run

_____

▤ Check data type of loop control variables
     Not Run

📁 *Stateflow*   ✅0 ❌0 ⚠0 ▤14

_____

⊞ Check state machine type of Stateflow charts
    Not Run

_____

⊞ Check Stateflow charts for ordering of states and transitions
    Not Run

_____

⊞ Check usage of bitwise operations in Stateflow charts
    Not Run

_____

⊞ Check for Strong Data Typing with Simulink I/O
    Not Run

_____

⊞ Check Stateflow debugging options
    Not Run

_____

⊞ Check Stateflow charts for transition paths that cross parallel state boundaries
    Not Run

_____

⊞ Check for inappropriate use of transition paths
    Not Run

_____

⊞ Check Stateflow charts for strong data typing
    Not Run

_____

⊞ Check naming of ports in Stateflow charts
    Not Run

_____

⊞ Check scoping of Stateflow data objects
    Not Run

_____

⊞ Check Stateflow charts for uniquely defined data objects
    Not Run

_____

📋 Check usage of shift operations for Stateflow data

    Not Run

_____

📋 Check assignment operations in Stateflow charts

    Not Run

_____

📋 Check Stateflow charts for unary operators

    Not Run

📁 *MATLAB* ✅0 ❌0 ⚠️0 📋10

_____

📋 Check usage of standardized MATLAB function headers

    Not Run

_____

📋 Check for MATLAB Function interfaces with inherited properties

    Not Run

_____

📋 Check MATLAB Function metrics

    Not Run

_____

📋 Check MATLAB Code Analyzer messages

    Not Run

_____

📋 Check if/elseif/else patterns in MATLAB Function blocks

    Not Run

_____

📋 Check switch statements in MATLAB Function blocks

    Not Run

_____

📋 Check usage of relational operators in MATLAB Function blocks

    Not Run

_____

📋 Check usage of equality operators in MATLAB Function blocks

   Not Run

_____

📋 Check usage of logical operators and functions in MATLAB Function blocks

   Not Run

_____

📋 Check type and size of condition expressions

   Not Run

📁 *Configuration*   ✅0 ❌0 ⚠️0 📋32

_____

📋 Check safety-related diagnostic settings for data store memory

   Not Run

_____

📋 Check safety-related diagnostic settings for saving

   Not Run

_____

📋 Check safety-related model referencing settings

   Not Run

_____

📋 Check safety-related code generation settings for comments

   Not Run

_____

📋 Check safety-related code generation interface settings

   Not Run

_____

📋 Check safety-related solver settings for simulation time

   Not Run

_____

📋 Check safety-related solver settings for solver options

   Not Run

_____

📋 Check safety-related solver settings for tasking and sample-time

   Not Run

_____

📋 Check safety-related diagnostic settings for solvers

   Not Run

_____

📋 Check safety-related diagnostic settings for sample time

   Not Run

_____

📋 Check safety-related optimization settings for logic signals

   Not Run

_____

📋 Check safety-related block reduction optimization settings

   Not Run

_____

📋 Check safety-related code generation settings for code style

   Not Run

_____

📋 Check safety-related optimization settings for application lifespan

   Not Run

_____

📋 Check safety-related code generation identifier settings

   Not Run

_____

📋 Check safety-related optimization settings for loop unrolling threshold

   Not Run

_____

📋 Check safety-related optimization settings for data initialization

   Not Run

_____

⊞ Check safety-related optimization settings for data type conversions
    Not Run

_____

⊞ Check safety-related optimization settings for division arithmetic exceptions
    Not Run

_____

⊞ Check safety-related optimization settings for specified minimum and maximum values
    Not Run

_____

⊞ Check safety-related diagnostic settings for compatibility
    Not Run

_____

⊞ Check safety-related diagnostic settings for parameters
    Not Run

_____

⊞ Check safety-related diagnostic settings for Merge blocks
    Not Run

_____

⊞ Check safety-related diagnostic settings for model initialization
    Not Run

_____

⊞ Check safety-related diagnostic settings for data used for debugging
    Not Run

_____

⊞ Check safety-related diagnostic settings for signal connectivity
    Not Run

_____

⊞ Check safety-related diagnostic settings for bus connectivity
    Not Run

_____

⊞ Check safety-related diagnostic settings that apply to function-call connectivity
    Not Run

───

📄 Check safety-related diagnostic settings for type conversions
   Not Run

───

📄 Check safety-related diagnostic settings for model referencing
   Not Run

───

📄 Check safety-related diagnostic settings for Stateflow
   Not Run

───

📄 Check safety-related diagnostic settings for signal data
   Not Run

📁 *Naming* ✅0 ❌0 ⚠0 📋2

───

📄 Check model file name
   Not Run

───

📄 Check model object names
   Not Run

📁 *Requirements* ✅0 ❌0 ⚠0 📋1

───

📄 Check for model elements that do not link to requirements
   Not Run

📁 *Code* ✅0 ❌0 ⚠0 📋2

───

📄 Check for blocks not recommended for MISRA C:2012
   Not Run

───

▤ Check configuration parameters for MISRA C:2012

Not Run

___

📁 Simulink ✅0 ❌0 ⚠0 ▤9

_____

▤ *Check for Discrete-Time Integrator blocks with initial condition uncertainty*

Not Run

_____

▤ *Check root model Inport block specifications*

Not Run

_____

▤ *Identify unconnected lines, input ports, and output ports*

Not Run

_____

▤ *Check usage of tunable parameters in blocks*

Not Run

_____

▤ *Check for blocks that have constraints on tunable parameters*

Not Run

_____

▤ *Identify questionable subsystem settings*

Not Run

_____

▤ *Check bus signals treated as vectors*

Not Run

_____

▤ *Check for potentially delayed function-call block return values*

Not Run

_____

▤ *Check usage of Merge blocks*

Not Run

📁 Stateflow ✅0 ❌0 ⚠️0 🗒️3

_____

🗒️ *Check for Strong Data Typing with Simulink I/O*
   Not Run

_____

🗒️ *Check definition of Stateflow data*
   Not Run

_____

🗒️ *Check usage of exclusive and default states in state machines*
   Not Run

_____

📁 Library Links ✅0 ❌0 ⚠️0 🗒️3

_____

🗒️ *Identify disabled library links*
   Not Run

_____

🗒️ *Identify parameterized library links*
   Not Run

_____

🗒️ *Identify unresolved library links*
   Not Run

_____

📁 Model Referencing ✅0 ❌0 ⚠️0 🗒️2

_____

🗒️ *Check for model reference configuration mismatch*
   Not Run

_____

🗒️ *Check for parameter tunability information ignored for referenced models*
   Not Run

_____

📁 Requirements Consistency ✅0 ❌0 ⚠️0 🗒️4

_____

📄 Identify requirement links that specify invalid locations within documents
    Not Run

_____

📄 Identify requirement links with missing documents
    Not Run

_____

📄 Identify requirement links with path type inconsistent with preferences
    Not Run

_____

📄 Identify selection-based links having description fields that do not match their requirements document text
    Not Run

📁 Simulink Coder   ✅0 ❌0 ⚠️0 📄3
_____

📄 Check sample times and tasking mode
    Not Run

_____

📄 Check solver for code generation
    Not Run

_____

📄 Check the hardware implementation
    Not Run

📁 Modeling Standards for DO-254   ✅0 ❌0 ⚠️0 📄91
_____

📄 Display model version information
    Not Run

📁 High-Integrity Systems   ✅0 ❌0 ⚠️0 📄48

📁 *Simulink*   ✅*0* ❌*0* ⚠️*0* 📋*16*

_____

📋 Check usage of Abs blocks

Not Run

_____

📋 Check usage of conditionally executed subsystems

Not Run

_____

📋 Check Relational Operator blocks equating floating-point types

Not Run

_____

📋 Check usage of Relational Operator blocks

Not Run

_____

📋 Check usage of Logical Operator blocks

Not Run

_____

📋 Check usage of bit operation blocks

Not Run

_____

📋 Check for inconsistent vector indexing methods

Not Run

_____

📋 Check data types for blocks with index signals

Not Run

_____

📋 Check for root Inports with missing properties

Not Run

_____

📋 Check for root Inports with missing range definitions

Not Run

_____

📄 Check for root Outports with missing range definitions
  Not Run

_____

📄 Check usage of Assignment blocks
  Not Run

_____

📄 Check global variables in graphical functions
  Not Run

_____

📄 Check usage of Gain blocks
  Not Run

_____

📄 Check for length of user-defined object names
  Not Run

_____

📄 Check data type of loop control variables
  Not Run

📁 *Stateflow* ✅0 ❌0 ⚠0 📋11

_____

📄 Check Stateflow charts for ordering of states and transitions
  Not Run

_____

📄 Check usage of bitwise operations in Stateflow charts
  Not Run

_____

📄 Check for Strong Data Typing with Simulink I/O
  Not Run

_____

📄 Check Stateflow debugging options
  Not Run

_____

🔳 Check Stateflow charts for transition paths that cross parallel state boundaries
Not Run

_____

🔳 Check for inappropriate use of transition paths
Not Run

_____

🔳 Check naming of ports in Stateflow charts
Not Run

_____

🔳 Check scoping of Stateflow data objects
Not Run

_____

🔳 Check Stateflow charts for uniquely defined data objects
Not Run

_____

🔳 Check usage of shift operations for Stateflow data
Not Run

_____

🔳 Check Stateflow charts for unary operators
Not Run

📁 *MATLAB* ✅0 ❌0 ⚠️0 🔳7

_____

🔳 Check usage of standardized MATLAB function headers
Not Run

_____

🔳 Check MATLAB Code Analyzer messages
Not Run

_____

🔳 Check if/elseif/else patterns in MATLAB Function blocks
Not Run

_____

⊞ Check switch statements in MATLAB Function blocks

Not Run

_____

⊞ Check usage of relational operators in MATLAB Function blocks

Not Run

_____

⊞ Check usage of equality operators in MATLAB Function blocks

Not Run

_____

⊞ Check usage of logical operators and functions in MATLAB Function blocks

Not Run

📁 *Configuration* ✅0 ❌0 ⚠️0 ⊞11

_____

⊞ Check safety-related diagnostic settings for saving

Not Run

_____

⊞ Check safety-related model referencing settings

Not Run

_____

⊞ Check safety-related block reduction optimization settings

Not Run

_____

⊞ Check safety-related diagnostic settings for parameters

Not Run

_____

⊞ Check safety-related diagnostic settings for model initialization

Not Run

_____

⊞ Check safety-related diagnostic settings for signal connectivity

Not Run

_____

23

▦ Check safety-related diagnostic settings for bus connectivity
   Not Run

_____

▦ Check safety-related diagnostic settings for type conversions
   Not Run

_____

▦ Check safety-related diagnostic settings for model referencing
   Not Run

_____

▦ Check safety-related diagnostic settings for Stateflow
   Not Run

_____

▦ Check safety-related diagnostic settings for signal data
   Not Run

📁 *Naming* ✅0 ❌0 ⚠0 ▦2

_____

▦ Check model file name
   Not Run

_____

▦ Check model object names
   Not Run

📁 *Requirements* ✅0 ❌0 ⚠0 ▦1

_____

▦ Check for model elements that do not link to requirements
   Not Run

📁 Library Links ✅0 ❌0 ⚠0 ▦3

_____

▦ *Identify disabled library links*
   Not Run

📄 *Identify parameterized library links*
Not Run

📄 *Identify unresolved library links*
Not Run

📁 Model Referencing ✅0 ❌0 ⚠️0 📋1

📄 *Check for model reference configuration mismatch*
Not Run

📁 Requirements Consistency ✅0 ❌0 ⚠️0 📋4

📄 *Identify requirement links that specify invalid locations within documents*
Not Run

📄 *Identify requirement links with missing documents*
Not Run

📄 *Identify requirement links with path type inconsistent with preferences*
Not Run

📄 *Identify selection-based links having description fields that do not match their requirements document text*
Not Run

📁 HDL Coder ✅0 ❌0 ⚠️0 📋34

📁 *Checks for blocks and block settings* ✅0 ❌0 ⚠️0 📋7

⊞ Check for infinite and continuous sample time sources
    Not Run

_____

⊞ Check for unsupported blocks
    Not Run

_____

⊞ Check for MATLAB Function block settings
    Not Run

_____

⊞ Check for Stateflow chart settings
    Not Run

_____

⊞ Check for obsolete Unit Delay Enabled/Resettable blocks
    Not Run

_____

⊞ Check for unsupported storage class for signal objects
    Not Run

_____

⊞ Check for large matrix operations
    Not Run

📁 *Industry standard checks*   ✅0 ❌0 ⚠0 ⊞11

_____

⊞ Check VHDL file extension
    Not Run

_____

⊞ Check naming conventions
    Not Run

_____

⊞ Check top-level subsystem/port names
    Not Run

_____

📑 Check module/entity names
    Not Run

_____

📑 Check package file names
    Not Run

_____

📑 Check signal and port names
    Not Run

_____

📑 Check generics
    Not Run

_____

📑 Check clock, reset, and enable signals
    Not Run

_____

📑 Check architecture name
    Not Run

_____

📑 Check entity and architecture
    Not Run

_____

📑 Check clock settings
    Not Run

📁 *Model configuration checks*   ✅0 ❌0 ⚠0 📑6

_____

📑 Check for safe model parameters
    Not Run

_____

📑 Check for global reset setting for Xilinx and Altera devices
    Not Run

_____

▤ Check inline configurations setting
    Not Run

_____

▤ Check for visualization settings
    Not Run

_____

▤ Check delay balancing setting
    Not Run

_____

▤ Check algebraic loops
    Not Run

📁 *Native Floating Point checks* ✅0 ❌0 ⚠0 ▤8

_____

▤ Check for blocks with nonzero output latency
    Not Run

_____

▤ Check blocks with nonzero ulp error
    Not Run

_____

▤ Check for single datatypes in the model
    Not Run

_____

▤ Check for double datatypes in the model with Native Floating Point
    Not Run

_____

▤ Check for Data Type Conversion blocks with incompatible settings
    Not Run

_____

▤ Check for HDL Reciprocal block usage
    Not Run

_____

🗒 Check for Relational Operator block usage

    Not Run

_____

🗒 Check for unsupported blocks with Native Floating Point

    Not Run

📁 *Checks for ports and subsystems* ✅0 ❌0 ⚠0 🗒2

_____

🗒 Check for invalid top level subsystem

    Not Run

_____

🗒 Check initial conditions of enabled and triggered subsystems

    Not Run

📁 Modeling Standards for IEC 61508 ✅62 ❌0 ⚠43 🗒1

_____

✅ Display configuration management data

    Display model configuration and checksum information

Model configuration and checksum information

Attribute	Value
Model Version	1.565
Author	dongliyuan
Date	Mon Dec 20 15:40:59 2021
Model Checksum	2556881298 2768319665 381822641 1525871067

_____

✅ Display model metrics and complexity report

    Display number of elements and name, level, and depth of subsystems for the model or subsystem

Model metrics information

Display number of elements for Simulink blocks and Stateflow constructs

Summary

Element Type	Count
Inport	90
Outport	45
SubSystem	30
Stateflow	2

Simulink

Block Type	Count
Inport	90
Constant	52
Outport	45
SubSystem	30
Product	27
Sum	22
Switch	14
RelationalOperator	13
From	9
SignalConversion	8
Logic	7

Lookup_n-D	6
Delay	6
EnablePort	5
Goto	5
Abs	4
ActionPort	4
Ground	4
UnitDelay	3
Terminator	3
LookupNDDirect	2
TriggerPort	1
If	1
Merge	1
Sqrt	1
S-Function	1
EventListener	1

∧ Less

Stateflow

Stateflow construct	Count
Stateflow Transitions	44
Stateflow Junctions	38
Stateflow Data	14
Stateflow Charts	2

complexity information

Display name, level, and depth of subsystems

Maximum Subsystem Depth: 6

Subsystem Depth

Subsystem Name	Level	Depth
SWC_TCF/SWC_TCF_1ms_sys	1	5
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc	2	4
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc	3	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc	3	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc	3	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg	3	3
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode	4	2
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem	5	1

SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem1" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode /If Action Subsystem1	5	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem2" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode /If Action Subsystem2	5	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem3" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode /If Action Subsystem3	5	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir	4	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir	4	1
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	2	2
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero	3	1
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem	3	1
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc	2	3
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc	3	1
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc	3	1
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc	3	2
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Saturation Dynamic" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Saturation Dynamic	4	1
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc	3	1

SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc	2	3
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	3	2
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero	4	1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem	4	1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend	3	1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube	3	1
SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable	2	1
SWC_TCF/SWC_TCF_Init	1	1

∧ Less

_____

✅ Check for unconnected objects
Identify unconnected lines, input ports, and output ports in the model

Passed
There are no unconnected lines, input ports, and output ports in this model.

📁 High-Integrity Systems ✅59 ❌0 ⚠️29 📋1

📁 *Simulink* ✅25 ❌0 ⚠️3 📋0

_____

✅ Check usage of Abs blocks
Identify Abs blocks that have unreachable code or produce overflows

Passed
No Abs blocks found causing unreachable code or produce overflows.

✅ Check usage of Math Function blocks (rem and reciprocal functions)

Identify Math Function blocks using rem and reciprocal functions that cause non-finite results

Passed

All Math Function blocks in the model use operators appropriately.

_____

✅ Check usage of Math Function blocks (log and log10 functions)

Identify Math Function blocks using log and log10 functions that cause non-finite results

Passed

All Math Function blocks in the model use operators appropriately.

_____

✅ Check usage of While Iterator blocks

Identify While Iterator blocks that do not have a positive value for the maximum number of iterations

Passed

No While Iterator blocks found that might cause infinite loops

_____

✅ Check usage of For and While Iterator subsystems

Identify sample time-dependent blocks in While and For Iterator subsystems.

Passed

No sample time-dependent blocks in For or While Iterator subsystems.

_____

✅ Check usage of For Iterator blocks

Identify For Iterator blocks that cause variable loops

Passed

No For Iterator blocks found that cause variable loops.

_____

✅ Check usage of If blocks and If Action Subsystem blocks

Identify If and If Action Subsystem blocks without else conditions

Passed

No If blocks with questionable configurations or connections were found.

_____

✅ Check usage of Switch Case blocks and Switch Case Action Subsystem blocks

Identify inappropriately used Switch Case blocks and Switch Case Action Subsystem blocks

Passed

No Switch Case blocks with questionable configurations or connections were found.

_____

✅ Check usage of conditionally executed subsystems

Identify inappropriate blocks in conditionally executed subsystems.

Passed

No blocks with improper sample times or asynchronously executed sample-time dependent blocks were found.

_____

✅ Check usage of Merge blocks

Identify Merge blocks constructs which can lead to ambiguous behavior.

Passed

No merge blocks found which can lead to ambiguous behavior.

_____

✅ Check Relational Operator blocks equating floating-point types

Identify Relational Operator blocks that equate floating-point types

Passed

No Relational Operator blocks found that equate floating-point types.

_____

✅ Check usage of Relational Operator blocks

Identify Relational Operator blocks that operate on different data types or have a non-boolean output

Passed

No Relational Operator blocks found that operate on different data types or have a non-boolean output.

_____

✅ Check usage of Logical Operator blocks

Identify Logical Operator blocks that operate on non-boolean data types

Passed

No Logical Operator blocks found that operate on non-boolean data types.

36

_____

✅ Check usage of bit operation blocks

Identify bit operation blocks with signed data types as inputs

Passed

No bit operation blocks found with signed data types as inputs.

_____

✅ Check for blocks not recommended for C/C++ production code deployment

Identify blocks not supported by code generation or not recommended for C/C++ production code deployment.

Passed

No blocks found which are not recommended for C/C++ production code deployment.

_____

✅ Check for inconsistent vector indexing methods

Identify inconsistent usage of vector indexing methods across the model or subsystem

Passed

No blocks found using inconsistent indexing modes.

_____

⚠️ Check data types for blocks with index signals

Identify blocks with index signals that have data types other than integers or enums.

Warning

The following blocks have inappropriate data types for index signals or variables:

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/LookupCosTable

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/LookupSinTable

Recommended Action

Change the data type of index signals or variables to an integer or enum data type.

_____

✅ Check usage of variant blocks

Check variant block settings that might result in code that doesn't trace back to requirements.

Passed

There are no variant blocks that have "Generate preprocessor conditionals" active.

_____

✅ Check usage of lookup table blocks

Check for Lookup Table blocks, Prelookup blocks and Interpolation blocks that do not generate out-of-range checking code.

Passed

No lookup table blocks found to not generate out-of-range checking code.

_____

✅ Check usage of Signal Routing blocks

Identify usage of Signal Routing blocks in Simulink that might impact safety

Passed

No Switch blocks that might generate code with inequality operations (~=) in expressions where at least one side of the expression is a floating-point variable or constant were found.

_____

⚠️ Check for root Inports with missing properties

Identify Inport blocks in the top-level of the model with missing or inherited sample times, data types, or port dimensions. Inport block properties are specified with block parameters or Simulink signal data objects that explicitly resolve to the connected signal lines.

Warning

The following Inport blocks have undefined or inherited sample times, data types or port dimensions:

- SWC_TCF/HSPF_StrrTempFlt

- SWC_TCF/SWD_AgRtr

- SWC_TCF/SWD_Spd

Recommended Action

Specify a data type for the listed Inport blocks or Simulink signal objects.

_____ Identify
Inport blocks in the top-level of the model with missing or inherited sample times, data types, or port
dimensions. Inport block properties are specified with block parameters or Simulink signal data
objects that explicitly resolve to the connected signal lines.

Warning

The following Inport blocks have undefined or inherited sample times, data types or port dimensions:

- SWC_TCF/HSPF_StrrTempFlt

- SWC_TCF/SWD_AgRtr

- SWC_TCF/SWD_Spd

Recommended Action

Specify port dimension for the listed Inport blocks or Simulink signal objects.

_____

⚠ Check for root Inports with missing range definitions
Identify root-level Inport blocks with missing or erroneous minimum or maximum values. Inport
block minimum and maximum values are specified with block parameters or Simulink signal objects
that explicitly resolve to the connected signal lines.

Warning

The following Inport blocks and Simulink signal objects have missing or erroneous range definitions:

- SWC_TCF/HSPF_StrrTempFlt

- SWC_TCF/SWD_AgRtr

- SWC_TCF/SWD_Spd

Recommended Action

39

Model contains Inport blocks or Simulink signal objects with inherited data type. For the Inport blocks or Simulink signal objects, select a build-in, enum, Simulink.Bus, Simulink.NumericType or a Simulink.AliasType data type.

_____ Identify root-level Inport blocks with missing or erroneous minimum or maximum values. Inport block minimum and maximum values are specified with block parameters or Simulink signal objects that explicitly resolve to the connected signal lines.

Warning

The following Inport blocks and Simulink signal objects have missing or erroneous range definitions:

- SWC_TCF/L2Com_HiPrecVolt

- SWC_TCF/L2Com_ModeReq

- SWC_TCF/L2Sampling_DycU_Mon

- SWC_TCF/L2Sampling_DycV_Mon

- SWC_TCF/L2Sampling_DycW_Mon

- SWC_TCF/TRSP_iU

- SWC_TCF/TRSP_iV

- SWC_TCF/TRSP_iW

Recommended Action

Model contains Inports with numeric data types that have missing range parameters (minimum and/or maximum). For the listed Inport blocks and Simulink signal objects, specify scalar minimum and maximum parameters.

_____

✅ Check for root Outports with missing range definitions

Identify root-level Outport blocks with missing or erroneous minimum or maximum values. Outport block minimum and maximum values are specified with block parameters or Simulink signal objects that explicitly resolve to the connected signal lines.

Passed

There are no missing or erroneous Outport range properties at the model root level.
Note: Root Outports with inherited data types are not analyzed by this check.

_____

✅ Check usage of Assignment blocks

Identify Assignment blocks whose array fields are not initialized.

Passed

All Assignment blocks are configured with block parameter "Action if any output element is not assigned" set to Warning or Error.

_____

✅ Check global variables in graphical functions

Identify expressions that both read and write to the same global data.

Passed

No expressions found that both read and write to the same global data.

_____

✅ Check usage of Gain blocks

Identify Gain blocks with value which resolves to 1

Passed

No Gain blocks found with value which resolves to 1.

_____

✅ Check for length of user-defined object names

Identify user-defined object names with length greater than threshold

Passed

No Subsystem blocks found with function name length greater than Maximum identifier length. There are no data objects with names having length greater than Maximum identifier length.

_____

✅ Check data type of loop control variables

Identify loop control variables using non-integer data types.

Passed

No For Iterator blocks or MATLAB Function blocks found using non-integer data type for loop control counter variable.

📁 *Stateflow* ✅11 ❌0 ⚠️3 🗒️0

_____

⚠️ Check state machine type of Stateflow charts

    Identify Stateflow Charts whose State Machine Type differs from the type set in the Model Advisor Configuration Editor.

Warning

The following Stateflow Charts do not set the recommended state machine type:

Block	Parameter	Current Value	Recommended Values
	StateMachineType	Mealy	Classic

Recommended Action

Select the appropriate state machine type for each Stateflow Chart.

_____

✅ Check Stateflow charts for ordering of states and transitions

    Identify Stateflow charts that do not use explicit ordering of parallel states and transitions.

Passed

No Stateflow Charts found that deviate from recommended state/transition execution order settings.

_____

✅ Check usage of bitwise operations in Stateflow charts

    Identify usage of signed data type operands to bitwise operations in Stateflow charts.

Passed

No Stateflow objects found that use signed data type operands with bitwise operations.

_____

✅ Check for Strong Data Typing with Simulink I/O

Verify configuration settings for strong data typing on the boundaries between Simulink and Stateflow

Passed

No Stateflow charts found that set 'Use Strong Data Typing with Simulink I/O' to off.

_____

⚠️ Check Stateflow debugging options

Identify whether Stateflow debugging options are set appropriately

Warning

The following Stateflow debugging options are not set appropriately:

Parameter	Current Value	Recommended Values
Wrap on overflow (IntegerOverflowMsg)	warning	error
Simulation range checking (SignalRangeChecking)	none	error

Recommended Action

Change the Stateflow debugging options to the recommended value.

_____

✅ Check Stateflow charts for transition paths that cross parallel state boundaries

Identify transition paths that cross parallel state boundaries in Stateflow charts.

Passed

No transition paths crossing parallel state boundaries were found in Stateflow charts.

_____

✅ Check for inappropriate use of transition paths

Identify transition paths that go into and out of a state without ending on a substate.

Passed

No transition paths found that go into and out of a state without ending on a substate.

_____

⚠️ Check Stateflow charts for strong data typing

Identify expressions with variables and parameters of different data types in Stateflow objects.

Warning

The following expressions consist of variables and parameters of different datatypes:

Block Path	Expression
	TubeOut=CAL_TCF_TrqInvalid_s16;
	TubeOut=CAL_TCF_TrqInvalid_s16;
	TubeOut=(CH1H+CH1L)/2;
	TubeOut=(CH2H+CH2L)/2;
	TubeErr=1;
	TubeOut=(CH2L+CH1H)/2;
	TubeOut=(CH1L+CH2H)/2;
	TubeErr=0;
	TubeErr=0;

Recommended Action

Revisit expressions listed above to avoid operations with different data types.

_____

✅ Check naming of ports in Stateflow charts

Identify mismatches between names of Stateflow ports and associated signals

Passed

There are no name mismatches between Stateflow ports and associated signals

_____

✅ Check scoping of Stateflow data objects

Identify Stateflow data objects with local scope that are not scoped at the chart level or below

Passed

All Stateflow data objects are properly scoped.

_____

✅ Check Stateflow charts for uniquely defined data objects

Identify local data identifiers that are defined in multiple scopes within a chart.

Passed

No Stateflow data identifiers found to be defined in multiple scopes.

_____

✅ Check usage of shift operations for Stateflow data

Identify usage of Stateflow bit-shifting operations that might impact safety.

Passed

There are no Stateflow bit-shifting operations greater than the bit-width of the input or output type.

_____

✅ Check assignment operations in Stateflow charts

Identify assignment operations in Stateflow objects which cast integer and fixed-point calculations to wider datatype.

Passed

No assignment operations were found which cast integer and fixed-point calculations to wider datatype.

_____

✅ Check Stateflow charts for unary operators

Identify unary minus operators on unsigned data types in Stateflow objects.

Passed

No unary minus operations on unsigned data types were found in Stateflow objects.

📁 *MATLAB* ✅*10* ❌*0* ⚠️*0* 📄*0*

_____

✅ Check usage of standardized MATLAB function headers

Identify usage of standardized function headers in MATLAB function.

Passed

No MATLAB function blocks found without standardized function headers.

_____

✅ Check for MATLAB Function interfaces with inherited properties

Identify MATLAB Functions that have inputs, outputs, or parameters with inherited complexity or data type properties.

Passed

No MATLAB Function interfaces with inherited complexity or data type properties found.

_____

✅ Check MATLAB Function metrics

Identify MATLAB Functions that violate code and complexity metrics.

Passed

No MATLAB Function blocks found that violate code and complexity metrics.

_____

✅ Check MATLAB Code Analyzer messages

Check MATLAB functions for %#codegen directive, MATLAB Code Analyzer messages, and justification message IDs.

Passed

No MATLAB Function blocks found with Code Analyzer messages, missing %#codegen directive or inappropriate usage of justification message IDs.

_____

✅ Check if/elseif/else patterns in MATLAB Function blocks

Identify if/elseif/else patterns without appropriate else conditions in embedded MATLAB code

Passed

No inappropriate if/elseif/else patterns found.

_____

✅ Check switch statements in MATLAB Function blocks

Identify inappropriately used switch statements in embedded MATLAB code

Passed

No inappropriately used switch statements found.

_____

✅ Check usage of relational operators in MATLAB Function blocks

Identify relational operators operating on operands of different data types in MATLAB Function blocks.

Passed

No relational operators found operating on operands of different data types.

_____

✅ Check usage of equality operators in MATLAB Function blocks

Identify equality operators used with floating-point operands in MATLAB Function blocks.

Passed

No equality operators found operating on floating-point operands.

_____

✅ Check usage of logical operators and functions in MATLAB Function blocks

Identify logical operators and functions operating on operands with numerical data types.

Passed

No logical operators or functions found operating on operands with numerical data types.

_____

✅ Check type and size of condition expressions

Identify condition expressions which are not logical scalars.

Passed

No condition expressions found which are not logical scalars.

📁 *Configuration* ✅11 ❌0 ⚠️21 📄0

_____

⚠️ Check safety-related diagnostic settings for data store memory

Check diagnostic settings in the model configuration that apply to data store memory and might impact safety.

47

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Detect read before write (ReadBeforeWriteMsg)	UseLocalSettings	EnableAllAsError
Warning	Detect write after read (WriteAfterReadMsg)	UseLocalSettings	EnableAllAsError
Warning	Detect write after write (WriteAfterWriteMsg)	UseLocalSettings	EnableAllAsError
Warning	Duplicate data store names (UniqueDataStoreMsg)	none	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

Check safety-related diagnostic settings for saving
Check diagnostic settings in the model configuration that apply to saving model files.

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Block diagram contains disabled library links (SaveWithDisabledLinksMsg)	warning	error
Warning	Block diagram contains parameterized library links (SaveWithParameterizedLinksMsg)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

✅ Check safety-related model referencing settings
   Check model referencing settings in the model configuration that might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Rebuild (UpdateModelReferenceTargets)	IfOutOfDateOrStructuralChange	AssumeUpToDate, IfOutOfDateOrStructuralChange
Pass	Pass fixed-size scalar root inputs by value for code generation (ModelReferencePassRootInputsByReference) *	on	on

		off	off
Pass	Minimize algebraic loop occurrences (ModelReferenceMinAlgLoopOccurre nces)	off	off

Recommended Action

* The Command-Line values provided in the table are reverse of the settings in the Configuration Parameters Dialog. Therefore, 'on' in the Command-Line corresponds to an "Off" setting in the dialog, and 'off' in the Command-Line corresponds to an "On" setting in the dialog.

_____

⚠ Check safety-related code generation settings for comments

Check code generation settings in the model configuration that apply comments and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Warning	Requirements in block comments (ReqsInCode)	off	on	SystemTargetFile, GenerateComments

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related code generation interface settings
Check code generation interface settings in the model configuration that might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Warning	absolute time (SupportAbsoluteTime)	on	off	SystemTargetFile
Warning	Remove error status field in real-time model data structure (SuppressErrorStatus)	off	on	SystemTargetFile

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

✅ Check safety-related solver settings for simulation time
Identify if the model Start time is set to 0 and Stop time is less than the Application Life Span.

Passed
No issues found with solver settings for simulation time.

_____

✅ Check safety-related solver settings for solver options
Check solver settings in the model configuration that apply to solvers and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Type (SolverType)	Fixed-step	Fixed-step
Pass	Solver (SolverName)	FixedStepDiscrete	FixedStepDiscrete

_____

✅ Check safety-related solver settings for tasking and sample-time

Check solver settings in the model configuration that apply to tasking and sample-time constraints and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Not Recommended Values
Pass	Automatically handle rate transition for data transfer (AutoInsertRateTranBlk)	off	on

_____

⚠️ Check safety-related diagnostic settings for solvers

Check diagnostic settings in the model configuration that apply to solvers and might impact safety.

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Algebraic loop (AlgebraicLoopMsg)	warning	error
Warning	Minimize algebraic loop (ArtificialAlgebraicLoopMsg)	warning	error
Warning	Block priority violation (BlockPriorityViolationMsg)	warning	error
Warning	Automatic solver parameter selection (SolverPrmCheckMsg)	none	error
Warning	State name clash (StateNameClashWarn)	none	warning

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for sample time

Check diagnostic settings in the model configuration that apply to sample time and might impact safety.

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Source block specifies -1 sample time (InheritedTsInSrcMsg)	warning	error
Warning	Enforce sample times specified by Signal Specification blocks (SigSpecEnsureSampleTimeMsg)	warning	error
Warning	Single task rate transition (SingleTaskRateTransMsg)	none	error
Warning	Tasks with equal priority (TasksWithSamePriorityMsg)	warning	error
Warning	Unspecified inheritability of sample time (UnknownTsInhSupMsg)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

Check safety-related optimization settings for logic signals

Check optimization settings in the model configuration that apply to logic signals and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Implement logic signals as Boolean data (vs. double) (BooleanDataType)	on	on

_____

⚠ Check safety-related block reduction optimization settings

Check block reduction optimization settings in the model configuration that might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Block reduction (BlockReduction)	on	off

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related code generation settings for code style

Check code generation settings in the model configuration that apply to code style and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Warning	Parentheses level (ParenthesesLevel)	Nominal	Maximum	SystemTargetFile
Warning	Preserve operand order in expression (PreserveExpressionOrder)	off	on	SystemTargetFile

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related optimization settings for application lifespan
Check optimization settings in the model configuration that apply to application lifespan and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Application lifespan (days) (LifeSpan)	auto	inf

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related code generation identifier settings

Check code generation identifier settings in the model configuration that might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Not Recommended Values	Prerequisites
Warning	Minimum mangle length (MangleLength)	1	1, 2, 3	SystemTargetFile

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

✅ Check safety-related optimization settings for loop unrolling threshold

Check optimization settings in the model configuration that apply to loop unrolling threshold and might impact safety.

Passed

57

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Not Recommended Values
Pass	Loop unrolling threshold (RollThreshold)	5	0, 1

_____

⚠️ Check safety-related optimization settings for data initialization

Check optimization settings in the model configuration that apply to data initialization and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Warning	Remove root level I/O zero initialization (ZeroExternalMemoryAtStartup) *	off	on	SystemTargetFile
Warning	Remove internal data zero initialization (ZeroInternalMemoryAtStartup) *	off	on	SystemTargetFile

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

* The Command-Line values provided in the table are reverse of the settings in the Configuration Parameters Dialog. Therefore, 'on' in the Command-Line corresponds to an "Off" setting in the dialog, and 'off' in the Command-Line corresponds to an "On" setting in the dialog.

_____

⚠ Check safety-related optimization settings for data type conversions

  Check optimization settings in the model configuration that apply to data type conversions and might impact safety.

 Warning

 The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Remove code from floating-point to integer conversions that wraps out-of-range values (EfficientFloat2IntCast)	off	on

 Recommended Action

 Follow the links in the result table to modify the model configuration parameters.

_____

✅ Check safety-related optimization settings for division arithmetic exceptions

  Check optimization settings in the model configuration that apply to division arithmetic exceptions and might impact safety.

 Passed

59

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Prerequisites
D - Pass	System target file (SystemTargetFile)	ERT based target	ERT based target	
Pass	Remove code that protects against division arithmetic exceptions (NoFixptDivByZeroProtection)	off	off	SystemTargetFile

_____

✅ Check safety-related optimization settings for specified minimum and maximum values

Check optimization settings in the model configuration that apply to specified minimum and maximum values and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Pass	Optimize using the specified minimum and maximum values (UseSpecifiedMinMax)	off	off	SystemTargetFile
D - Pass	System target file (SystemTargetFile)	ERT based target	ERT based target	

_____

⚠ Check safety-related diagnostic settings for compatibility

    Check diagnostic settings in the model configuration that affect compatibility and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	S-function upgrades needed (SFcnCompatibilityMsg)	none	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for parameters

    Check diagnostic settings in the model configuration that apply to parameters and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Detect underflow (ParameterUnderflowMsg)	none	error
Warning	Detect precision loss (ParameterPrecisionLossMsg)	none	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

✅ Check safety-related diagnostic settings for Merge blocks

Check diagnostic settings in the model configuration that apply to Merge blocks and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Detect multiple driving blocks executing at the same time step (MergeDetectMultiDrivingBlocksExec)	error	error

_____

✅ Check safety-related diagnostic settings for model initialization

Check diagnostic settings in the model configuration that affect model initialization and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Underspecified initialization detection (UnderspecifiedInitializationDetection)	Simplified	Simplified

_____

⚠ Check safety-related diagnostic settings for data used for debugging

Check diagnostic settings in the model configuration that apply to data used for debugging and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Model Verification block enabling (AssertControl)	UseLocalSettings	DisableAll

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for signal connectivity

Check diagnostic settings in the model configuration that apply to signal connectivity and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Signal label mismatch (SignalLabelMismatchMsg)	none	error
Warning	Unconnected block input ports (UnconnectedInputMsg)	warning	error
Warning	Unconnected block output ports (UnconnectedOutputMsg)	warning	error
Warning	Unconnected line (UnconnectedLineMsg)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for bus connectivity

Check diagnostic settings in the model configuration that apply to bus connectivity and might impact safety.

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warnin g	Unspecified bus object at root Outport block (RootOutportRequireBusObjec t)	warning	error
Warnin g	Element name mismatch (BusObjectLabelMismatch)	warning	error
Warnin g	Bus signal treated as vector (StrictBusMsg)	WarnOnBusTreatedAsVect or	ErrorOnBusTreatedAsVect or
Warnin g	Non-bus signals treated as bus signals (NonBusSignalsTreatedAsBus)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

Check safety-related diagnostic settings that apply to function-call connectivity

Check diagnostic settings in the model configuration that apply to function-call connectivity and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	InvalidFcnCallConnMsg	error	error
Pass	Context-dependent inputs (FcnCallInpInsideContextMsg)	error	error

_____

⚠ Check safety-related diagnostic settings for type conversions

Check diagnostic settings in the model configuration that apply to type conversions and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Vector/matrix block input conversion (VectorMatrixConversionMsg)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for model referencing

    Check diagnostic settings in the model configuration that apply to model referencing and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Port and parameter mismatch (ModelReferenceIOMismatchMessage)	none	error
Warning	Invalid root Inport/Outport block connection (ModelReferenceIOMsg)	none	error
Warning	Unsupported data logging (ModelReferenceDataLoggingMessage)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for Stateflow

    Check diagnostic settings in the model configuration that apply to Stateflow and might impact safety.

Warning

67

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Invalid input data access in chart initialization (SFInvalidInputDataAccessInChartInitDiag)	warning	error
Warning	Transition outside natural parent (SFTransitionOutsideNaturalParentDiag)	warning	error
Warning	Unreachable execution path (SFUnreachableExecutionPathDiag)	warning	error
Warning	Undirected event broadcasts (SFUndirectedBroadcastEventsDiag)	warning	error
Warning	Transition action specified before condition action (SFTransitionActionBeforeConditionDiag)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for signal data

Check diagnostic settings in the model configuration that apply to signal data and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Division by singular matrix (CheckMatrixSingularityMsg)	none	error
Warning	Underspecified data types (UnderSpecifiedDataTypeMsg)	none	error
Warning	Wrap on overflow (IntegerOverflowMsg)	warning	error
Warning	Saturate on overflow (IntegerSaturationMsg)	warning	error
Warning	Inf or NaN block output (SignalInfNanChecking)	warning	error
Warning	Simulation range checking (SignalRangeChecking)	none	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

📁 *Naming* ✅1 ❌0 ⚠️1 🗒️0
_____

✅ Check model file name
Identify inappropriate characters and length issues in model file name

Passed
No issues found with model file name.

_____

⚠️ Check model object names
Identify invalid names of following model objects (first invalid name fragment is highlighted):

- Blocks

- Signals

69

- Parameters

- Buses

- Stateflow elements

Warning

The following model objects have invalid names:

Block	Name
SWC_TCF/SWC_TCF_1ms_sys/function	*(Reserved identifier)*
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Logical Operator	LogicalOperator
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator1" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator1	RelationalOperator1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem	IfActionSubsystem
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem/Action Port" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem/Action Port	ActionPort
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem1"	IfActionSubsystem1

title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/Motor Mode/If Action Subsystem1	
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem1/Action Port" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/Motor Mode/If Action Subsystem1/Action Port	ActionPort
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem2" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/Motor Mode/If Action Subsystem2	IfActionSubsystem2
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem2/Action Port" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/Motor Mode/If Action Subsystem2/Action Port	ActionPort
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem3" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/Motor Mode/If Action Subsystem3	IfActionSubsystem3
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem3/Action Port" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/Motor Mode/If Action Subsystem3/Action Port	ActionPort
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/ Logical Operator	LogicalOperator

SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator1" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator1	RelationalOperator1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator2" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator2	RelationalOperator2
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Logical Operator	LogicalOperator
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator1" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator1	RelationalOperator1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator2" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator2	RelationalOperator2

SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero	CompareToZero
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator	LogicalOperator
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator1" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator1	LogicalOperator1
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Unit Delay1" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Unit Delay1	UnitDelay1
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Saturation Dynamic" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Saturation Dynamic	SaturationDynamic
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/60div2PI" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/60div2PI	0div2PI
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion	SignalConversion
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion1" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion1	SignalConversion1
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion2" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion2	SignalConversion2
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion3" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion3	SignalConversion3
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion4" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion4	SignalConversion4
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion5" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion5	SignalConversion5
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion6" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion6	SignalConversion6

SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion7" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion7	SignalConversion7
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero	CompareToZero
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator	LogicalOperator
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator1" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator1	LogicalOperator1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Unit Delay1" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Unit Delay1	UnitDelay1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/1-D Lookup Table" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/1-D Lookup Table	DLookupTable
SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/Relational Operator2" title="SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/Relational Operator2	RelationalOperator2
SWC_TCF/SWC_TCF_Init/Event Listener	EventListener

∧ Less

Signal	Name
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc	PhPower

74

Recommended Action

Change flagged names of model objects

📁 *Requirements* ✅0 ❌0 ⚠️0 📃1

_____

📃 Check for model elements that do not link to requirements

Not Run

📁 *Code* ✅1 ❌0 ⚠️1 📃0

_____

✅ Check for blocks not recommended for MISRA C:2012

Identify blocks that are not recommended for MISRA C:2012 compliant code generation.

Passed

None of the blocks are defined as "not recommended" for MISRA C:2012 compliant code generation.

_____

⚠️ Check configuration parameters for MISRA C:2012

Identify configuration parameters that might impact MISRA C:2012 compliant code generation.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values	Prerequisites

	Model Verification block enabling (AssertControl)	UseLocalSettings	DisableAll	
Warning	Generate shared constants (GenerateSharedConstants)	on	off	UtilityFuncGeneration
Warning	Parentheses level (ParenthesesLevel)	Nominal	Maximum	SystemTargetFile
Warning	Casting modes (CastingMode)	Nominal	Standards	SystemTargetFile
Warning	Use division for fixed-point net slope computation (UseDivisionForNetSlopeComputation)	off	on, UseDivisionForReciprocalsOfIntegersOnly	
Warning	Replace multiplications by powers of two with signed bitwise shifts (EnableSignedLeftShifts)	on	off	SystemTargetFile
Warning	Allow right shifts on signed integers (EnableSignedRightShifts)	on	off	SystemTargetFile
Warning	Undirected event broadcasts (SFUndirectedBroadcastEventsDiag)	warning	error	
Warning	Compile-time recursion limit for MATLAB functions (CompileTimeRecursionLimit)	50	0	
Warning	Enable run-time recursion for MATLAB functions (EnableRuntimeRecursion)	on	off	
Warning	MATLAB user comments (MATLABFcnDesc)	off	on	GenerateComments,

				SystemTargetFil e

∧ Less

Recommended Action

Modify the configuration parameters listed above to the recommended values.

📁 Bug Reports   ✅0 ❌0 ⚠14 🗒0

_____

⚠ *Display bug reports for Embedded Coder*

Display bug reports for Embedded Coder (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning
Unable to query bug reports from Mathworks.

Recommended Action
Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠ *Display bug reports for IEC Certification Kit*

Display bug reports for IEC Certification Kit (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning
Unable to query bug reports from Mathworks.

77

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Polyspace Code Prover*

Display bug reports for Polyspace Code Prover (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Polyspace Bug Finder*

Display bug reports for Polyspace Bug Finder (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value

Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Polyspace Code Prover Server*

Display bug reports for Polyspace Code Prover Server (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

 Warning
Unable to query bug reports from Mathworks.

 Recommended Action
Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Polyspace Bug Finder Server*

Display bug reports for Polyspace Bug Finder Server (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

 Warning
Unable to query bug reports from Mathworks.

 Recommended Action
Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Simulink Design Verifier*

Display bug reports for Simulink Design Verifier (R2020a) available at
https://www.mathworks.com/support/bugreports .

NOTE: This check does not determine whether your model might be affected by these bugs.

 Warning

Unable to query bug reports from Mathworks.

 Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠ *Display bug reports for Simulink PLC Coder*

Display bug reports for Simulink PLC Coder (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

 Warning

Unable to query bug reports from Mathworks.

 Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠ *Display bug reports for Simulink Check*

Display bug reports for Simulink Check (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

 Warning

Unable to query bug reports from Mathworks.

 Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠ *Display bug reports for Simulink Coverage*

Display bug reports for Simulink Coverage (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning
Unable to query bug reports from Mathworks.

Recommended Action
Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠ *Display bug reports for Simulink Test*

Display bug reports for Simulink Test (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning
Unable to query bug reports from Mathworks.

Recommended Action
Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Simulink Requirements*

Display bug reports for Simulink Requirements (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for AUTOSAR Blockset*

Display bug reports for AUTOSAR Blockset (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for HDL Coder*

Display bug reports for HDL Coder (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action
Make sure your internet connection works and you are able to access https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

📁 Modeling Standards for IEC 62304 ✅0 ❌0 ⚠️0 📋106

_____

📋 Display configuration management data
Not Run

_____

📋 Display model metrics and complexity report
Not Run

_____

📋 Check for unconnected objects
Not Run

📁 High-Integrity Systems ✅0 ❌0 ⚠️0 📋89

📁 _Simulink_ ✅_0_ ❌_0_ ⚠️_0_ 📋_28_

_____

📋 Check usage of Abs blocks
Not Run

_____

📋 Check usage of Math Function blocks (rem and reciprocal functions)
Not Run

_____

📋 Check usage of Math Function blocks (log and log10 functions)
Not Run

_____

83

⊞ Check usage of While Iterator blocks

Not Run

_____

⊞ Check usage of For and While Iterator subsystems

Not Run

_____

⊞ Check usage of For Iterator blocks

Not Run

_____

⊞ Check usage of If blocks and If Action Subsystem blocks

Not Run

_____

⊞ Check usage of Switch Case blocks and Switch Case Action Subsystem blocks

Not Run

_____

⊞ Check usage of conditionally executed subsystems

Not Run

_____

⊞ Check usage of Merge blocks

Not Run

_____

⊞ Check Relational Operator blocks equating floating-point types

Not Run

_____

⊞ Check usage of Relational Operator blocks

Not Run

_____

⊞ Check usage of Logical Operator blocks

Not Run

_____

⊞ Check usage of bit operation blocks

Not Run

_____

📄 Check for blocks not recommended for C/C++ production code deployment

Not Run

_____

📄 Check for inconsistent vector indexing methods

Not Run

_____

📄 Check data types for blocks with index signals

Not Run

_____

📄 Check usage of variant blocks

Not Run

_____

📄 Check usage of lookup table blocks

Not Run

_____

📄 Check usage of Signal Routing blocks

Not Run

_____

📄 Check for root Inports with missing properties

Not Run

_____

📄 Check for root Inports with missing range definitions

Not Run

_____

📄 Check for root Outports with missing range definitions

Not Run

_____

📄 Check usage of Assignment blocks

Not Run

_____

⊞ Check global variables in graphical functions
Not Run

_____

⊞ Check usage of Gain blocks
Not Run

_____

⊞ Check for length of user-defined object names
Not Run

_____

⊞ Check data type of loop control variables
Not Run

📁 *Stateflow* ✅0 ❌0 ⚠️0 ⊞14

_____

⊞ Check state machine type of Stateflow charts
Not Run

_____

⊞ Check Stateflow charts for ordering of states and transitions
Not Run

_____

⊞ Check usage of bitwise operations in Stateflow charts
Not Run

_____

⊞ Check for Strong Data Typing with Simulink I/O
Not Run

_____

⊞ Check Stateflow debugging options
Not Run

_____

⊞ Check Stateflow charts for transition paths that cross parallel state boundaries
Not Run

_____

🗒 Check for inappropriate use of transition paths

Not Run

_____

🗒 Check Stateflow charts for strong data typing

Not Run

_____

🗒 Check naming of ports in Stateflow charts

Not Run

_____

🗒 Check scoping of Stateflow data objects

Not Run

_____

🗒 Check Stateflow charts for uniquely defined data objects

Not Run

_____

🗒 Check usage of shift operations for Stateflow data

Not Run

_____

🗒 Check assignment operations in Stateflow charts

Not Run

_____

🗒 Check Stateflow charts for unary operators

Not Run

📁 *MATLAB* ✅*0* ❌*0* ⚠*0* 🗒*10*

_____

🗒 Check usage of standardized MATLAB function headers

Not Run

_____

🗒 Check for MATLAB Function interfaces with inherited properties

Not Run

_____

87

📋 Check MATLAB Function metrics
   Not Run

_____

📋 Check MATLAB Code Analyzer messages
   Not Run

_____

📋 Check if/elseif/else patterns in MATLAB Function blocks
   Not Run

_____

📋 Check switch statements in MATLAB Function blocks
   Not Run

_____

📋 Check usage of relational operators in MATLAB Function blocks
   Not Run

_____

📋 Check usage of equality operators in MATLAB Function blocks
   Not Run

_____

📋 Check usage of logical operators and functions in MATLAB Function blocks
   Not Run

_____

📋 Check type and size of condition expressions
   Not Run

📁 *Configuration*   ✅0 ❌0 ⚠️0 📋32

_____

📋 Check safety-related diagnostic settings for data store memory
   Not Run

_____

📋 Check safety-related diagnostic settings for saving
   Not Run

_____

Check safety-related model referencing settings

Not Run

_____

Check safety-related code generation settings for comments

Not Run

_____

Check safety-related code generation interface settings

Not Run

_____

Check safety-related solver settings for simulation time

Not Run

_____

Check safety-related solver settings for solver options

Not Run

_____

Check safety-related solver settings for tasking and sample-time

Not Run

_____

Check safety-related diagnostic settings for solvers

Not Run

_____

Check safety-related diagnostic settings for sample time

Not Run

_____

Check safety-related optimization settings for logic signals

Not Run

_____

Check safety-related block reduction optimization settings

Not Run

_____

Check safety-related code generation settings for code style

Not Run

_____

▤ Check safety-related optimization settings for application lifespan

Not Run

_____

▤ Check safety-related code generation identifier settings

Not Run

_____

▤ Check safety-related optimization settings for loop unrolling threshold

Not Run

_____

▤ Check safety-related optimization settings for data initialization

Not Run

_____

▤ Check safety-related optimization settings for data type conversions

Not Run

_____

▤ Check safety-related optimization settings for division arithmetic exceptions

Not Run

_____

▤ Check safety-related optimization settings for specified minimum and maximum values

Not Run

_____

▤ Check safety-related diagnostic settings for compatibility

Not Run

_____

▤ Check safety-related diagnostic settings for parameters

Not Run

_____

▤ Check safety-related diagnostic settings for Merge blocks

Not Run

_____

📄 Check safety-related diagnostic settings for model initialization
　　Not Run

_____

📄 Check safety-related diagnostic settings for data used for debugging
　　Not Run

_____

📄 Check safety-related diagnostic settings for signal connectivity
　　Not Run

_____

📄 Check safety-related diagnostic settings for bus connectivity
　　Not Run

_____

📄 Check safety-related diagnostic settings that apply to function-call connectivity
　　Not Run

_____

📄 Check safety-related diagnostic settings for type conversions
　　Not Run

_____

📄 Check safety-related diagnostic settings for model referencing
　　Not Run

_____

📄 Check safety-related diagnostic settings for Stateflow
　　Not Run

_____

📄 Check safety-related diagnostic settings for signal data
　　Not Run

📁 *Naming*　 ✅*0* ❌*0* ⚠️*0* 📄*2*

_____

📄 Check model file name
　　Not Run

_____

📄 Check model object names
    Not Run

📁 *Requirements* ✅0 ❌0 ⚠0 📄1
_____

📄 Check for model elements that do not link to requirements
    Not Run

📁 *Code* ✅0 ❌0 ⚠0 📄2
_____

📄 Check for blocks not recommended for MISRA C:2012
    Not Run

_____

📄 Check configuration parameters for MISRA C:2012
    Not Run

📁 Bug Reports ✅0 ❌0 ⚠0 📄14
_____

📄 *Display bug reports for Embedded Coder*
    Not Run

_____

📄 *Display bug reports for IEC Certification Kit*
    Not Run

_____

📄 *Display bug reports for Polyspace Code Prover*
    Not Run

_____

📄 *Display bug reports for Polyspace Bug Finder*
    Not Run

_____

📄 *Display bug reports for Polyspace Code Prover Server*
    Not Run

_____

📋 *Display bug reports for Polyspace Bug Finder Server*
Not Run

_____

📋 *Display bug reports for Simulink Design Verifier*
Not Run

_____

📋 *Display bug reports for Simulink PLC Coder*
Not Run

_____

📋 *Display bug reports for Simulink Check*
Not Run

_____

📋 *Display bug reports for Simulink Coverage*
Not Run

_____

📋 *Display bug reports for Simulink Test*
Not Run

_____

📋 *Display bug reports for Simulink Requirements*
Not Run

_____

📋 *Display bug reports for AUTOSAR Blockset*
Not Run

_____

📋 *Display bug reports for HDL Coder*
Not Run

📁 Modeling Standards for ISO 26262 ✅62 ❌0 ⚠️43 📋1

_____

✅ Display configuration management data
   Display model configuration and checksum information

Model configuration and checksum information

Attribute	Value
Model Version	1.565
Author	dongliyuan
Date	Mon Dec 20 15:40:59 2021
Model Checksum	2556881298 2768319665 381822641 1525871067

_____

✅ Display model metrics and complexity report

Display number of elements and name, level, and depth of subsystems for the model or subsystem

Model metrics information

Display number of elements for Simulink blocks and Stateflow constructs

Summary

Element Type	Count
Inport	90
Outport	45
SubSystem	30
Stateflow	2

Simulink

Block Type	Count
Inport	90
Constant	52
Outport	45
SubSystem	30
Product	27
Sum	22
Switch	14
RelationalOperator	13
From	9
SignalConversion	8
Logic	7
Lookup_n-D	6
Delay	6
EnablePort	5
Goto	5
Abs	4
ActionPort	4
Ground	4
UnitDelay	3
Terminator	3
LookupNDDirect	2
TriggerPort	1
If	1
Merge	1
Sqrt	1
S-Function	1

EventListener	1

∧ Less

Stateflow

Stateflow construct	Count
Stateflow Transitions	44
Stateflow Junctions	38
Stateflow Data	14
Stateflow Charts	2

_____ **Model complexity information**

Display name, level, and depth of subsystems

Maximum Subsystem Depth: 6

Subsystem Depth

Subsystem Name	Level	Depth
SWC_TCF/SWC_TCF_1ms_sys	1	5

SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc	2	4
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc	3	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc	3	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc	3	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg	3	3
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode	4	2
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode /If Action Subsystem	5	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem1" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode /If Action Subsystem1	5	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem2" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode /If Action Subsystem2	5	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem3" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode /If Action Subsystem3	5	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir	4	1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir	4	1

SWC_TCF/SWC_TCF_1ms_sys/Debouncer	2	2
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero	3	1
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem	3	1
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc	2	3
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc	3	1
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc	3	1
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc	3	2
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Saturation Dynamic" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Saturation Dynamic	4	1
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc	3	1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc	2	3
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	3	2
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero	4	1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem	4	1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend	3	1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube	3	1
SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable	2	1
SWC_TCF/SWC_TCF_Init	1	1

∧ Less

✅ Check for unconnected objects

Identify unconnected lines, input ports, and output ports in the model

Passed

There are no unconnected lines, input ports, and output ports in this model.

📁 High-Integrity Systems   ✅59 ❌0 ⚠️29 🗒️1

📁 *Simulink*   ✅*25* ❌*0* ⚠️*3* 🗒️*0*

✅ Check usage of Abs blocks

Identify Abs blocks that have unreachable code or produce overflows

Passed

No Abs blocks found causing unreachable code or produce overflows.

✅ Check usage of Math Function blocks (rem and reciprocal functions)

Identify Math Function blocks using rem and reciprocal functions that cause non-finite results

Passed

All Math Function blocks in the model use operators appropriately.

✅ Check usage of Math Function blocks (log and log10 functions)

Identify Math Function blocks using log and log10 functions that cause non-finite results

Passed

All Math Function blocks in the model use operators appropriately.

✅ Check usage of While Iterator blocks

Identify While Iterator blocks that do not have a positive value for the maximum number of iterations

Passed

No While Iterator blocks found that might cause infinite loops

99

_____

✅ Check usage of For and While Iterator subsystems

Identify sample time-dependent blocks in While and For Iterator subsystems.

Passed

No sample time-dependent blocks in For or While Iterator subsystems.

_____

✅ Check usage of For Iterator blocks

Identify For Iterator blocks that cause variable loops

Passed

No For Iterator blocks found that cause variable loops.

_____

✅ Check usage of If blocks and If Action Subsystem blocks

Identify If and If Action Subsystem blocks without else conditions

Passed

No If blocks with questionable configurations or connections were found.

_____

✅ Check usage of Switch Case blocks and Switch Case Action Subsystem blocks

Identify inappropriately used Switch Case blocks and Switch Case Action Subsystem blocks

Passed

No Switch Case blocks with questionable configurations or connections were found.

_____

✅ Check usage of conditionally executed subsystems

Identify inappropriate blocks in conditionally executed subsystems.

Passed

No blocks with improper sample times or asynchronously executed sample-time dependent blocks were found.

_____

✅ Check usage of Merge blocks

Identify Merge blocks constructs which can lead to ambiguous behavior.

Passed

No merge blocks found which can lead to ambiguous behavior.

100

_____

✅ Check Relational Operator blocks equating floating-point types

Identify Relational Operator blocks that equate floating-point types

Passed

No Relational Operator blocks found that equate floating-point types.

_____

✅ Check usage of Relational Operator blocks

Identify Relational Operator blocks that operate on different data types or have a non-boolean output

Passed

No Relational Operator blocks found that operate on different data types or have a non-boolean output.

_____

✅ Check usage of Logical Operator blocks

Identify Logical Operator blocks that operate on non-boolean data types

Passed

No Logical Operator blocks found that operate on non-boolean data types.

_____

✅ Check usage of bit operation blocks

Identify bit operation blocks with signed data types as inputs

Passed

No bit operation blocks found with signed data types as inputs.

_____

✅ Check for blocks not recommended for C/C++ production code deployment

Identify blocks not supported by code generation or not recommended for C/C++ production code deployment.

Passed

No blocks found which are not recommended for C/C++ production code deployment.

_____

✅ Check for inconsistent vector indexing methods

Identify inconsistent usage of vector indexing methods across the model or subsystem

Passed

No blocks found using inconsistent indexing modes.

_____

⚠️ Check data types for blocks with index signals

Identify blocks with index signals that have data types other than integers or enums.

Warning

The following blocks have inappropriate data types for index signals or variables:

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/LookupCosTable

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/LookupSinTable

Recommended Action

Change the data type of index signals or variables to an integer or enum data type.

_____

✅ Check usage of variant blocks

Check variant block settings that might result in code that doesn't trace back to requirements.

Passed

There are no variant blocks that have "Generate preprocessor conditionals" active.

_____

✅ Check usage of lookup table blocks

Check for Lookup Table blocks, Prelookup blocks and Interpolation blocks that do not generate out-of-range checking code.

Passed

No lookup table blocks found to not generate out-of-range checking code.

_____

✅ Check usage of Signal Routing blocks

Identify usage of Signal Routing blocks in Simulink that might impact safety

Passed

No Switch blocks that might generate code with inequality operations (~=) in expressions where at least one side of the expression is a floating-point variable or constant were found.

_____

⚠ Check for root Inports with missing properties

Identify Inport blocks in the top-level of the model with missing or inherited sample times, data types, or port dimensions. Inport block properties are specified with block parameters or Simulink signal data objects that explicitly resolve to the connected signal lines.

Warning

The following Inport blocks have undefined or inherited sample times, data types or port dimensions:

- SWC_TCF/HSPF_StrrTempFlt

- SWC_TCF/SWD_AgRtr

- SWC_TCF/SWD_Spd

Recommended Action

Specify a data type for the listed Inport blocks or Simulink signal objects.

_____ Identify Inport blocks in the top-level of the model with missing or inherited sample times, data types, or port dimensions. Inport block properties are specified with block parameters or Simulink signal data objects that explicitly resolve to the connected signal lines.

Warning

The following Inport blocks have undefined or inherited sample times, data types or port dimensions:

- SWC_TCF/HSPF_StrrTempFlt

- SWC_TCF/SWD_AgRtr

- SWC_TCF/SWD_Spd

Recommended Action

Specify port dimension for the listed Inport blocks or Simulink signal objects.

_____

⚠ Check for root Inports with missing range definitions

Identify root-level Inport blocks with missing or erroneous minimum or maximum values. Inport block minimum and maximum values are specified with block parameters or Simulink signal objects that explicitly resolve to the connected signal lines.

Warning

The following Inport blocks and Simulink signal objects have missing or erroneous range definitions:

- SWC_TCF/HSPF_StrrTempFlt

- SWC_TCF/SWD_AgRtr

- SWC_TCF/SWD_Spd

Recommended Action

Model contains Inport blocks or Simulink signal objects with inherited data type. For the Inport blocks or Simulink signal objects, select a build-in, enum, Simulink.Bus, Simulink.NumericType or a Simulink.AliasType data type.

_____ Identify root-level Inport blocks with missing or erroneous minimum or maximum values. Inport block minimum and maximum values are specified with block parameters or Simulink signal objects that explicitly resolve to the connected signal lines.

Warning

The following Inport blocks and Simulink signal objects have missing or erroneous range definitions:

- SWC_TCF/L2Com_HiPrecVolt

- SWC_TCF/L2Com_ModeReq

- SWC_TCF/L2Sampling_DycU_Mon

- SWC_TCF/L2Sampling_DycV_Mon

- SWC_TCF/L2Sampling_DycW_Mon

- SWC_TCF/TRSP_iU

- SWC_TCF/TRSP_iV

- SWC_TCF/TRSP_iW

Recommended Action

Model contains Inports with numeric data types that have missing range parameters (minimum and/or maximum). For the listed Inport blocks and Simulink signal objects, specify scalar minimum and maximum parameters.

_____

Check for root Outports with missing range definitions
Identify root-level Outport blocks with missing or erroneous minimum or maximum values. Outport block minimum and maximum values are specified with block parameters or Simulink signal objects that explicitly resolve to the connected signal lines.

Passed
There are no missing or erroneous Outport range properties at the model root level.
Note: Root Outports with inherited data types are not analyzed by this check.

_____

Check usage of Assignment blocks
Identify Assignment blocks whose array fields are not initialized.

Passed
All Assignment blocks are configured with block parameter "Action if any output element is not assigned" set to Warning or Error.

_____

Check global variables in graphical functions
Identify expressions that both read and write to the same global data.

Passed
No expressions found that both read and write to the same global data.

_____

✅ Check usage of Gain blocks

Identify Gain blocks with value which resolves to 1

Passed

No Gain blocks found with value which resolves to 1.

_____

✅ Check for length of user-defined object names

Identify user-defined object names with length greater than threshold

Passed

No Subsystem blocks found with function name length greater than Maximum identifier length. There are no data objects with names having length greater than Maximum identifier length.

_____

✅ Check data type of loop control variables

Identify loop control variables using non-integer data types.

Passed

No For Iterator blocks or MATLAB Function blocks found using non-integer data type for loop control counter variable.

📁 *Stateflow* ✅*11* ❌*0* ⚠️*3* 📄*0*

_____

⚠️ Check state machine type of Stateflow charts

Identify Stateflow Charts whose State Machine Type differs from the type set in the Model Advisor Configuration Editor.

Warning

The following Stateflow Charts do not set the recommended state machine type:

Block	Parameter	Current Value	Recommended Values

	StateMachineType	Mealy	Classic

Recommended Action

Select the appropriate state machine type for each Stateflow Chart.

_____

✅ Check Stateflow charts for ordering of states and transitions

Identify Stateflow charts that do not use explicit ordering of parallel states and transitions.

Passed

No Stateflow Charts found that deviate from recommended state/transition execution order settings.

_____

✅ Check usage of bitwise operations in Stateflow charts

Identify usage of signed data type operands to bitwise operations in Stateflow charts.

Passed

No Stateflow objects found that use signed data type operands with bitwise operations.

_____

✅ Check for Strong Data Typing with Simulink I/O

Verify configuration settings for strong data typing on the boundaries between Simulink and Stateflow

Passed

No Stateflow charts found that set 'Use Strong Data Typing with Simulink I/O' to off.

_____

⚠️ Check Stateflow debugging options

Identify whether Stateflow debugging options are set appropriately

Warning

The following Stateflow debugging options are not set appropriately:

Parameter	Current Value	Recommended Values
Wrap on overflow (IntegerOverflowMsg)	warning	error
Simulation range checking (SignalRangeChecking)	none	error

Recommended Action

Change the Stateflow debugging options to the recommended value.

_____

✅ Check Stateflow charts for transition paths that cross parallel state boundaries
Identify transition paths that cross parallel state boundaries in Stateflow charts.

Passed
No transition paths crossing parallel state boundaries were found in Stateflow charts.

_____

✅ Check for inappropriate use of transition paths
Identify transition paths that go into and out of a state without ending on a substate.

Passed
No transition paths found that go into and out of a state without ending on a substate.

_____

⚠️ Check Stateflow charts for strong data typing
Identify expressions with variables and parameters of different data types in Stateflow objects.

Warning

The following expressions consist of variables and parameters of different datatypes:

Block Path	Expression
	TubeOut=CAL_TCF_TrqInvalid_s16;
	TubeOut=CAL_TCF_TrqInvalid_s16;
	TubeOut=(CH1H+CH1L)/2;
	TubeOut=(CH2H+CH2L)/2;
	TubeErr=1;
	TubeOut=(CH2L+CH1H)/2;
	TubeOut=(CH1L+CH2H)/2;
	TubeErr=0;
	TubeErr=0;

Recommended Action

Revisit expressions listed above to avoid operations with different data types.

_____

✅ Check naming of ports in Stateflow charts
Identify mismatches between names of Stateflow ports and associated signals

Passed
There are no name mismatches between Stateflow ports and associated signals

_____

✅ Check scoping of Stateflow data objects
Identify Stateflow data objects with local scope that are not scoped at the chart level or below

Passed
All Stateflow data objects are properly scoped.

_____

✅ Check Stateflow charts for uniquely defined data objects
Identify local data identifiers that are defined in multiple scopes within a chart.

Passed

No Stateflow data identifiers found to be defined in multiple scopes.

_____

✅ Check usage of shift operations for Stateflow data

Identify usage of Stateflow bit-shifting operations that might impact safety.

Passed

There are no Stateflow bit-shifting operations greater than the bit-width of the input or output type.

_____

✅ Check assignment operations in Stateflow charts

Identify assignment operations in Stateflow objects which cast integer and fixed-point calculations to wider datatype.

Passed

No assignment operations were found which cast integer and fixed-point calculations to wider datatype.

_____

✅ Check Stateflow charts for unary operators

Identify unary minus operators on unsigned data types in Stateflow objects.

Passed

No unary minus operations on unsigned data types were found in Stateflow objects.

📁 _MATLAB_ ✅10 ❌0 ⚠0 🗐0

_____

✅ Check usage of standardized MATLAB function headers

Identify usage of standardized function headers in MATLAB function.

Passed

No MATLAB function blocks found without standardized function headers.

_____

✅ Check for MATLAB Function interfaces with inherited properties

Identify MATLAB Functions that have inputs, outputs, or parameters with inherited complexity or data type properties.

Passed

No MATLAB Function interfaces with inherited complexity or data type properties found.

✅ Check MATLAB Function metrics

Identify MATLAB Functions that violate code and complexity metrics.

Passed

No MATLAB Function blocks found that violate code and complexity metrics.

✅ Check MATLAB Code Analyzer messages

Check MATLAB functions for %#codegen directive, MATLAB Code Analyzer messages, and justification message IDs.

Passed

No MATLAB Function blocks found with Code Analyzer messages, missing %#codegen directive or inappropriate usage of justification message IDs.

✅ Check if/elseif/else patterns in MATLAB Function blocks

Identify if/elseif/else patterns without appropriate else conditions in embedded MATLAB code

Passed

No inappropriate if/elseif/else patterns found.

✅ Check switch statements in MATLAB Function blocks

Identify inappropriately used switch statements in embedded MATLAB code

Passed

No inappropriately used switch statements found.

✅ Check usage of relational operators in MATLAB Function blocks

Identify relational operators operating on operands of different data types in MATLAB Function blocks.

Passed

No relational operators found operating on operands of different data types.

✅ Check usage of equality operators in MATLAB Function blocks

Identify equality operators used with floating-point operands in MATLAB Function blocks.

111

Passed

No equality operators found operating on floating-point operands.

_____

✅ Check usage of logical operators and functions in MATLAB Function blocks

Identify logical operators and functions operating on operands with numerical data types.

Passed

No logical operators or functions found operating on operands with numerical data types.

_____

✅ Check type and size of condition expressions

Identify condition expressions which are not logical scalars.

Passed

No condition expressions found which are not logical scalars.

📁 *Configuration*   ✅*11* ❌*0* ⚠️*21* 🗒*0*

_____

⚠️ Check safety-related diagnostic settings for data store memory

Check diagnostic settings in the model configuration that apply to data store memory and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Detect read before write (ReadBeforeWriteMsg)	UseLocalSettings	EnableAllAsError
Warning	Detect write after read (WriteAfterReadMsg)	UseLocalSettings	EnableAllAsError

Warning	Detect write after write (WriteAfterWriteMsg)	UseLocalSettings	EnableAllAsError
Warning	Duplicate data store names (UniqueDataStoreMsg)	none	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for saving

Check diagnostic settings in the model configuration that apply to saving model files.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Block diagram contains disabled library links (SaveWithDisabledLinksMsg)	warning	error
Warning	Block diagram contains parameterized library links (SaveWithParameterizedLinksMsg)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

✅ Check safety-related model referencing settings

Check model referencing settings in the model configuration that might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Rebuild (UpdateModelReferenceTargets)	IfOutOfDateOrStructuralChange	AssumeUpToDate, IfOutOfDateOrStructuralChange
Pass	Pass fixed-size scalar root inputs by value for code generation (ModelReferencePassRootInputsByReference) *	on	on
Pass	Minimize algebraic loop occurrences (ModelReferenceMinAlgLoopOccurrences)	off	off

Recommended Action

* The Command-Line values provided in the table are reverse of the settings in the Configuration Parameters Dialog. Therefore, 'on' in the Command-Line corresponds to an "Off" setting in the dialog, and 'off' in the Command-Line corresponds to an "On" setting in the dialog.

_____

⚠ Check safety-related code generation settings for comments

    Check code generation settings in the model configuration that apply comments and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Warning	Requirements in block comments (ReqsInCode)	off	on	SystemTargetFile, GenerateComments

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related code generation interface settings

    Check code generation interface settings in the model configuration that might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Warning	absolute time (SupportAbsoluteTime)	on	off	SystemTargetFile
Warning	Remove error status field in real-time model data structure (SuppressErrorStatus)	off	on	SystemTargetFile

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

✅ Check safety-related solver settings for simulation time
Identify if the model Start time is set to 0 and Stop time is less than the Application Life Span.

Passed
No issues found with solver settings for simulation time.

_____

✅ Check safety-related solver settings for solver options
Check solver settings in the model configuration that apply to solvers and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Type (SolverType)	Fixed-step	Fixed-step
Pass	Solver (SolverName)	FixedStepDiscrete	FixedStepDiscrete

_____

✅ Check safety-related solver settings for tasking and sample-time

Check solver settings in the model configuration that apply to tasking and sample-time constraints and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Not Recommended Values
Pass	Automatically handle rate transition for data transfer (AutoInsertRateTranBlk)	off	on

_____

⚠️ Check safety-related diagnostic settings for solvers

Check diagnostic settings in the model configuration that apply to solvers and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Algebraic loop (AlgebraicLoopMsg)	warning	error

Status	Parameter	Current Value	Recommended Values
Warning	Minimize algebraic loop (ArtificialAlgebraicLoopMsg)	warning	error
Warning	Block priority violation (BlockPriorityViolationMsg)	warning	error
Warning	Automatic solver parameter selection (SolverPrmCheckMsg)	none	error
Warning	State name clash (StateNameClashWarn)	none	warning

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for sample time
Check diagnostic settings in the model configuration that apply to sample time and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Source block specifies -1 sample time (InheritedTsInSrcMsg)	warning	error
Warning	Enforce sample times specified by Signal Specification blocks (SigSpecEnsureSampleTimeMsg)	warning	error

118

Warning	Single task rate transition (SingleTaskRateTransMsg)	none	error
Warning	Tasks with equal priority (TasksWithSamePriorityMsg)	warning	error
Warning	Unspecified inheritability of sample time (UnknownTsInhSupMsg)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

✅ Check safety-related optimization settings for logic signals

Check optimization settings in the model configuration that apply to logic signals and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Implement logic signals as Boolean data (vs. double) (BooleanDataType)	on	on

_____

⚠️ Check safety-related block reduction optimization settings

Check block reduction optimization settings in the model configuration that might impact safety.

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Block reduction (BlockReduction)	on	off

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related code generation settings for code style

Check code generation settings in the model configuration that apply to code style and might impact safety.

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Warning	Parentheses level (ParenthesesLevel)	Nominal	Maximum	SystemTargetFile
Warning	Preserve operand order in expression (PreserveExpressionOrder)	off	on	SystemTargetFile

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related optimization settings for application lifespan

Check optimization settings in the model configuration that apply to application lifespan and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Application lifespan (days) (LifeSpan)	auto	inf

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related code generation identifier settings

Check code generation identifier settings in the model configuration that might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Not Recommended Values	Prerequisites
Warning	Minimum mangle length (MangleLength)	1	1, 2, 3	SystemTargetFile

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

✅ Check safety-related optimization settings for loop unrolling threshold
    Check optimization settings in the model configuration that apply to loop unrolling threshold and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Not Recommended Values
Pass	Loop unrolling threshold (RollThreshold)	5	0, 1

_____

⚠️ Check safety-related optimization settings for data initialization
    Check optimization settings in the model configuration that apply to data initialization and might impact safety.

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Warning	Remove root level I/O zero initialization (ZeroExternalMemoryAtStartup) *	off	on	SystemTargetFile
Warning	Remove internal data zero initialization (ZeroInternalMemoryAtStartup) *	off	on	SystemTargetFile

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

* The Command-Line values provided in the table are reverse of the settings in the Configuration Parameters Dialog. Therefore, 'on' in the Command-Line corresponds to an "Off" setting in the dialog, and 'off' in the Command-Line corresponds to an "On" setting in the dialog.

_____

⚠ Check safety-related optimization settings for data type conversions

Check optimization settings in the model configuration that apply to data type conversions and might impact safety.

123

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Remove code from floating-point to integer conversions that wraps out-of-range values (EfficientFloat2IntCast)	off	on

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

Check safety-related optimization settings for division arithmetic exceptions
Check optimization settings in the model configuration that apply to division arithmetic exceptions and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Prerequisites
D - Pass	System target file (SystemTargetFile)	ERT based target	ERT based target	
Pass	Remove code that protects against division arithmetic exceptions (NoFixptDivByZeroProtection)	off	off	SystemTargetFile

_____

✅ Check safety-related optimization settings for specified minimum and maximum values

Check optimization settings in the model configuration that apply to specified minimum and maximum values and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Pass	Optimize using the specified minimum and maximum values (UseSpecifiedMinMax)	off	off	SystemTargetFile
D - Pass	System target file (SystemTargetFile)	ERT based target	ERT based target	

_____

⚠️ Check safety-related diagnostic settings for compatibility

Check diagnostic settings in the model configuration that affect compatibility and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	S-function upgrades needed (SFcnCompatibilityMsg)	none	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for parameters

Check diagnostic settings in the model configuration that apply to parameters and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Detect underflow (ParameterUnderflowMsg)	none	error
Warning	Detect precision loss (ParameterPrecisionLossMsg)	none	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

✅ Check safety-related diagnostic settings for Merge blocks

Check diagnostic settings in the model configuration that apply to Merge blocks and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Detect multiple driving blocks executing at the same time step (MergeDetectMultiDrivingBlocksExec)	error	error

_____

✅ Check safety-related diagnostic settings for model initialization

Check diagnostic settings in the model configuration that affect model initialization and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Underspecified initialization detection (UnderspecifiedInitializationDetection)	Simplified	Simplified

_____

⚠ Check safety-related diagnostic settings for data used for debugging

   Check diagnostic settings in the model configuration that apply to data used for debugging and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Model Verification block enabling (AssertControl)	UseLocalSettings	DisableAll

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for signal connectivity

   Check diagnostic settings in the model configuration that apply to signal connectivity and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Signal label mismatch (SignalLabelMismatchMsg)	none	error
Warning	Unconnected block input ports (UnconnectedInputMsg)	warning	error
Warning	Unconnected block output ports (UnconnectedOutputMsg)	warning	error
Warning	Unconnected line (UnconnectedLineMsg)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for bus connectivity

Check diagnostic settings in the model configuration that apply to bus connectivity and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Unspecified bus object at root Outport block (RootOutportRequireBusObject)	warning	error

Warnin g	Element name mismatch (BusObjectLabelMismatch)	warning	error
Warnin g	Bus signal treated as vector (StrictBusMsg)	WarnOnBusTreatedAsVect or	ErrorOnBusTreatedAsVect or
Warnin g	Non-bus signals treated as bus signals (NonBusSignalsTreatedAsBus)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

✅ Check safety-related diagnostic settings that apply to function-call connectivity

Check diagnostic settings in the model configuration that apply to function-call connectivity and might impact safety.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	InvalidFcnCallConnMsg	error	error
Pass	Context-dependent inputs (FcnCallInpInsideContextMsg)	error	error

_____

130

⚠ Check safety-related diagnostic settings for type conversions

Check diagnostic settings in the model configuration that apply to type conversions and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Vector/matrix block input conversion (VectorMatrixConversionMsg)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for model referencing

Check diagnostic settings in the model configuration that apply to model referencing and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

131

Status	Parameter	Current Value	Recommended Values
Warning	Port and parameter mismatch (ModelReferenceIOMismatchMessage)	none	error
Warning	Invalid root Inport/Outport block connection (ModelReferenceIOMsg)	none	error
Warning	Unsupported data logging (ModelReferenceDataLoggingMessage)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for Stateflow

Check diagnostic settings in the model configuration that apply to Stateflow and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Invalid input data access in chart initialization (SFInvalidInputDataAccessInChartInitDiag)	warning	error
Warning	Transition outside natural parent (SFTransitionOutsideNaturalParentDiag)	warning	error

Warning	Unreachable execution path (SFUnreachableExecutionPathDiag)	warning	error
Warning	Undirected event broadcasts (SFUndirectedBroadcastEventsDiag)	warning	error
Warning	Transition action specified before condition action (SFTransitionActionBeforeConditionDiag)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check safety-related diagnostic settings for signal data

Check diagnostic settings in the model configuration that apply to signal data and might impact safety.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Division by singular matrix (CheckMatrixSingularityMsg)	none	error
Warning	Underspecified data types (UnderSpecifiedDataTypeMsg)	none	error
Warning	Wrap on overflow (IntegerOverflowMsg)	warning	error

	Saturate on overflow (IntegerSaturationMsg)	warning	error
Warning			
Warning	Inf or NaN block output (SignalInfNanChecking)	warning	error
Warning	Simulation range checking (SignalRangeChecking)	none	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

📁 *Naming* ✅1 ❌0 ⚠1 🗒0

_____

✅ Check model file name
Identify inappropriate characters and length issues in model file name

Passed
No issues found with model file name.

_____

⚠ Check model object names
Identify invalid names of following model objects (first invalid name fragment is highlighted):

- Blocks

- Signals

- Parameters

- Buses

- Stateflow elements

Warning
The following model objects have invalid names:

134

Block	Name
SWC_TCF/SWC_TCF_1ms_sys/function	*(Reserved identifier)*
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Logical Operator	LogicalOperator
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator1" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator1	RelationalOperator1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem	IfActionSubsystem
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem/Action Port" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem/Action Port	ActionPort
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem1" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem1	IfActionSubsystem1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem1/Action Port" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem1/Action Port	ActionPort

SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem2" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem2	IfActionSubsystem2
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem2/Action Port" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem2/Action Port	ActionPort
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem3" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem3	IfActionSubsystem3
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem3/Action Port" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem3/Action Port	ActionPort
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Logical Operator	LogicalOperator
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator1"	RelationalOperator1

title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator1	
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator2" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/Relational Operator2	RelationalOperator2
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Logical Operator	LogicalOperator
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator1" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator1	RelationalOperator1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator2" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/Relational Operator2	RelationalOperator2
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero	CompareToZero
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator	LogicalOperator
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator1" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator1	LogicalOperator1

SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Unit Delay1" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Unit Delay1	UnitDelay1
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Saturation Dynamic" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Saturation Dynamic	SaturationDynamic
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/60div2PI" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/60div2PI	0div2PI
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion	SignalConversion
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion1" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion1	SignalConversion1
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion2" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion2	SignalConversion2
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion3" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion3	SignalConversion3
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion4" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion4	SignalConversion4
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion5" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion5	SignalConversion5
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion6" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion6	SignalConversion6
SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion7" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion7	SignalConversion7
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero	CompareToZero

SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator	LogicalOperator
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator1" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator1	LogicalOperator1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Relational Operator	RelationalOperator
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Unit Delay1" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Unit Delay1	UnitDelay1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/1-D Lookup Table" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/1-D Lookup Table	DLookupTable
SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/Relational Operator2" title="SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/Relational Operator2	RelationalOperator2
SWC_TCF/SWC_TCF_Init/Event Listener	EventListener

∧ Less

Signal	Name
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc	PhPower

Recommended Action

Change flagged names of model objects

📁 *Requirements* ✅0 ❌0 ⚠0 📋1

_____

📋 Check for model elements that do not link to requirements

Not Run

📁 *Code* ✅1 ❌0 ⚠1 📋0

_____

✅ Check for blocks not recommended for MISRA C:2012

Identify blocks that are not recommended for MISRA C:2012 compliant code generation.

Passed

None of the blocks are defined as "not recommended" for MISRA C:2012 compliant code generation.

_____

⚠ Check configuration parameters for MISRA C:2012

Identify configuration parameters that might impact MISRA C:2012 compliant code generation.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Warning	Model Verification block enabling (AssertControl)	UseLocalSettings	DisableAll	

140

Warning	Generate shared constants (GenerateSharedConstants)	on	off	UtilityFuncGeneration
Warning	Parentheses level (ParenthesesLevel)	Nominal	Maximum	SystemTargetFile
Warning	Casting modes (CastingMode)	Nominal	Standards	SystemTargetFile
Warning	Use division for fixed-point net slope computation (UseDivisionForNetSlopeComputation)	off	on, UseDivisionForReciprocalsOfIntegersOnly	
Warning	Replace multiplications by powers of two with signed bitwise shifts (EnableSignedLeftShifts)	on	off	SystemTargetFile
Warning	Allow right shifts on signed integers (EnableSignedRightShifts)	on	off	SystemTargetFile
Warning	Undirected event broadcasts (SFUndirectedBroadcastEventsDiag)	warning	error	
Warning	Compile-time recursion limit for MATLAB functions (CompileTimeRecursionLimit)	50	0	
Warning	Enable run-time recursion for MATLAB functions (EnableRuntimeRecursion)	on	off	
Warning	MATLAB user comments (MATLABFcnDesc)	off	on	GenerateComments, SystemTargetFile

∧ Less

Recommended Action

Modify the configuration parameters listed above to the recommended values.

📁 Bug Reports   ✅0 ❌0 ⚠️14 🗒0
_____

⚠️ *Display bug reports for Embedded Coder*

Display bug reports for Embedded Coder (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning
Unable to query bug reports from Mathworks.

Recommended Action
Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for IEC Certification Kit*

Display bug reports for IEC Certification Kit (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning
Unable to query bug reports from Mathworks.

Recommended Action
Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Polyspace Code Prover*

Display bug reports for Polyspace Code Prover (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Polyspace Bug Finder*

Display bug reports for Polyspace Bug Finder (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

143

⚠️ *Display bug reports for Polyspace Code Prover Server*

Display bug reports for Polyspace Code Prover Server (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Polyspace Bug Finder Server*

Display bug reports for Polyspace Bug Finder Server (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Simulink Design Verifier*

Display bug reports for Simulink Design Verifier (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Simulink PLC Coder*

Display bug reports for Simulink PLC Coder (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Simulink Check*

Display bug reports for Simulink Check (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

Warning

Unable to query bug reports from Mathworks.

Recommended Action

Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

_____

⚠️ *Display bug reports for Simulink Coverage*

Display bug reports for Simulink Coverage (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

 Warning
Unable to query bug reports from Mathworks.

 Recommended Action
Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠️ *Display bug reports for Simulink Test*

Display bug reports for Simulink Test (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

 Warning
Unable to query bug reports from Mathworks.

 Recommended Action
Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠ *Display bug reports for Simulink Requirements*

Display bug reports for Simulink Requirements (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

 Warning
Unable to query bug reports from Mathworks.

 Recommended Action
Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠ *Display bug reports for AUTOSAR Blockset*

Display bug reports for AUTOSAR Blockset (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

 Warning
Unable to query bug reports from Mathworks.

 Recommended Action
Make sure your internet connection works and you are able to access
https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

_____

⚠ *Display bug reports for HDL Coder*

Display bug reports for HDL Coder (R2020a) available at
https://www.mathworks.com/support/bugreports .
NOTE: This check does not determine whether your model might be affected by these bugs.

 Warning

147

Unable to query bug reports from Mathworks.

Recommended Action
Make sure your internet connection works and you are able to access https://www.mathworks.com/support/bugreports/.

Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

📁 Modeling Standards for EN 50128   ✅0 ❌0 ⚠0 ▤106
_____

▤ Display configuration management data
Not Run

_____

▤ Display model metrics and complexity report
Not Run

_____

▤ Check for unconnected objects
Not Run

📁 High-Integrity Systems   ✅0 ❌0 ⚠0 ▤89

📁 *Simulink*   ✅*0* ❌*0* ⚠*0* ▤*28*
_____

▤ Check usage of Abs blocks
Not Run

_____

▤ Check usage of Math Function blocks (rem and reciprocal functions)
Not Run

_____

▤ Check usage of Math Function blocks (log and log10 functions)
Not Run

_____

▦ Check usage of While Iterator blocks

Not Run

_____

▦ Check usage of For and While Iterator subsystems

Not Run

_____

▦ Check usage of For Iterator blocks

Not Run

_____

▦ Check usage of If blocks and If Action Subsystem blocks

Not Run

_____

▦ Check usage of Switch Case blocks and Switch Case Action Subsystem blocks

Not Run

_____

▦ Check usage of conditionally executed subsystems

Not Run

_____

▦ Check usage of Merge blocks

Not Run

_____

▦ Check Relational Operator blocks equating floating-point types

Not Run

_____

▦ Check usage of Relational Operator blocks

Not Run

_____

▦ Check usage of Logical Operator blocks

Not Run

_____

▦ Check usage of bit operation blocks

Not Run

_____

⊞ Check for blocks not recommended for C/C++ production code deployment

Not Run

_____

⊞ Check for inconsistent vector indexing methods

Not Run

_____

⊞ Check data types for blocks with index signals

Not Run

_____

⊞ Check usage of variant blocks

Not Run

_____

⊞ Check usage of lookup table blocks

Not Run

_____

⊞ Check usage of Signal Routing blocks

Not Run

_____

⊞ Check for root Inports with missing properties

Not Run

_____

⊞ Check for root Inports with missing range definitions

Not Run

_____

⊞ Check for root Outports with missing range definitions

Not Run

_____

⊞ Check usage of Assignment blocks

Not Run

_____

⊞ Check global variables in graphical functions
   Not Run

_____

⊞ Check usage of Gain blocks
   Not Run

_____

⊞ Check for length of user-defined object names
   Not Run

_____

⊞ Check data type of loop control variables
   Not Run

📁 *Stateflow*   ✅*0* ❌*0* ⚠️*0* ⊞*14*

_____

⊞ Check state machine type of Stateflow charts
   Not Run

_____

⊞ Check Stateflow charts for ordering of states and transitions
   Not Run

_____

⊞ Check usage of bitwise operations in Stateflow charts
   Not Run

_____

⊞ Check for Strong Data Typing with Simulink I/O
   Not Run

_____

⊞ Check Stateflow debugging options
   Not Run

_____

⊞ Check Stateflow charts for transition paths that cross parallel state boundaries
   Not Run

_____

⊟ Check for inappropriate use of transition paths
    Not Run

_____

⊟ Check Stateflow charts for strong data typing
    Not Run

_____

⊟ Check naming of ports in Stateflow charts
    Not Run

_____

⊟ Check scoping of Stateflow data objects
    Not Run

_____

⊟ Check Stateflow charts for uniquely defined data objects
    Not Run

_____

⊟ Check usage of shift operations for Stateflow data
    Not Run

_____

⊟ Check assignment operations in Stateflow charts
    Not Run

_____

⊟ Check Stateflow charts for unary operators
    Not Run

📁 *MATLAB* ✅*0* ❌*0* ⚠️*0* ⊟*10*

_____

⊟ Check usage of standardized MATLAB function headers
    Not Run

_____

⊟ Check for MATLAB Function interfaces with inherited properties
    Not Run

_____

🗒 Check MATLAB Function metrics

Not Run

_____

🗒 Check MATLAB Code Analyzer messages

Not Run

_____

🗒 Check if/elseif/else patterns in MATLAB Function blocks

Not Run

_____

🗒 Check switch statements in MATLAB Function blocks

Not Run

_____

🗒 Check usage of relational operators in MATLAB Function blocks

Not Run

_____

🗒 Check usage of equality operators in MATLAB Function blocks

Not Run

_____

🗒 Check usage of logical operators and functions in MATLAB Function blocks

Not Run

_____

🗒 Check type and size of condition expressions

Not Run

📁 *Configuration*   ✅0 ❌0 ⚠0 🗒32

_____

🗒 Check safety-related diagnostic settings for data store memory

Not Run

_____

🗒 Check safety-related diagnostic settings for saving

Not Run

_____

▦ Check safety-related model referencing settings
    Not Run

_____

▦ Check safety-related code generation settings for comments
    Not Run

_____

▦ Check safety-related code generation interface settings
    Not Run

_____

▦ Check safety-related solver settings for simulation time
    Not Run

_____

▦ Check safety-related solver settings for solver options
    Not Run

_____

▦ Check safety-related solver settings for tasking and sample-time
    Not Run

_____

▦ Check safety-related diagnostic settings for solvers
    Not Run

_____

▦ Check safety-related diagnostic settings for sample time
    Not Run

_____

▦ Check safety-related optimization settings for logic signals
    Not Run

_____

▦ Check safety-related block reduction optimization settings
    Not Run

_____

▦ Check safety-related code generation settings for code style
    Not Run

▤ Check safety-related optimization settings for application lifespan

Not Run

▤ Check safety-related code generation identifier settings

Not Run

▤ Check safety-related optimization settings for loop unrolling threshold

Not Run

▤ Check safety-related optimization settings for data initialization

Not Run

▤ Check safety-related optimization settings for data type conversions

Not Run

▤ Check safety-related optimization settings for division arithmetic exceptions

Not Run

▤ Check safety-related optimization settings for specified minimum and maximum values

Not Run

▤ Check safety-related diagnostic settings for compatibility

Not Run

▤ Check safety-related diagnostic settings for parameters

Not Run

▤ Check safety-related diagnostic settings for Merge blocks

Not Run

155

▤ Check safety-related diagnostic settings for model initialization

    Not Run

_____

▤ Check safety-related diagnostic settings for data used for debugging

    Not Run

_____

▤ Check safety-related diagnostic settings for signal connectivity

    Not Run

_____

▤ Check safety-related diagnostic settings for bus connectivity

    Not Run

_____

▤ Check safety-related diagnostic settings that apply to function-call connectivity

    Not Run

_____

▤ Check safety-related diagnostic settings for type conversions

    Not Run

_____

▤ Check safety-related diagnostic settings for model referencing

    Not Run

_____

▤ Check safety-related diagnostic settings for Stateflow

    Not Run

_____

▤ Check safety-related diagnostic settings for signal data

    Not Run

📁 *Naming* ✅0 ❌0 ⚠0 ▤2

_____

▤ Check model file name

    Not Run

_____

156

📋 Check model object names
    Not Run

📁 *Requirements* ✅0 ❌0 ⚠️0 📋1
_____

📋 Check for model elements that do not link to requirements
    Not Run

📁 *Code* ✅0 ❌0 ⚠️0 📋2
_____

📋 Check for blocks not recommended for MISRA C:2012
    Not Run

_____

📋 Check configuration parameters for MISRA C:2012
    Not Run

📁 Bug Reports ✅0 ❌0 ⚠️0 📋14
_____

📋 *Display bug reports for Embedded Coder*
    Not Run

_____

📋 *Display bug reports for IEC Certification Kit*
    Not Run

_____

📋 *Display bug reports for Polyspace Code Prover*
    Not Run

_____

📋 *Display bug reports for Polyspace Bug Finder*
    Not Run

_____

📋 *Display bug reports for Polyspace Code Prover Server*
    Not Run

157

_____

📋 *Display bug reports for Polyspace Bug Finder Server*

Not Run

_____

📋 *Display bug reports for Simulink Design Verifier*

Not Run

_____

📋 *Display bug reports for Simulink PLC Coder*

Not Run

_____

📋 *Display bug reports for Simulink Check*

Not Run

_____

📋 *Display bug reports for Simulink Coverage*

Not Run

_____

📋 *Display bug reports for Simulink Test*

Not Run

_____

📋 *Display bug reports for Simulink Requirements*

Not Run

_____

📋 *Display bug reports for AUTOSAR Blockset*

Not Run

_____

📋 *Display bug reports for HDL Coder*

Not Run

📁 Model Metrics ✅0 ❌0 ⚠0 📋10

📁 Count Metrics ✅0 ❌0 ⚠0 📋7

_____
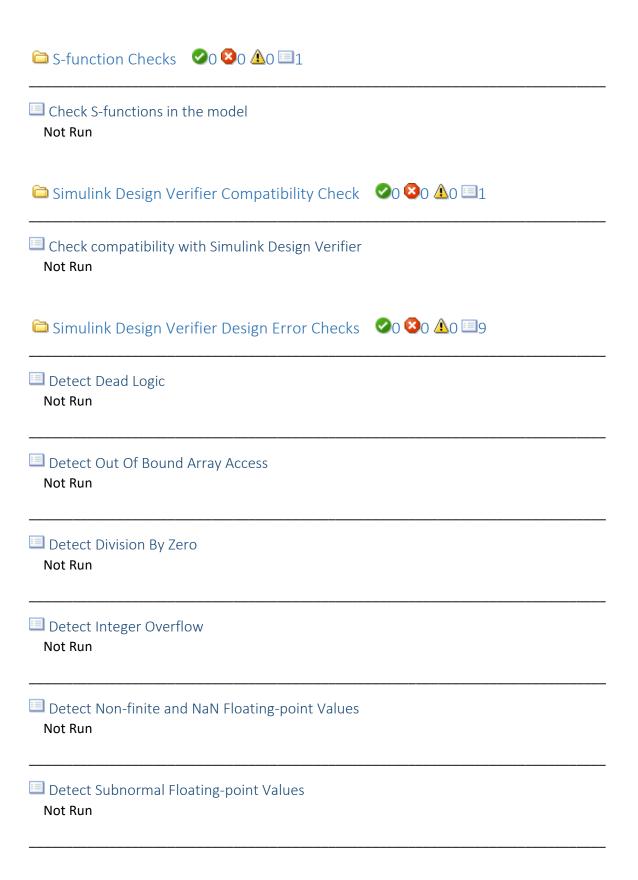
▤ *Simulink block metric*

Not Run

_____

▤ *Subsystem metric*

Not Run

_____

▤ *Library link metric*

Not Run

_____

▤ *Effective lines of MATLAB code metric*

Not Run

_____

▤ *Stateflow chart objects metric*

Not Run

_____

▤ *Lines of code for Stateflow blocks metric*

Not Run

_____

▤ *Subsystem depth metric*

Not Run

📁 Complexity Metrics   ✅0 ❌0 ⚠0 ▤1

_____

▤ *Cyclomatic complexity metric*

Not Run

📁 Readability Metrics   ✅0 ❌0 ⚠0 ▤2

_____

▤ *Nondescriptive block name metric*

Not Run

_____

▥ Data and structure layer separation metric

    Not Run

📁 Modeling Signals and Parameters using Buses ✅0 ❌0 ⚠0 ▤3

▤ Check for optimal bus virtuality

    Not Run

▤ Check structure parameter usage with bus signals

    Not Run

▤ Check bus signals treated as vectors

    Not Run

📁 Code Generation Efficiency ✅0 ❌0 ⚠0 ▤11

▤ Check optimization settings

    Not Run

▤ Identify blocks using one-based indexing

    Not Run

▤ Identify questionable software environment specifications

    Not Run

▤ Identify lookup table blocks that generate expensive out-of-range checking code

    Not Run

▤ Identify questionable code instrumentation (data I/O)

    Not Run
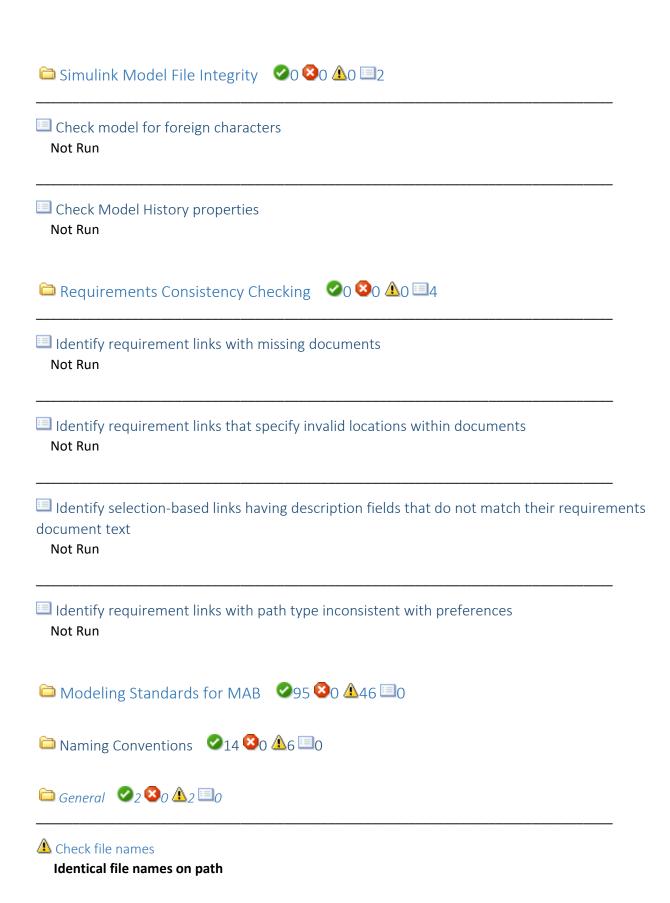
📃 Check output types of logic blocks
Not Run

_____

📃 Check configuration parameters for generation of inefficient saturation code
Not Run

_____

📃 Identify blocks that generate expensive rounding code
Not Run

_____

📃 Identify questionable fixed-point operations
Not Run

_____

📃 Identify blocks that generate expensive fixed-point and saturation code
Not Run

_____

📃 Identify blocks generating inefficient algorithms
Not Run

_____

📁 Modeling Single-Precision Systems ✅0 ❌0 ⚠0 📃1

_____

📃 Identify questionable operations for strict single-precision design
Not Run

_____

📁 Migrating to Simplified Initialization mode ✅0 ❌0 ⚠0 📃4

_____

📃 Check usage of Merge blocks
Not Run

_____

📃 Check usage of Outport blocks
Not Run

_____

161

⊞ Check usage of Discrete-Time Integrator blocks
Not Run

_____

⊞ Check model settings for migration to simplified initialization mode
Not Run

📁 Row-major code generation   ✅0 ❌0 ⚠0 ⊞3
_____

⊞ Identify blocks generating inefficient algorithms
Not Run

_____

⊞ Check for blocks not supported for row-major code generation
Not Run

_____

⊞ Identify TLC S-Functions with unset array layout
Not Run

📁 Model Referencing   ✅0 ❌0 ⚠0 ⊞8
_____

⊞ Check for model reference configuration mismatch
Not Run

_____

⊞ Check diagnostic settings ignored during accelerated model reference simulation
Not Run

_____

⊞ Check code generation identifier formats used for model reference
Not Run

_____

⊞ Check for parameter tunability information ignored for referenced models
Not Run

_____

162

📰 Check for implicit signal resolution

    Not Run

_____

📰 Check bus signals treated as vectors

    Not Run

_____

📰 Check root model Inport block specifications

    Not Run

_____

📰 Check for large number of function arguments from virtual bus across model reference boundary

    Not Run

📁 Managing Library Links And Variants   ✅0 ❌0 ⚠️0 📰4

_____

📰 Identify disabled library links

    Not Run

_____

📰 Identify parameterized library links

    Not Run

_____

📰 Identify unresolved library links

    Not Run

_____

📰 Identify configurable subsystem template blocks having the instances in the model for converting to variant subsystem blocks.

    Not Run

📁 Data Transfer Efficiency   ✅0 ❌0 ⚠️0 📰1

_____

📰 Check Delay, Unit Delay and Zero-Order Hold blocks for rate transition

    Not Run

163

📁 S-function Checks ✅0 ❌0 ⚠0 🗏1

_____

🗏 Check S-functions in the model
   Not Run

📁 Simulink Design Verifier Compatibility Check ✅0 ❌0 ⚠0 🗏1

_____

🗏 Check compatibility with Simulink Design Verifier
   Not Run

📁 Simulink Design Verifier Design Error Checks ✅0 ❌0 ⚠0 🗏9

_____

🗏 Detect Dead Logic
   Not Run

_____

🗏 Detect Out Of Bound Array Access
   Not Run

_____

🗏 Detect Division By Zero
   Not Run

_____

🗏 Detect Integer Overflow
   Not Run

_____

🗏 Detect Non-finite and NaN Floating-point Values
   Not Run

_____

🗏 Detect Subnormal Floating-point Values
   Not Run

_____

164

⊟ Detect Specified Minimum and Maximum Value Violations
    Not Run

_____

⊟ Detect Data Store Access Violations
    Not Run

_____

⊟ Detect Block Input Range Violations
    Not Run

📁 Simulation Accuracy   ✅0 ❌0 ⚠0 ⊟1

_____

⊟ Check for non-continuous signals driving derivative ports
    Not Run

📁 Simulation Runtime Accuracy Diagnostics   ✅0 ❌0 ⚠0 ⊟2

_____

⊟ Runtime diagnostics for S-functions
    Not Run

_____

⊟ Check if Read/Write diagnostics are enabled for Data Store blocks
    Not Run

📁 Managing Data Store Memory Blocks   ✅0 ❌0 ⚠0 ⊟3

_____

⊟ Check Data Store Memory blocks for multitasking, strong typing, and shadowing issues
    Not Run

_____

⊟ Check data store block sample times for modeling errors
    Not Run

_____

⊟ Check for potential ordering issues involving data store access
    Not Run

165

📁 Simulink Model File Integrity   ✅0 ❌0 ⚠️0 📋2

📋 Check model for foreign characters
    Not Run

📋 Check Model History properties
    Not Run

📁 Requirements Consistency Checking   ✅0 ❌0 ⚠️0 📋4

📋 Identify requirement links with missing documents
    Not Run

📋 Identify requirement links that specify invalid locations within documents
    Not Run

📋 Identify selection-based links having description fields that do not match their requirements document text
    Not Run

📋 Identify requirement links with path type inconsistent with preferences
    Not Run

📁 Modeling Standards for MAB   ✅95 ❌0 ⚠️46 📋0

📁 Naming Conventions   ✅14 ❌0 ⚠️6 📋0

📁 *General*   ✅2 ❌0 ⚠️2 📋0

⚠️ Check file names
    Identical file names on path

166

The following files have names which are identical to files present in MATLAB path:

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\SWC_TCF.slx

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\SWC_TCF.slxc

Recommended Action

Consider having unique file names.

_____

Characters allowed for file names

The following files have invalid names:

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF Model Advisor Report.pdf

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF 组件 MIL 测试规范.xlsx

Recommended Action

Consider having only alphanumeric characters and underscores in file name.

_____

⚠ Check folder names

Characters allowed for folder names

Warning

The following folders have invalid names:

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{3b20e685-1899-4cfa-9c05-f530eb9f6760}

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{787f5d95-b258-49c9-8038-8d2e120122f6}

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{820b4466-2956-446f-8110-d7a5941f2e39}

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{8a84d2d8-387c-4f98-9d8f-54a174b7cf24}

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{dc9488be-f2e0-4cf4-b747-f56ce47eecc0}

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{e02a4140-a168-44d9-99b9-107312fa2fcb}

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{e5a311be-6f4c-4248-aa17-9590275e9c8b}

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report\TCF_CurrAgTrqCalc MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report\TCF_Debounce MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report\TCF_IdIqCalc MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report\TCF_IsCalc MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report\TCF_LdSubLqCalc MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report\TCF_PinputCalc MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report\TCF_PlossCalc MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report\TCF_PwrTrqCalc MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report\TCF_TrqBlend MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report\TCF_TrqCalCEnable MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\TCF MIL Test Report\TCF_TrqTube MIL Test Report

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\dig\Code Cache

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\dig\Local Storage

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\dig\Code Cache\js\index-dir

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\dig\blob_storage\b7bd92c7-053e-415b-bf26-7624c9e980b0

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{3b20e685-1899-4cfa-9c05-f530eb9f6760}\Code Cache

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{3b20e685-1899-4cfa-9c05-f530eb9f6760}\Code Cache\js\index-dir

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{3b20e685-1899-4cfa-9c05-f530eb9f6760}\blob_storage\3c77510d-238c-4b4d-97ec-1480e2cd00c0

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{787f5d95-b258-49c9-8038-8d2e120122f6}\Code Cache

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{787f5d95-b258-49c9-8038-8d2e120122f6}\Code Cache\js\index-dir

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{787f5d95-b258-49c9-8038-8d2e120122f6}\blob_storage\81f2fe4a-3a01-490f-880c-bb2fe65109e4

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{820b4466-2956-446f-8110-d7a5941f2e39}\Code Cache

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{820b4466-2956-446f-8110-d7a5941f2e39}\Code Cache\js\index-dir

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\{820b4466-2956-446f-8110-d7a5941f2e39}\blob_storage\4713480a-580a-49ae-97d7-77d880c06e54

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\ TCF_V2\{8a84d2d8-387c-4f98-9d8f-54a174b7cf24}\Code Cache

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\ TCF_V2\{8a84d2d8-387c-4f98-9d8f-54a174b7cf24}\Code Cache\js\index-dir

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\ TCF_V2\{8a84d2d8-387c-4f98-9d8f-54a174b7cf24}\blob_storage\4f5a02b5-f4b5-4f97-b3e8- 355ae3b9c2c4

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\ TCF_V2\{dc9488be-f2e0-4cf4-b747-f56ce47eecc0}\Code Cache

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\ TCF_V2\{dc9488be-f2e0-4cf4-b747-f56ce47eecc0}\Code Cache\js\index-dir

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\ TCF_V2\{dc9488be-f2e0-4cf4-b747-f56ce47eecc0}\blob_storage\109f9986-5504-40da-9f5b- d080aa66da71

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\ TCF_V2\{e02a4140-a168-44d9-99b9-107312fa2fcb}\Code Cache

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\ TCF_V2\{e02a4140-a168-44d9-99b9-107312fa2fcb}\Code Cache\js\index-dir

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\ TCF_V2\{e02a4140-a168-44d9-99b9-107312fa2fcb}\blob_storage\6fec1f3a-d25f-49a3-8cca- bc8a0807e7fa

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\ TCF_V2\{e5a311be-6f4c-4248-aa17-9590275e9c8b}\Code Cache

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\ TCF_V2\{e5a311be-6f4c-4248-aa17-9590275e9c8b}\Code Cache\js\index-dir

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\

TCF_V2\{e5a311be-6f4c-4248-aa17-9590275e9c8b}\blob_storage\395d9e3b-5005-4602-9cdd-d7df6847fa61

∧ Less

Recommended Action

Consider having only alphanumeric characters and underscores in folder name.

_____

Underscore at the beginning

 Warning

The following folders have underscores at the beginning of the folder name:

-
  E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\slprj_jitprj

-
  E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\slprj_sfprj

-
  E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\slprj_sfprj\SWC_TCF_self

-
  E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V2\slprj\ert_sharedutils

 Recommended Action

Consider having alphabetic character at the beginning of the folder name.

_____

Number at the beginning

The following folders have numbers at the beginning of the folder name:

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V
2\{3b20e685-1899-4cfa-9c05-f530eb9f6760}\blob_storage\3c77510d-238c-4b4d-97ec-1480e2cd00c0

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V
2\{787f5d95-b258-49c9-8038-8d2e120122f6}\blob_storage\81f2fe4a-3a01-490f-880c-bb2fe65109e4

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V
2\{820b4466-2956-446f-8110-d7a5941f2e39}\blob_storage\4713480a-580a-49ae-97d7-
77d880c06e54

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V
2\{8a84d2d8-387c-4f98-9d8f-54a174b7cf24}\blob_storage\4f5a02b5-f4b5-4f97-b3e8-355ae3b9c2c4

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V
2\{dc9488be-f2e0-4cf4-b747-f56ce47eecc0}\blob_storage\109f9986-5504-40da-9f5b-d080aa66da71

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V
2\{e02a4140-a168-44d9-99b9-107312fa2fcb}\blob_storage\6fec1f3a-d25f-49a3-8cca-bc8a0807e7fa

- E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\TCF\TCF_V
2\{e5a311be-6f4c-4248-aa17-9590275e9c8b}\blob_storage\395d9e3b-5005-4602-9cdd-
d7df6847fa61

Recommended Action

Consider having alphabetic character at the beginning of the folder name.

_____

 Check length of model file name
  Check length of model file name

Passed

Model name is valid.

_____
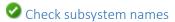
✅ Check length of folder name at every level of model path

The model file name is: SWC_TCF

Passed

Folder names are valid.

📁 _Content_   ✅12 ❌0 ⚠️4 📄0

_____

✅ Check subsystem names

Identify subsystem names with incorrect characters.

Passed

All the subsystem names use correct characters.

_____

✅ Check port block names

Identify Inport or Outport block names with incorrect characters.

Passed

All the Inport or Outport block names use correct characters.

_____

⚠️ Check character usage in block names

Characters allowed for block names

Warning

The following blocks have invalid names:

•

-
-
-
- SWC_TCF/SWC_TCF_Init/Event Listener

  Recommended Action

  Consider having only alphanumeric characters and underscores in block name.

  _____

  Number at the beginning

  The following blocks have numbers at the beginning of the block name:

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/60div2PI

  Recommended Action

  Consider having alphabetic character at the beginning of the block name.

  _____ **Single reserved MATLAB word**

  The following blocks have reserved MATLAB words as the block name:

- SWC_TCF/SWC_TCF_1ms_sys/function

  Recommended Action

175

Consider not having reserved MATLAB word as the block name.

_____

✅ Check length of subsystem names
   Check length of subsystem names

   Passed
   All subsystem names are valid.

_____

✅ Check length of block names
   Check length of block names

   Passed
   All block names are valid.

_____

✅ Check length of Inport and Outport names
   Check length of Inport and Outport names

   Passed
   All Inport and Outport names are valid.

_____

⚠️ Check usable characters for signal names and bus names
   Identify invalid characters in signal and bus names

   Warning

   Invalid characters are used in signal/bus names.

Signal	Name
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc	PhPower

Recommended Action

Consider using different name for signal/bus.

_____

✅ Check usable characters for parameter names
Identify invalid characters in parameter names

Passed
No invalid characters are used in parameter names.

_____

✅ Check length of signal and bus names
Check length of signal and bus names

Passed
All signal and bus names are valid.

_____

✅ Check length of parameter names
Check length of parameter names

Passed
All parameter names are valid.

_____

✅ Check usable characters for Stateflow data names
Identify invalid characters in Stateflow data names.

Passed
No invalid characters are used in Stateflow data names.

_____

✅ Check length of Stateflow data name
Check if the length of Stateflow data names are within limit.

Passed
All Stateflow data names are valid.

_____

⚠ Check duplication of Simulink Data names

    Simulink Data names should be unique across base workspace, model workspace and data dictionary.

jc_0791_b: Check for repeated data names across base workspace and data dictionary

Warning

The following Simulink data names are not unique across base workspace and data dictionary:

Variable Name	Workspace
CAL_TCF_AgTrqTubeCAy_af32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_HiSpdDirStop_f32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_HiTrqDirStop_f32	base workspace SWC_TCF_DataDictionary.sldd

CAL_TCF_IsPwrLosCAx_af32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_LdSubLqCAzGen_af32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_LdSubLqCAzMot_af32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_LdSubLqIdCAx_af32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_LdSubLqIqCAy_af32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_LoSpdDirStop_f32	base workspace SWC_TCF_DataDictionary.sldd

CAL_TCF_LoTrqDirStop_f32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_MotorPole_u8	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_NPwrLosCAy_af32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_Psi_f32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_PwrLossCAz_af32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_PwrTrqSpdCompa_af32	base workspace

	SWC_TCF_DataDictionary.sldd
CAL_TCF_PwrTrqTubeCAy_af32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_SpeedCtlMode_u8	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_TempStrMax_f32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_TempStrMin_f32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_TempStrPlossFact_f32	base workspace SWC_TCF_DataDictionary.sldd

CAL_TCF_TrqCalcMonCountTrh_s16	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_TrqCalcMonDebTrh_s16	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_TrqCalcMonErrRst_b	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_TrqInvalid_s16	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_TrqTubeNCAx_af32	base workspace SWC_TCF_DataDictionary.sldd
CAL_TCF_flgUsePlossCompa_b	base workspace

	SWC_TCF_DataDictionary.sldd
GLB_TCF_BwELect_u8	base workspace SWC_TCF_DataDictionary.sldd
GLB_TCF_BwGene_u8	base workspace SWC_TCF_DataDictionary.sldd
GLB_TCF_CircAge_f32	base workspace SWC_TCF_DataDictionary.sldd
GLB_TCF_DigtValue_u16	base workspace SWC_TCF_DataDictionary.sldd
GLB_TCF_FwELect_u8	base workspace SWC_TCF_DataDictionary.sldd

GLB_TCF_FwGene_u8	base workspace SWC_TCF_DataDictionary.sldd
GLB_TCF_MotorBw_u8	base workspace SWC_TCF_DataDictionary.sldd
GLB_TCF_MotorFw_u8	base workspace SWC_TCF_DataDictionary.sldd
GLB_TCF_MotorStop_u8	base workspace SWC_TCF_DataDictionary.sldd
GLB_TCF_NegvTrq_u8	base workspace SWC_TCF_DataDictionary.sldd
GLB_TCF_PosvTrq_u8	base workspace SWC_TCF_DataDictionary.sldd

GLB_TCF_ZeroTrq_u8	base workspace SWC_TCF_DataDictionary.sldd
Tbl_cos_table	base workspace SWC_TCF_DataDictionary.sldd
Tbl_sin_table	base workspace SWC_TCF_DataDictionary.sldd
VAR_HSPF_StrrTempFlt_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_L2Sampling_DycUMon_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_L2Sampling_DycVMon_f32	base workspace

	SWC_TCF_DataDictionary.sldd
VAR_L2Sampling_DycWMon_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_BlendTrq_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_CurrAgTrq1_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_CurrAgTrqTubeH1_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_CurrAgTrqTubeL1_f32	base workspace SWC_TCF_DataDictionary.sldd

VAR_TCF_Is_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_LdsubLq_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_MotorMode_u8	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_Pinput_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_Ploss_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_PwrTrq1_f32	base workspace

	SWC_TCF_DataDictionary.sldd
VAR_TCF_PwrTrqTubeH1_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_PwrTrqTubeL1_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_TrqCalcErr_b	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_TrqCalcMonRslt_b	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_TrqDir_u8	base workspace SWC_TCF_DataDictionary.sldd

VAR_TCF_idAct_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_iqAct_f32	base workspace SWC_TCF_DataDictionary.sldd
VAR_TCF_nDir_u8	base workspace SWC_TCF_DataDictionary.sldd
boolean	base workspace SWC_TCF_DataDictionary.sldd
float32	base workspace SWC_TCF_DataDictionary.sldd
float64	base workspace SWC_TCF_DataDictionary.sldd

sint16	base workspace SWC_TCF_DataDictionary.sldd
sint32	base workspace SWC_TCF_DataDictionary.sldd
sint64	base workspace SWC_TCF_DataDictionary.sldd
sint8	base workspace SWC_TCF_DataDictionary.sldd
uint16	base workspace SWC_TCF_DataDictionary.sldd
uint32	base workspace

	SWC_TCF_DataDictionary.sldd
uint64	base workspace SWC_TCF_DataDictionary.sldd
uint8	base workspace SWC_TCF_DataDictionary.sldd

∧ Less

Recommended Action

Consider renaming repeated data either in the base workspace or the data dictionary.

_____

⚠ Check unused data in Simulink Model
Check for unused data in Data Dictionary

The following data variables in the data dictionary are unused:

191

Data Objects	Source
boolean	SWC_TCF_DataDictionary.sldd
float32	SWC_TCF_DataDictionary.sldd
float64	SWC_TCF_DataDictionary.sldd
sint16	SWC_TCF_DataDictionary.sldd
sint32	SWC_TCF_DataDictionary.sldd
sint64	SWC_TCF_DataDictionary.sldd
sint8	SWC_TCF_DataDictionary.sldd
uint16	SWC_TCF_DataDictionary.sldd
uint32	SWC_TCF_DataDictionary.sldd
uint64	SWC_TCF_DataDictionary.sldd
uint8	SWC_TCF_DataDictionary.sldd

∧ Less

Recommended Action

Consider removing the unused data variables.

_____

✅ Check for unused data in Stateflow Charts
Checks if the model parameter 'Unused data, events, messages and functions' is not set to 'none'.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Unused data, events, messages and functions (SFUnusedDataAndEventsDiag)	warning	error, warning

---

✅ Check usage of restricted variable names

Checks whether variable names used in MATLAB Function blocks are reserved for C/C++/MATLAB keywords

Passed

No variable names conflict with reserved keywords

📁 Simulink   ✅32 ❌0 ⚠️30 📋0

📁 *Configuration Parameters*   ✅2 ❌0 ⚠️2 📋0

✅ Check Implement logic signals as Boolean data (vs. double)

Identify whether **Implement logic signals as Boolean data (vs. double)** is selected.

Passed

Implement logic signals as Boolean data (vs. double) is selected.

✅ Check Signed Integer Division Rounding mode

jc_0642: Integer rounding mode setting

Identifies blocks with block parameter 'Integer Rounding Mode' set to 'Simplest' when the configuration parameter 'Signed integer division rounds to' is set to 'Undefined'.

Passed

Configuration parameter 'Signed integer division rounds to' is not set to 'Undefined'.

⚠️ Check diagnostic settings for incorrect calculation results

Identify data validity diagnostic settings which detect incorrect calculation results.

193

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values
Warning	Division by singular matrix (CheckMatrixSingularityMsg)	none	error
Warning	Inf or NaN block output (SignalInfNanChecking)	warning	error
Warning	Wrap on overflow (IntegerOverflowMsg)	warning	error
Warning	Saturate on overflow (IntegerSaturationMsg)	warning	error

Recommended Action

Follow the links in the result table to modify the model configuration parameters.

_____

⚠ Check model diagnostic parameters

Identify diagnostic parameters that are set to `none`.

Warning

The following diagnostics parameters are set to `none`:

- Duplicate data store names

Recommended Action

In the Configuration Parameters dialog box, set the above diagnostic parameters to `warning` or `error`.

📁 *Diagram Appearance* ✅6 ❌0 ⚠️11 🗒️0

_____

⚠️ Check for Simulink diagrams using nonstandard display attributes

Identify nonstandard display attributes in Simulink diagrams.

_____

Check format settings

Identify incorrect model-level format options.

Warning

The following format display options are incorrect.

Display Attribute	Recommended Value	Actual Value
Debug > Information Overlays > Nonscalar Signals	on	off
Debug > Information Overlays > Port Data Type	off	on
Debug > Information Overlays > Execution Context	off	on
Debug > Information Overlays > Colors	none	disabled

Recommended Action

Set the format options to the recommended value.

_____ **Check block colors**

Identify blocks using nonstandard colors.

The following blocks use nonstandard colors:

- SWC_TCF/HSPF_StrrTempFlt

- SWC_TCF/L2Com_HiPrecVolt

- SWC_TCF/L2Com_ModeReq

- SWC_TCF/L2Sampling_DycU_Mon

- SWC_TCF/L2Sampling_DycV_Mon

- SWC_TCF/L2Sampling_DycW_Mon

- SWC_TCF/SWC_TCF_1ms

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/Add

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/CAL_TOM_GLB_Pn_u8

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/CAL_TOM_Psi_f32

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/Product

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/Product1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/Product2

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_CurrAgTrq

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_LdsubLq

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_idAct

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_iqAct

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TorqueCalculationFactor"
  title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TorqueCalculationFactor

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/Enable

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/From

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/From1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/Goto

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/Goto1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Add

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Add1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Add2

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Add3

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/CAL_TOM_GLB_CircAge_f32

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/CAL_TOM_GLB_DigtValue_u16"
  title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/CAL_TOM_GLB_DigtValue_u16

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/ClarkTransformCoefficient"
  title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/ClarkTransformCoefficient

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/ClarkTransformCoefficient1"
  title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/ClarkTransformCoefficient1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Divide

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Divide1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From2

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From5

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From6

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Goto2

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Goto3

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/LookupCosTable

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/LookupSinTable

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Product

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Product1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Product2

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Product3

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Product4

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Product5

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/SWD_AgRtr

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/SqrtThree_f32

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TCF_idAct

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TCF_iqAct

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TRSP_iU

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TRSP_iV

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TRSP_iW

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Abs

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Abs1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Constant

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Constant1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/LdSubLqGen_L2

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/LdSubLqMot_L2

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Logical Operator

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Switch

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_LdsubLq

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_MotorMode

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_idAct

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_iqAct

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/SWD_AgRtr

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/SWD_Spd

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TCF_CurrAgTrq

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TCF_TrqDir

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TCF_nDir

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iU

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iV

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iW

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Add

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/BooleanIN

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Boolean_ZERO

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Constant

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Constant1

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Count

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/CountTrh

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Deb

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/DebTrh

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Delay1

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Delay2

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator1

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/OutLock

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Relational Operator

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Rst

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Add

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Constant

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Constant1

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Count

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/CountTrh

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Delay

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Enable

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Relational Operator

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Switch

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Switch

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Switch1

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Switch2

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Unit Delay1

- SWC_TCF/SWC_TCF_1ms_sys/From

- SWC_TCF/SWC_TCF_1ms_sys/From1

- SWC_TCF/SWC_TCF_1ms_sys/From2

- SWC_TCF/SWC_TCF_1ms_sys/Goto

- SWC_TCF/SWC_TCF_1ms_sys/Ground

- SWC_TCF/SWC_TCF_1ms_sys/Ground1

- SWC_TCF/SWC_TCF_1ms_sys/Ground2

- SWC_TCF/SWC_TCF_1ms_sys/Ground3

- SWC_TCF/SWC_TCF_1ms_sys/HSPF_StrrTempFlt

- SWC_TCF/SWC_TCF_1ms_sys/L2Com_HiPrecVolt

- SWC_TCF/SWC_TCF_1ms_sys/L2Com_ModeReq

- SWC_TCF/SWC_TCF_1ms_sys/L2Sampling_DycUMon

- SWC_TCF/SWC_TCF_1ms_sys/L2Sampling_DycVMon

- SWC_TCF/SWC_TCF_1ms_sys/L2Sampling_DycWMon

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/Enable

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/HSPF_StrrTempFlt

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/Add

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/Add1

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/Add2

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/ClarkTransformCoefficient

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/ClarkTransformCoefficient1

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/Divide

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/Product

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/Product1

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/Product2

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/Product3

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/Sqrt

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/SqrtThree_f32

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TCF_Is

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TRSP_iU

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TRSP_iV

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TRSP_iW

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Com_HiPrecVolt

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Sampling_DycU

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Sampling_DycV

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Sampling_DycW

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Add1

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Add2

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Add3

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Add4

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Constant2

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Com_HiPrecVolt

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycUMon

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycVMon

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycWMon

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Product

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Product1

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Product2

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Product3

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Product4

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Product5

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TCF_Pinput

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TRSP_iU

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TRSP_iV

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TRSP_iW

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Abs

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Add

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_TempStrMax_f32

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_TempStrMin_f32

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_TempStrPlossFact_f32"
  title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_TempStrPlossFact_f3
  2

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_flgUsePlossCompa_b

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Constant1

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/HSPF_StrrTempFlt

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/IS_N_Ploss_T

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Product
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Product2
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/SWD_Spd
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Saturation Dynamic
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Switch
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/TCF_Is
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/TCF_Ploss
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/60div2PI
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/Abs
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/Add
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/Divide
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/Product1
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/Product2
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/PwrTrqSpdCompa
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/SWD_Spd
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/TCF_Pinput
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/TCF_Ploss
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/TCF_PwrTrq
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/SWD_Spd
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TCF_PwrTrq
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TRSP_iU
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TRSP_iV
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TRSP_iW
- SWC_TCF/SWC_TCF_1ms_sys/SWD_AgRtr
- SWC_TCF/SWC_TCF_1ms_sys/SWD_Spd
- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion
- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion1

- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion2

- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion3

- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion4

- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion5

- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion6

- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion7

- SWC_TCF/SWC_TCF_1ms_sys/TCF_BlendTrq

- SWC_TCF/SWC_TCF_1ms_sys/TCF_TrqCalcErr

- SWC_TCF/SWC_TCF_1ms_sys/TCF_TrqDir

- SWC_TCF/SWC_TCF_1ms_sys/TCF_nDir

- SWC_TCF/SWC_TCF_1ms_sys/TRSP_iU

- SWC_TCF/SWC_TCF_1ms_sys/TRSP_iV

- SWC_TCF/SWC_TCF_1ms_sys/TRSP_iW

- SWC_TCF/SWC_TCF_1ms_sys/Terminator

- SWC_TCF/SWC_TCF_1ms_sys/Terminator1

- SWC_TCF/SWC_TCF_1ms_sys/Terminator2

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_HiTrqMonDebTrh_s4

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_HiTrqMonDebTrh_s5

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_TrqMonErrRst_b2

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Add

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/BooleanIN

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Boolean_ZERO

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Constant

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Constant1

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/CountTrh

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/DebTrh

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Delay1

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Delay2

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator1

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/OutLock

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Relational Operator

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Rst

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Add

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Constant

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Constant1

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Count

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/CountTrh

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Delay

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Enable

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Relational Operator

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Switch

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Switch

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Switch1

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Switch2

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Unit Delay1

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Enable

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/SWD_Spd

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TCF_BlendTrq

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TCF_CurrAgTrq

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TCF_PwrTrq

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TCF_TrqCalcErr
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_BlendTrq
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_CurrAgTrq_TubeH
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_CurrAgTrq_TubeL
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_PwrTrq_TubeH
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_PwrTrq_TubeL
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_TrqCalcMonRslt
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/1-D Lookup Table
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/Add
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/Add1
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/Add2
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/Add3
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/Cal_PowerTrqTube_T
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/SWD_Spd
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_CurrAgTrq
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_CurrAgTrq_TubeH
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_CurrAgTrq_TubeL
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_PwrTrq
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_PwrTrq_TubeH
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_PwrTrq_TubeL
- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable
- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/CAL_TOM_SpeedCtlMode_u8
- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/Enable
- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/L2Com_ModeReq
- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/Relational Operator2

- SWC_TCF/SWC_TCF_1ms_sys/function

- SWC_TCF/SWC_TCF_Init

- SWC_TCF/SWC_TCF_Init/Event Listener

- SWC_TCF/SWD_AgRtr

- SWC_TCF/SWD_Spd

- SWC_TCF/TCF_BlendTrq

- SWC_TCF/TCF_TrqCalcErr

- SWC_TCF/TCF_TrqDir

- SWC_TCF/TCF_nDir

- SWC_TCF/TRSP_iU

- SWC_TCF/TRSP_iV

- SWC_TCF/TRSP_iW

∧ Less

Recommended Action

Set the block foreground color to black and the background color to white.

_____

Check canvas colors
Identify canvases that are not white.

Passed
All diagrams use a white canvas.

_____

Check diagram zoom

Identify diagrams that do not have zoom factor set to 100 %.

Note: Zoom factors can differ for each instance of a model diagram opened in Simulink Editor

The following diagrams do not have zoom factor set to 100 percent:

- SWC_TCF

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem"
  title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem1"
  title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem1

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem2"
  title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem2

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem3"
  title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action Subsystem3

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable

∧ Less

Recommended Action

For each listed diagram, select **Modeling > Environment > Zoom > Normal View (100%)**.

_____

✅ Check Model font settings
Identify blocks and charts with different font settings from input parameters.

Passed
Font settings of the blocks and charts and input parameters are same.

_____

⚠️ Check whether block names appear below blocks
Identify blocks where the name is not displayed below the block.

Warning

The following blocks have names that do not display below the blocks:

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Delay2

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Unit Delay1

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Delay2

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Unit Delay1

Recommended Action

Change the location such that the block name is below the block.

_____

⚠️ Check the display attributes of block names

Identify whether to display block names.

Check for blocks with hidden names and obvious function

Identify block names that are displayed but can be hidden due to obvious behavior.

Passed

All blocks with obvious behavior have hidden names.

Check for missing block names

Identify block names that are hidden but should be displayed to show a descriptive name.

Passed

All displayed names provide descriptive information.

Check for non-descriptive displayed block names

Identify block names that are displayed but should be hidden due to a lack of a descriptive name.

Warning

The following blocks have a name displayed, however, the name is not descriptive:

-

-

-

210

-
- SWC_TCF/SWC_TCF_Init/Event Listener

  Recommended Action

  Modify the block name to provide descriptive information, or hide the block name by selecting (**Format > Auto Name > Hide Automatic Block Name**).

  _____

  ⚠ Check for nondefault block attributes

  Identify blocks that use and fail to display nondefault values.

  Warning

  The following blocks use and fail to display nondefault values:

Block	Parameter	Expected Value	Actual Value
SWC_TCF/SWC_TCF_1ms_sys/function	SampleTimeType	triggered	periodic
SWC_TCF/SWC_TCF_1ms_sys/function	SampleTime	1	-1
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/LdSubLqGen_L2" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/LdSubLqGen_L2	RndMeth	Floor	Simplest
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/LdSubLqMot_L2"	RndMeth	Floor	Simplest

title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/LdSubLqMot_L2			
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/Merge" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/Merge	InitialOutput	[]	1
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/IS_N_Ploss_T" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/IS_N_Ploss_T	RndMeth	Floor	Simplest
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/PwrTrqSpdCompa" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/PwrTrqSpdCompa	UseLastTableValue	off	on
SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/PwrTrqSpdCompa" title="SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/PwrTrqSpdCompa	RndMeth	Floor	Simplest
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/1-D Lookup Table" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/1-D Lookup Table	RndMeth	Floor	Simplest
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/Cal_PowerTrqTube_T" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/Cal_PowerTrqTube_T	RndMeth	Floor	Simplest

Recommended Action

For the above blocks, display the nondefault value using the Block Annotation pane of the Block Properties dialog box.

_____

⚠ Check Model Description

Identify layers in the model having inconsistent description format.

Warning

Following layers do not have model descriptions:

- SWC_TCF

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg

-

-

-

-

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable

- SWC_TCF/SWC_TCF_Init

∧ Less

Recommended Action

Consider adding model description for all the layers.

213

_____ Identify
layers in the model having inconsistent description format.

Following layers do not have consistent model description format:

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube

∧ Less

Recommended Action

Consider having a consistent format for the model description

Example: If description tags are 'Input:, Description:, and Output:' then format should be as following:

Input: add input information here

Description: add model description here

Output: add output information here

_____

✅ Check if blocks are shaded in the model

Check if blocks are shaded in the model

Passed

Blocks in the model are not shaded.

_____

✅ Check for unconnected ports and signal lines

Identify unconnected block input ports, output ports, and signal lines.

Passed

All lines and ports in the model are connected.

_____

⚠️ Check signal line connections

Check signal intersections

Warning

The following signals intersect with other signals in the diagram:

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube

∧ Less

Recommended Action

Reposition the above listed signals to avoid intersections.

⚠️ Check signal flow in model

Identify subsystems which do not have a signal flow from left to right.

Warning

The signal flow of diagrams in the following subsystems can be improved:

- SWC_TCF

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem

Recommended Action

Ensure that the signal flow in the mentioned subsystems is from left to right.

- All sequential blocks, except the blocks on feedback path,must be placed from left to right.

- All parallel blocks, except the blocks on feedback path, must be placed from top to bottom.

- All blocks should be oriented to the right.

_____

✅ Check usage of tunable parameters in blocks

Identify tunable parameters used to specify expressions, data type conversions, or indexing operations.

Passed

Tunable parameters are not used in the model.

_____

⚠ Check connections between structural subsystems

Identify connections between structural subsystems.

Check direct connections between structural subsystems

Warning

The following structural subsystems do not have direct connections between them:

Subsystem 1	Subsystem 2
SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable	SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc
SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable	
SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

Recommended Action

Consider making at least one direct connection between the listed subsystems in the given direction (Subsystem 1 to Subsystem 2) with the exception of memory, delay and bus creator/selector blocks.

_____

⚠ Check for consistency in model element names

Check if model elements connected to a signal are following consistent naming

Warning

218

The following model elements are not consistent with the connected signal name:

Block Path	Expression
	Naming mismatch with signal name "VAR_TCF_TrqDir_u8"
	Naming mismatch with signal name "VAR_TCF_TrqDir_u8"
	Naming mismatch with signal name "VAR_TCF_nDir_u8"
	Naming mismatch with signal name "VAR_TCF_nDir_u8"
	Naming mismatch with signal name "VAR_TCF_LdsubLq_f32"
	Naming mismatch with signal name "VAR_TCF_LdsubLq_f32"
	Naming mismatch with signal name "VAR_TCF_CurrAgTrq1_f32"
	Naming mismatch with signal name "VAR_TCF_CurrAgTrq1_f32"
	Naming mismatch with signal name "VAR_TCF_CurrAgTrq1_f32"
	Naming mismatch with signal name "VAR_TCF_MotorMode_u8"
	Naming mismatch with signal name "VAR_TCF_MotorMode_u8"
	Naming mismatch with signal name "VAR_TCF_iqAct_f32"
	Naming mismatch with signal name "VAR_TCF_iqAct_f32"
	Naming mismatch with signal name "VAR_TCF_iqAct_f32"
	Naming mismatch with signal name "VAR_TCF_idAct_f32"
	Naming mismatch with signal name "VAR_TCF_idAct_f32"
	Naming mismatch with signal name "VAR_TCF_idAct_f32"
	Naming mismatch with signal name "nabs_antizero"
	Naming mismatch with signal name "VAR_TCF_Is_f32"
	Naming mismatch with signal name "VAR_TCF_Is_f32"
	Naming mismatch with signal name "VAR_TCF_Pinput_f32"
	Naming mismatch with signal name "VAR_TCF_Pinput_f32"
	Naming mismatch with signal name "VAR_TCF_Ploss_f32"

	Naming mismatch with signal name "VAR_TCF_Ploss_f32"
	Naming mismatch with signal name "VAR_TCF_PwrTrq1_f32"
	Naming mismatch with signal name "VAR_TCF_PwrTrq1_f32"
	Naming mismatch with signal name "VAR_TCF_TrqCalcMonRslt_b"
	Naming mismatch with signal name "VAR_TCF_TrqCalcErr_b"
	Naming mismatch with signal name "VAR_TCF_CurrAgTrqTubeL1_f32"
	Naming mismatch with signal name "VAR_TCF_CurrAgTrqTubeL1_f32"
	Naming mismatch with signal name "VAR_TCF_CurrAgTrqTubeH1_f32"
	Naming mismatch with signal name "VAR_TCF_CurrAgTrqTubeH1_f32"
	Naming mismatch with signal name "VAR_TCF_PwrTrqTubeL1_f32"
	Naming mismatch with signal name "VAR_TCF_PwrTrqTubeL1_f32"
	Naming mismatch with signal name "VAR_TCF_PwrTrqTubeH1_f32"
	Naming mismatch with signal name "VAR_TCF_PwrTrqTubeH1_f32"
	Naming mismatch with signal name "VAR_TCF_BlendTrq_f32"
	Naming mismatch with signal name "VAR_TCF_BlendTrq_f32"

∧ Less

Recommended Action

Consider renaming the deviating model elements to match the signal name or to be consistent with Inport/Outport blocks.

_____

⚠ Check trigger signal names
Check names of the origin of the trigger signal and the conditional input block

Warning

The name of the block at the origin of the trigger signal and the conditional input block name at the destination are dissimilar:

- SWC_TCF/SWC_TCF_1ms_sys/function

Recommended Action

The name of the block at the origin of the trigger signal and the conditional input block name at the destination must include the same name.

_____

⚠ Check for mixing basic blocks and subsystems

Identify levels in the model that include basic blocks and subsystems. Each level of a model must be designed with blocks of the same level (for example, only subsystems or only basic blocks).

Warning

The following level(s) in the model include basic blocks and subsystems:

System	Block path
SWC_TCF/SWC_TCF_1ms_sys	SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion
SWC_TCF/SWC_TCF_1ms_sys	SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion1" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion1
SWC_TCF/SWC_TCF_1ms_sys	SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion2" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion2
SWC_TCF/SWC_TCF_1ms_sys	SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion3" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion3

SWC_TCF/SWC_TCF_1ms_sys	SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion4" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion4
SWC_TCF/SWC_TCF_1ms_sys	SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion5" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion5
SWC_TCF/SWC_TCF_1ms_sys	SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion6" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion6
SWC_TCF/SWC_TCF_1ms_sys	SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion7" title="SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion7
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode	SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_MotorBw_u8" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_MotorBw_u8
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode	SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_MotorFw_u8" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_MotorFw_u8
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode	SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_NegvTrq_u8" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_NegvTrq_u8
SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode	SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_PosvTrq_u8" title="SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_PosvTrq_u8
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Add
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Boolean_ZERO" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Boolean_ZERO

SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Constant" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Constant
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Constant1" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Constant1
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Delay1
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Delay2
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator1" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator1
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Relational Operator
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Switch
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Switch1" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Switch1
SWC_TCF/SWC_TCF_1ms_sys/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Switch2" title="SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Switch2
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_HiTrqMonDebTrh_s4" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_HiTrqMonDebTrh_s4

SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_HiTrqMonDebTrh_s5" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_HiTrqMonDebTrh_s5
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_TrqMonErrRst_b2" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_TrqMonErrRst_b2
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Add" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Add
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Boolean_ZERO" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Boolean_ZERO
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Constant" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Constant
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Constant1" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Constant1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer"	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Delay1"

title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Delay1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Delay2" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Delay2
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator1" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Relational Operator" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Relational Operator
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Switch" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Switch
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Switch1" title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Switch1
SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer"	SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Switch2"

title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer	title="SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Switch2

∧ Less

Recommended Action

If possible, replace blocks at the identified level of the model hierarchy with basic blocks. Move nonvirtual blocks into the identified subsystem.

_____

✅ Check for avoiding algebraic loops between subsystems
 jc_0653: Guidelines for avoiding algebraic loops between subsystems.

 Passed
 No delay blocks in feedback loops violate the guidelines for avoiding algebraic loops between subsystems.

_____

✅ Check for prohibited sink blocks
 Identify sink blocks that must be removed prior to code generation.

 Passed
 There are no prohibited blocks in the subsystem.

📁 *Signal*   ✅0 ❌0 ⚠6 🗏0

_____

⚠ Check usage of vector and bus signals
 Check bus signals treated as vectors

 Warning

 The following configuration parameters are set inappropriately:

226

Parameter	Current Value	Recommended Values
Bus signal treated as vector (StrictBusMsg)	WarnOnBusTreatedAsVector	ErrorOnBusTreatedAsVector

Recommended Action

Consider setting the configuration parameters to the recommended value.

_____

⚠ Check signal line labels

Identify blocks that require labeled signals. A subset of source and destination blocks require labeled signals.

Check source block labels

Warning

The following signals have no label:

- SWC_TCF/SWC_TCF_1ms/

- SWC_TCF/SWC_TCF_1ms_sys/L2Com_ModeReq/

- SWC_TCF/SWC_TCF_1ms_sys/L2Sampling_DycUMon/

- SWC_TCF/SWC_TCF_1ms_sys/L2Sampling_DycVMon/

- SWC_TCF/SWC_TCF_1ms_sys/L2Sampling_DycWMon/

- SWC_TCF/SWC_TCF_1ms_sys/TRSP_iU/

- SWC_TCF/SWC_TCF_1ms_sys/TRSP_iV/

- SWC_TCF/SWC_TCF_1ms_sys/TRSP_iW/

- SWC_TCF/SWC_TCF_1ms_sys/L2Com_HiPrecVolt/

- SWC_TCF/SWC_TCF_1ms_sys/HSPF_StrrTempFlt/

- SWC_TCF/SWC_TCF_1ms_sys/SWD_Spd/

- SWC_TCF/SWC_TCF_1ms_sys/SWD_AgRtr/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iU/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iV/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iW/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/SWD_Spd/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/SWD_AgRtr/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_LdsubLq/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_idAct/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_iqAct/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TRSP_iU/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TRSP_iV/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TRSP_iW/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/SWD_AgRtr/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_idAct/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_iqAct/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_MotorMode/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TCF_CurrAgTrq/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/SWD_Spd/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/TCF_TrqDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/TCF_nDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/TCF_CurrAgTrqFlt/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/SWD_Spd/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/BooleanIN/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Rst/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/CountTrh/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/DebTrh/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/CountTrh/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Sampling_DycU/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Sampling_DycV/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Sampling_DycW/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TRSP_iU/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TRSP_iV/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TRSP_iW/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Com_HiPrecVolt/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/HSPF_StrrTempFlt/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/SWD_Spd/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TRSP_iU/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TRSP_iV/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TRSP_iW/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycUMon/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycVMon/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycWMon/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TRSP_iU/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TRSP_iV/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TRSP_iW/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Com_HiPrecVolt/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/TCF_Is/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/HSPF_StrrTempFlt/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/SWD_Spd/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/TCF_Pinput/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/TCF_Ploss/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TCF_PwrTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/SWD_Spd/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TCF_CurrAgTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/BooleanIN/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Rst/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/CountTrh/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/DebTrh/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/CountTrh/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_PwrTrq_TubeH/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_PwrTrq_TubeL/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_CurrAgTrq_TubeH/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_CurrAgTrq_TubeL/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_PwrTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/SWD_Spd/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_CurrAgTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/L2Com_ModeReq/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/From/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/From1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From2/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From5/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From6/

- SWC_TCF/SWC_TCF_1ms_sys/From/

- SWC_TCF/SWC_TCF_1ms_sys/From1/

- SWC_TCF/SWC_TCF_1ms_sys/From2/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem2/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem3/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/CAL_TOM_GLB_Pn_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/CAL_TOM_Psi_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TorqueCalculationFactor/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/CAL_TOM_GLB_CircAge_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/CAL_TOM_GLB_DigtValue_u16/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/ClarkTransformCoefficient/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/ClarkTransformCoefficient1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/SqrtThree_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Constant/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Constant1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_MotorBw_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_MotorFw_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_NegvTrq_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_PosvTrq_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/IfAction_Subsystem/GLB_TOM_FwELect_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/IfAction_Subsystem1/GLB_TOM_BwELect_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/IfAction_Subsystem2/GLB_TOM_FwGene_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/IfAction_Subsystem3/GLB_TOM_BwGene_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/CAL_TOM_HiTrqDirStop_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/CAL_TOM_LoTrqDirStop_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/GLB_TOM_NegvTrq_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/GLB_TOM_PosvTrq_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/GLB_TpcCod_ZeroTrq_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/CAL_TOM_HiSpdDirStop_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/CAL_TOM_LoSpdDirStop_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/GLB_TOM_MotorBw_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/GLB_TOM_MotorFw_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/GLB_TOM_MotorStop_u8/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Boolean_ZERO/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Constant/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Constant1/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Constant/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Constant1/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/ClarkTransformCoefficient/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/ClarkTransformCoefficient1/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/SqrtThree_f32/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Constant2/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_TempStrMax_f32/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_TempStrMin_f32/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_TempStrPlossFact_f32/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_flgUsePlossCompa_b/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Constant1/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/60div2PI/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_HiTrqMonDebTrh_s4/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_HiTrqMonDebTrh_s5/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_TrqMonErrRst_b2/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Boolean_ZERO/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Constant/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Constant1/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Constant/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Constant1/

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/CAL_TOM_SpeedCtlMode_u8/

^ Less

Recommended Action

Add a new or propagated label to the signal line.

_____ Identify blocks that require labeled signals. A subset of source and destination blocks require labeled signals.

Check destination block labels

Warning

The following signals have no label:

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_CurrAgTrq/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TCF_idAct/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TCF_iqAct/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_LdsubLq/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem/Out1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem1/Out1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem2/Out1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem3/Out1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/TCF_MotorMod /

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/TCF_TrqDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/TCF_nDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TCF_MotorMode/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TCF_TrqDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TCF_nDir/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Count/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/OutLock/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Count/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Deb/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TCF_Is/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TCF_Pinput/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/TCF_Ploss/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/TCF_PwrTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Count/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/OutLock/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_BlendTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_TrqCalcMonRslt/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_PwrTrq_TubeH/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_PwrTrq_TubeL/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_CurrAgTrq_TubeH/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_CurrAgTrq_TubeL/

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/Enable/

- SWC_TCF/SWC_TCF_1ms_sys/TCF_BlendTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TCF_TrqCalcErr/

- SWC_TCF/SWC_TCF_1ms_sys/TCF_TrqDir/

- SWC_TCF/SWC_TCF_1ms_sys/TCF_nDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Goto2/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Goto3/

- SWC_TCF/SWC_TCF_1ms_sys/Goto/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/

∧ Less

Recommended Action

Add a new or propagated label to the signal line.

_____

⚠ Check for propagated signal labels
   Identify propagated labels on signal lines.

Warning

The following subsystem-level Inport block signals should propagate signal labels from the parent system:

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc

Recommended Action

Add labels to the input signals.

238

_____ Identify propagated labels on signal lines.

The following signal labels are not propagated. Propagate signals coming from Subsystem blocks.

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

∧ Less

Recommended Action

Add labels to the output signals.

_____

⚠ Check position of signal labels
Check location of signal labels

Warning

The following signals do not have labels located at the origin of the signal line:

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF/SWC_TCF_1ms_sys

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

∧ Less

Recommended Action

241

Consider placing the labels at the origin of the signal line.

_____ **Check overlap of signal labels**

Warning

The following signals have labels which overlap other objects:

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc

Recommended Action

Consider placing the signal label so that it is readable.

_____ **Check position of signal labels**

Warning

The following signals have labels placed at the top of signal line:

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc

Recommended Action

Consider placing the labels underneath the signal lines.

_____

⚠ Check signal line labels

Identify blocks that require labeled signals. A subset of source and destination blocks require labeled signals.

Check source block labels

The following signals have no label:

- SWC_TCF/SWC_TCF_1ms/
- SWC_TCF/SWC_TCF_1ms_sys/L2Com_ModeReq/
- SWC_TCF/SWC_TCF_1ms_sys/L2Sampling_DycUMon/
- SWC_TCF/SWC_TCF_1ms_sys/L2Sampling_DycVMon/
- SWC_TCF/SWC_TCF_1ms_sys/L2Sampling_DycWMon/
- SWC_TCF/SWC_TCF_1ms_sys/TRSP_iU/
- SWC_TCF/SWC_TCF_1ms_sys/TRSP_iV/
- SWC_TCF/SWC_TCF_1ms_sys/TRSP_iW/
- SWC_TCF/SWC_TCF_1ms_sys/L2Com_HiPrecVolt/
- SWC_TCF/SWC_TCF_1ms_sys/HSPF_StrrTempFlt/
- SWC_TCF/SWC_TCF_1ms_sys/SWD_Spd/
- SWC_TCF/SWC_TCF_1ms_sys/SWD_AgRtr/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iU/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iV/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iW/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/SWD_Spd/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/SWD_AgRtr/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_LdsubLq/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_idAct/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_iqAct/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TRSP_iU/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TRSP_iV/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TRSP_iW/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/SWD_AgRtr/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_idAct/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_iqAct/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_MotorMode/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TCF_CurrAgTrq/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/SWD_Spd/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/TCF_TrqDir/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/TCF_nDir/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/TCF_CurrAgTrqFlt/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/SWD_Spd/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/BooleanIN/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Rst/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/CountTrh/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/DebTrh/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/CountTrh/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Sampling_DycU/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Sampling_DycV/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Sampling_DycW/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TRSP_iU/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TRSP_iV/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TRSP_iW/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Com_HiPrecVolt/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/HSPF_StrrTempFlt/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/SWD_Spd/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TRSP_iU/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TRSP_iV/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TRSP_iW/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycUMon/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycVMon/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycWMon/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TRSP_iU/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TRSP_iV/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TRSP_iW/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Com_HiPrecVolt/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/TCF_Is/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/HSPF_StrrTempFlt/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/SWD_Spd/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/TCF_Pinput/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/TCF_Ploss/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TCF_PwrTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/SWD_Spd/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TCF_CurrAgTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/BooleanIN/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Rst/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/CountTrh/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/DebTrh/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/CountTrh/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_PwrTrq_TubeH/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_PwrTrq_TubeL/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_CurrAgTrq_TubeH/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_CurrAgTrq_TubeL/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_PwrTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/SWD_Spd/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_CurrAgTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/L2Com_ModeReq/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/From/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/From1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From2/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From5/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/From6/

- SWC_TCF/SWC_TCF_1ms_sys/From/

- SWC_TCF/SWC_TCF_1ms_sys/From1/

- SWC_TCF/SWC_TCF_1ms_sys/From2/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem2/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem3/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/CAL_TOM_GLB_Pn_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/CAL_TOM_Psi_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TorqueCalculationFactor/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/CAL_TOM_GLB_CircAge_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/CAL_TOM_GLB_DigtValue_u16/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/ClarkTransformCoefficient/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/ClarkTransformCoefficient1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/SqrtThree_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Constant/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Constant1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_MotorBw_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_MotorFw_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_NegvTrq_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/GLB_TOM_PosvTrq_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem/GLB_TOM_FwELect_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem1/GLB_TOM_BwELect_u8/

247

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem2/GLB_TOM_FwGene_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem3/GLB_TOM_BwGene_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/CAL_TOM_HiTrqDirStop_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/CAL_TOM_LoTrqDirStop_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/GLB_TOM_NegvTrq_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/GLB_TOM_PosvTrq_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/GLB_TpcCod_ZeroTrq_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/CAL_TOM_HiSpdDirStop_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/CAL_TOM_LoSpdDirStop_f32/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/GLB_TOM_MotorBw_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/GLB_TOM_MotorFw_u8/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/GLB_TOM_MotorStop_u8/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Boolean_ZERO/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Constant/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Constant1/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Constant/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Constant1/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/ClarkTransformCoefficient/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/ClarkTransformCoefficient1/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/SqrtThree_f32/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Constant2/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_TempStrMax_f32/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_TempStrMin_f32/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_TempStrPlossFact_f32/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/CAL_TOM_flgUsePlossCompa_b/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/Constant1/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/60div2PI/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_HiTrqMonDebTrh_s4/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_HiTrqMonDebTrh_s5/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/CAL_TOM_TrqMonErrRst_b2/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Boolean_ZERO/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Constant/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Constant1/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Constant/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Constant1/
- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/CAL_TOM_SpeedCtlMode_u8/

∧ Less

Recommended Action

Add a new or propagated label to the signal line.

_____ Identify
blocks that require labeled signals. A subset of source and destination blocks require labeled signals.

Check destination block labels

The following signals have no label:

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_CurrAgTrq/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TCF_idAct/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TCF_iqAct/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_LdsubLq/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem/Out1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem1/Out1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem2/Out1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/If Action_Subsystem3/Out1/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/TCF_MotorMod /

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/TCF_TrqDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/TCF_nDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TCF_MotorMode/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TCF_TrqDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TCF_nDir/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Count/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/OutLock/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Count/

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Deb/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/TCF_Is/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/TCF_Pinput/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/TCF_Ploss/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/TCF_PwrTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Count/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/OutLock/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_BlendTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_TrqCalcMonRslt/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_PwrTrq_TubeH/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_PwrTrq_TubeL/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_CurrAgTrq_TubeH/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/TCF_CurrAgTrq_TubeL/

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/Enable/

- SWC_TCF/SWC_TCF_1ms_sys/TCF_BlendTrq/

- SWC_TCF/SWC_TCF_1ms_sys/TCF_TrqCalcErr/

- SWC_TCF/SWC_TCF_1ms_sys/TCF_TrqDir/

- SWC_TCF/SWC_TCF_1ms_sys/TCF_nDir/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Goto2/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Goto3/

- SWC_TCF/SWC_TCF_1ms_sys/Goto/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TrqDir/
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/nDir/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/
- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector2/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/
- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqTube/
- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/

∧ Less

Recommended Action

Add a new or propagated label to the signal line.

_____

⚠ Check for propagated signal labels

Identify propagated labels on signal lines.

The following subsystem-level Inport block signals should propagate signal labels from the parent system:

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PwrTrqCalc

Recommended Action

Add labels to the input signals.

_____ Identify propagated labels on signal lines.

The following signal labels are not propagated. Propagate signals coming from Subsystem blocks.

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc

∧ Less

Recommended Action

Add labels to the output signals.

📁 *Block Consistency* ✅*4* ❌*0* ⚠️*2* 🗒️*0*

_____

✅ Check Indexing Mode
Identify blocks and charts with inconsistent Indexing mode.

Passed
No inconsistent Indexing mode used in the model.

_____

✅ Check block orientation
Identify blocks which are rotated or reversed

Passed

No blocks found with rotated or reversed orientation

_____

⚠ Check if tunable block parameters are defined as named constants

Check if tunable block parameters are defined as named constants

Warning

The following tunable block parameters are not defined as named constants.

Block	Violations
	Value : 1.5
	Value : 2
	Value : 0.33333
	Value : 1.732051
	Value : 2
	Value : 0.33333
	Value : 1.732051
	Value : 0.5
	Value : 9.55

Recommended Action

Consider changing tunable block parameter literal values to named constants.

_____

✅ Check for sample time setting

Check if sample time property of a block is set to -1 (inherited).

Passed

All permitted blocks have sample time set to -1 (inherited).

_____

✅ Check usage of fixed-point data type with non-zero bias

jc_0643: Fixed-point setting
Identify blocks with a fixed-point data type whose bias is not zero.

Passed

No blocks found with the Data Type Assistant mode set to "Fixed point" and a bias value other than zero

_____

⚠️ Check type setting by data objects

jc_0644: Identify blocks that violate signal data type setting if signal objects are used.

Warning

The following blocks violate signal data type setting if signal objects are used.

- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion4

- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion5

- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion6

- SWC_TCF/SWC_TCF_1ms_sys/Signal Conversion7

Recommended Action

Set the output data type of the blocks either to "auto" or "Inherit via back propagation". This check excludes Data Type Conversion block, type setting by fixdt, double and boolean data types, and reusable internal part of function (treat as atomic unit).

📁 *Conditional Subsystem relations* ✅4 ❌0 ⚠️1 📄0

_____

⚠ Check position of conditional blocks and iterator blocks

Block layout in conditional subsystem

The following conditional blocks are not located at the top of the subsystem diagram:

- SWC_TCF/SWC_TCF_1ms_sys/function

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/Enable

Recommended Action

Reposition the conditional blocks listed above to the top of the subsystem diagram.

_____

✅ Check undefined initial output for conditional subsystems

Check undefined initial output for Outports/Merge blocks in conditional subsystems

Passed

The initial output setting for all Conditional Subsystems are valid.

_____

✅ Check usage of Merge block

jc_0659: Usage restrictions of signal lines inputted to Merge block
There must not be any block between a Conditional Subsystem block and a Merge block.

Passed

No blocks found between the Conditional Subsystem block and the Merge block.

_____

✅ Check logical expressions in If blocks

Checks If blocks for complex usage of primary expressions within a logical expression

Passed

Logical expressions inside If blocks are simple

_____

✅ Check default/else case in Switch Case blocks and If blocks

Check if default/else case in Switch Case blocks and If blocks are set to 'on'

Passed

Conditional Control blocks are valid.

📁 *Operation Blocks* ✅8 ❌0 ⚠6 📄0

_____

⚠ Check fundamental logical and numerical operations

Check input data types of blocks meant for numerical operations

Warning

The following numerical operation blocks have boolean data type as input:

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Add

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Add

Recommended Action

Consider having non-boolean inputs for the numerical operation blocks.

_____

⚠ Check usage of Sum blocks

Check number of inputs for Sum block

Warning

Following Sum blocks have more than two inputs:

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Add

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/Add

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/Add4

259

Recommended Action

Set Sum block to have no more than two inputs.

_____ **Check first input of Sum block**

Warning

Following Sum blocks don't have '+' sign as first input and are not part of a feedback loop:

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Add3

Recommended Action

Set first input to Sum block to '+' sign.

_____

⚠ Check operator order of Product blocks

Check number of inputs to Product blocks

Warning

The following Product blocks have invalid number of inputs:

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/Product1
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/Product2
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Divide
- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Divide1
- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/IsCalc/Divide

Recommended Action

Consider having not more than two inputs for the Product blocks.

_____

⚠ Check signs of input signals in product blocks
   jc_0611: Input signal sign during product block division

Identify blocks that perform division whose inputs have different sign bit.

Warning

The following product blocks have input signals with different sign bits.

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/Divide1

Recommended Action

Update the input signal sign bit so that the data types are the same for all input signals.

_____

✅ Check for parentheses in Fcn block expressions
   jc_0622: Guideline for using the Fcn block

Passed
All Fcn blocks use parentheses to mark operator precedence.

_____

✅ Check icon shape of Logical Operator blocks
   Icon shape of Logical Operator blocks

Passed
All Logical Operator blocks have consistent icon shape.

_____

✅ Check usage of Relational Operator blocks
   Identify Relational Operator blocks that connect to constants with the first (upper) input value.

261

Passed

All Relational Operator blocks with constant input values are configured correct.

_____

✅ Comparing floating point types in Simulink

jc_0800: Comparing floating point types in Simulink
Equivalence comparison should not be used for floating point numbers.

Passed

No Equivalence comparison done on floating point numbers.

_____

⚠️ Check usage of Lookup Tables

jc_0626: Guideline for using the Lookup Table system block

Checks for the recommended parameter settings in Lookup Tables to prevent unexpected results.

Warning

The following Lookup Tables violate the guideline:

Block	Parameter	Current Value	Recommended Values
	UseLastTableValue	off	on
	UseLastTableValue	off	on
	ExtrapMethod	Linear	Clip
	UseLastTableValue	off	on
	ExtrapMethod	Linear	Clip
	UseLastTableValue	off	on
	ExtrapMethod	Linear	Clip
	UseLastTableValue	off	on

Recommended Action

Consider changing the above mentioned block parameters with the recommended values.

_____

✅ Check usage of Memory and Unit Delay blocks
Identify Memory blocks not using a continuous sample time

Passed

No Memory blocks found with inappropriate sample time

_____

Identify Unit Delay blocks with non-discrete sample time

Passed

No Unit Delay blocks found with non-discrete sample time

_____

✅ Check for cascaded Unit Delay blocks
Identify cascaded and tapped pattern of Unit Delay blocks.

Passed

No cascaded Unit Delay blocks found that can be changed to Tapped Delay/Delay block.

_____

✅ Check usage of Discrete-Time Integrator block
jc_0627: Identify Discrete-Time Integrator blocks that violate saturation limit settings

Passed

No Discrete-Time Integrator blocks found that violate JMAAB guideline jc_0627

_____

✅ Check usage of the Saturation blocks
jc_0628: Identify the Saturation and Saturation Dynamic blocks that perform type casting.

Passed

No Saturation and/or Saturation Dynamic blocks perform type casting

_____

⚠️ Check output data type of operation blocks
jc_0651: Guideline for implementing a type conversion.

263

Following operation blocks explicitly specify output data type:

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/Enable

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Logical Operator

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/Relational Operator1

-

-

-

-

-

-

-

-

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/BooleanIN

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Compare To Zero

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/CountTrh

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/DebTrh

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Logical Operator1

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/OutLock

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Relational Operator

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Rst

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Enable

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Relational Operator

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/Enable

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Compare To Zero

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Logical Operator1

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Relational Operator

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Enable

-

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Enable

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/Relational Operator2

∧ Less

Recommended Action

Instead of explicitly specifying output data type on operation blocks, use 'Data Type Conversion' block when changing the data type of the block output signal.

📁 *Other blocks*   ✅8 ❌0 ⚠️2 🗐0

_____

⚠️ Check position of Inport and Outport blocks
Check positions of Inport blocks

The following Inport blocks are not placed to left side of the diagram:

- SWC_TCF/SWC_TCF_1ms

- SWC_TCF/SWC_TCF_1ms_sys/SWD_AgRtr

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iU

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iV

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/TRSP_iW

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/SWD_AgRtr

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_LdsubLq

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_idAct

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/CurrAgTrqCalc/TCF_iqAct

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/TRSP_iU

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/IdIqCalc/SWD_AgRtr

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_idAct

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_iqAct

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/LdsubLqCalc/TCF_MotorMode

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/TCF_TrqDir

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/MotorMode/TCF_nDir

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/CountTrh

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Sampling_DycU

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/L2Sampling_DycW

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TRSP_iU

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/TRSP_iW

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/SWD_Spd

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycUMon

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycVMon

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Sampling_DycWMon

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PinputCalc/L2Com_HiPrecVolt

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/TCF_Is

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/HSPF_StrrTempFlt

- SWC_TCF/SWC_TCF_1ms_sys/PwrTrqCalcProc/PlossCalc/SWD_Spd

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TCF_PwrTrq

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/CountTrh

- SWC_TCF/SWC_TCF_1ms_sys/TrqCalcEnable/L2Com_ModeReq

∧ Less

Recommended Action

Move the Inport blocks identified to the left of all other blocks in the diagram.

It is acceptable to move the Inport block to the right only to prevent signal crossings.

_____ **Check positions of Outport blocks**

The following Outport blocks are not placed to right side of the diagram:

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TCF_TrqDir

- SWC_TCF/SWC_TCF_1ms_sys/CurrAgTrqCalcProc/MotorModeJdg/TCF_nDir

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TCF_BlendTrq

- SWC_TCF/SWC_TCF_1ms_sys/TCF_TrqDir

- SWC_TCF/SWC_TCF_1ms_sys/TCF_nDir

Recommended Action

Move the Outport blocks identified to the right of all other blocks in the diagram.

It is acceptable to move the Outport block to the left only to prevent signal crossings.

_____

✅ Check display for port blocks

Identify Inport and Outport blocks that do not specify `Port number` for the **Icon display** block parameter.

267

Passed

All port blocks display the port number.

_____

✅ Check scope of From and Goto blocks

Identify incorrect scoping of From and Goto blocks. For signal flows, From and Goto blocks must use local scope. Control flow can use global scope.

Passed

All From and Goto blocks are used correctly.

_____

✅ Check for usage of Data Store Memory blocks

Identify the usage of Data Store Memory blocks.

Passed

Usage of Data Store Memory blocks is correct.

_____

⚠️ Check usage of Switch blocks

Identify Switch blocks that do not use Boolean inputs for the switch condition (input 2), and do not use $u2 \sim= 0$ for the **Criteria for passing first input** block parameter.

_____

Check Switch block parameters

Identify Switch blocks with the parameter **Criteria for passing first input** not set to $u2 \sim= 0$.

Warning

The block parameter **Criteria for passing first input** is not set to $u2 \sim= 0$ for the following blocks:

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Subsystem/Switch

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Switch

- SWC_TCF/SWC_TCF_1ms_sys/Debouncer/Switch2

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Subsystem/Switch

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Switch

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/Debouncer/Switch2

Recommended Action

Set the block parameter **Criteria for passing first input** to `u2 ~= 0`. This might require reworking the logic associated with the Switch block.

_____

Check for Boolean switch condition
Identify blocks that do not use Boolean signal switch conditions (input 2).

Passed
The switch condition is a Boolean signal.

_____

✅ Check input and output datatype for Switch blocks
jc_0650: Identify Switch blocks with mismatched input and output data types

Passed
No Switch blocks found with mismatched input and output data types

_____

✅ Check settings for data ports in Multiport Switch blocks
Identify Multiport Switch blocks that violate data port settings.

Passed
No Multiport Switch blocks found with inappropriate data port settings.

_____

✅ Check for missing ports in Variant Subsystems
Check for number of inputs/outputs to a Variant Subsystem.

Passed
No Variant Subsystems found having different number of inputs/outputs in the Variant Subsystem choices.

_____

✅ Check use of default variants

na_0036: Default variant

Identify variant subsystems that do not use default variants.

Passed

All variant subsystems in the model use default variants

_____

✅ Check use of single variable variant conditionals

Identify variant subsystems which use multi-variable compound conditions

Passed

No variant subsystems with multiple variable compound conditions found

📁 Stateflow   ✅41 ❌0 ⚠️10 📄0

📁 *Block/Data/Events*   ✅6 ❌0 ⚠️1 📄0

_____

✅ Check for Strong Data Typing with Simulink I/O

Check whether labeled input and output signals are strongly typed.

Passed

No Stateflow charts have **Use Strong Data Typing with Simulink I/O** cleared.

_____

⚠️ Check for names of Stateflow ports and associated signals

Identify mismatches between names of Stateflow ports and the associated signals.

Warning

The following signals have names that differ from those of the corresponding Stateflow ports:

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_PwrTrq_TubeH/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_PwrTrq_TubeL/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_CurrAgTrq_TubeH/

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TCF_CurrAgTrq_TubeL/

Recommended Action

Check whether the ports are connected properly and, if not, correct the connections. Change the names of the signals or the Stateflow ports so that the names match.

_____

✅ Check execution timing for default transition path
'Execute (enter) Chart At Initialization' should be set to OFF.

Passed
All Stateflow Charts pass the check.

_____

✅ Check definition of Stateflow data
Identify the Scope value set on Stateflow data defined at machine level.

Passed
All Stateflow data at machine level has been defined as per guideline.

_____

✅ Check usable number for first index
Identify usage of first index of Stateflow data.

Passed
All Stateflow data first index values are uniform.

_____

✅ Check scope of data in parallel states
jc_0722: Guidelines for setting local variables in parallel states
The scope of local variables should be restricted to one parallel state unless it is being used by other parallel states.

Passed
No Stateflow States were found.

_____

✅ Check definition of Stateflow events
Stateflow events should be defined at the smallest possible scope of usage.

Passed
All Stateflow events are defined at their smallest scope.

📁 *Diagram* ✅*11* ❌*0* ⚠️*3* 🗒️*0*

_____

✅ Check for unconnected objects in Stateflow Charts

Identify dangling transitions and unconnected Stateflow States and Junctions in Stateflow Charts.

Passed

No unconnected transitions, states or junctions found in Stateflow Charts.

_____

✅ Check usage of exclusive and default states in state machines

Identify Stateflow charts and substates that incorrectly use or define exclusive and default states.

_____

Check Stateflow charts for exclusive states

Identify Stateflow charts that have singular exclusive (OR) states.

Passed

The Stateflow charts do not have singular exclusive (OR) states.

_____

Check Stateflow charts for undefined default states

Identify Stateflow charts that do not define default states.

Passed

Each Stateflow chart defines a default state.

_____

Check for multiple states assigned as the default state

At the root level in the Stateflow hierarchy only one state should be assigned as the default.

Passed

The root level of the chart has only one default state assigned.

_____

Check for substates with singular OR states

States configured as OR should always be part of a group of states.

Passed

No singular OR states were detected.

_____

Check for substates without default states defined

At every level in the Stateflow hierarchy a default state should be assigned.

Passed

All substates have default states assigned.

_____

Check for substates with multiple default states defined

At every level in the Stateflow hierarchy only one state should be assigned as the default.

Passed

All levels of the chart have only one default state assigned.

_____

✅ Check for parallel Stateflow state used for grouping

jc_0721: Guidelines for using parallel states
Identify parallel Stateflow States used for grouping.

Passed

All Stateflow Charts pass the check.

_____

⚠️ Check Stateflow transition appearance

Check orientation of transition lines

Warning

The following transitions are not vertical or horizontal and/or diagonal for flow chart loops:

-

Recommended Action

Consider remodeling using either horizontal or vertical transitions only and diagonal transitions for flow chart loops.

_____

✅ Check default transition placement in Stateflow charts

jc_0531: Placement of default transition.

Passed

273

No Stateflow transitions and states found that violate the guidelines for default transition placement in Stateflow charts.

_____

✅ Check usage of transitions to external states

Identify transitions ending on external child states.

Passed

No direct transitions found from external state to child state.

_____

✅ Check for unexpected backtracking in state transitions

Identify configuration parameter settings which identify unexpected backtracking in state transitions.

Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	Unexpected backtracking (SFUnexpectedBacktrackingDiag)	error	error

_____

✅ Check starting point of internal transition in Stateflow

jc_0760: In all state charts and flow charts, internal transitions from state boundaries must start from the left edge of the state.

Passed

No Stateflow transitions found that violate the guidelines for starting point of internal transition in Stateflow.

_____

✅ Check usage of internal transitions in Stateflow states

Identify Stateflow states using multiple internal transitions.

Passed

No Stateflow states found with multiple internal transitions

_____

✅ Check prohibited combination of state action and flow chart

jc_0762: State actions within states and flow chart statements should not be used in combination.

Passed

No Stateflow states found that combine state action and flow chart.

_____

⚠️ Check transition orientations in flow charts

Identify transitions in Stateflow flow charts that are drawn incorrectly.

_____

Check for conditions drawn horizontally

Condition expressions should be drawn on the horizontal segments of flow charts.

Warning

The following transitions have condition expressions that are not horizontal:

-
-
-
-
-
-
-
-

275

-
-

Recommended Action

Draw condition transitions horizontally.

_____

Check for action transitions drawn vertically

Transitions with condition actions should be drawn on the vertical segments of flow charts.

Passed

All transitions with condition actions were drawn vertically.

_____

Check for transition actions in flow chart

Transition actions should not be used in flow charts.

Passed

No transition actions are used in flow charts.

_____

Check for junctions for default transitions

All Junctions in a flow chart should have a default exit transition.

Warning

The following Junctions do not have a default exit transition:

- SWC_TCF/SWC_TCF_1ms_sys/TrqBlendProc/TrqBlend/TubeSelector3/junction(#163)

Recommended Action

Consider reorganizing the flow chart to add a default transition.

276

_____

Check for transitions that combine condition and action

Flow charts should not combine condition evaluations and action expressions in a single transition.

Passed

No combined expressions were found in the chart.

_____

✅ Check usage of unconditional transitions in flow charts

Identify unconditional transitions in flow charts.

Passed

All unconditional transitions adhere to the guideline.

_____

⚠️ Check terminal junctions in Stateflow

Check for usage of only one terminal junction

Warning

The following Stateflow containers have more than one terminal junction:

Stateflow container	Terminal junctions

Recommended Action

Consider using only one terminal junction.

277

_____

✅ Check usage of Stateflow comments

   Identify comments that are nested or contain newline(s) in the middle in Stateflow for action language 'C'.

   Passed

   No comments found that are either nested or contain newline(s) in the middle.

📁 *Condition Transition/Action*   ✅13 ❌0 ⚠️3 🗒️0

_____

✅ Check Stateflow chart action language

   Check if the action language of Stateflow charts is set to 'C'.

   Passed

   All Stateflow Charts have action language set to 'C'.

_____

⚠️ Check usage of numeric literals in Stateflow

   Check for numeric literals in Stateflow

   Warning

   The following expressions contain numeric literals:

Block Path	Expression
	(CH1H+CH1L)/2
	(CH2H+CH2L)/2
	TubeErr=1;
	(CH2L+CH1H)/2
	(CH1L+CH2H)/2

Recommended Action

Consider remodeling to use named parameters and constants instead of numeric literals.

_____

✅ Check for pointers in Stateflow charts

Identify pointer operations on custom code variables.
Note: This check applies only to Stateflow charts that use C as the action language.

Passed

No pointer operations were found.

_____

✅ Check for usage of events and broadcasting events in Stateflow charts

Identify undirected event broadcasts in Stateflow

Passed

No instances of undirected event broadcast were found.

_____

✅ Check order of state action types

Identify out of order state action types in Stateflow states.

Passed

No Stateflow states found with out of order state action types

_____

✅ Check repetition of Action types

jc_0734: Number of state action types
Identifies repeated action types in a Stateflow State.

Passed

No Stateflow States were found.

_____

✅ Check if state action type 'exit' is used in the model

Check if state action type 'exit' is used in the model.

Passed

State action type 'exit' is not used in the model.

279

_____

✅ Check updates to variables used in state transition conditions

jc_0741: Variables used in state transition conditions must not perform an update by "during" state action type.

Passed

No Stateflow states found that violate the guidelines for updating the variables used in state transition conditions.

_____

✅ Check usage of transition conditions in Stateflow transitions

jc_0772: Identify unconditional Stateflow transitions with higher priority than conditional transitions

Passed

No unconditional Stateflow transitions found with higher priority than conditional transitions

_____

✅ Check condition actions and transition actions in Stateflow

Identify usage of transition actions in Stateflow.

Passed

No Stateflow charts have transition actions.

_____

✅ Check for MATLAB expressions in Stateflow blocks

Identify MATLAB expressions that are not suitable for code generation in Stateflow blocks.

Passed

No Stateflow objects found using MATLAB expressions unsuitable for code generation.

_____

✅ Check usage of floating-point expressions in Stateflow charts

Identify equal to operations (==) in expressions where at least one side of the expression is a floating-point variable or constant.

Passed

No equal to operations in expressions where at least one side of the expression is a floating-point variable or constant were found.

_____

✅ Check Stateflow operators

Identify the usage of operators in Stateflow.

Passed

No Stateflow blocks found with incorrect operator usage.

_____

⚠ Check prohibited comparison operation of logical type signals

Identify boolean variables in Stateflow charts using comparison operations.

Warning

The following stateflow objects have operations comparing Boolean variables with numbers of logical values:

-
-
-
-
-

Recommended Action

Boolean variables must use comparison operations of logical type.

_____

✓ Check usage of unary minus operations in Stateflow charts

Identify unary minus operations applied to unsigned integers in Stateflow objects.

Passed

No unary minus operations applied to unsigned integers in Stateflow objects were found.

_____

⚠ Check for implicit type casting in Stateflow

Operations performed between variables must be of the same data type.

Warning

281

The following operations and/or function calls have data type mismatch:

Block Path	Expression
	{TubeOutCAL_TCF_TrqInvalid_s16;}
	{TubeOutCAL_TCF_TrqInvalid_s16;}
	{TubeOut(CH1H+CH1L)/2;}
	{TubeOut(CH2H+CH2L)/2;}
	{TubeErr1;}
	{TubeOut(CH2L+CH1H)/2;}
	{TubeOut(CH1L+CH2H)/2;}
	{TubeErr0;}
	{TubeErr0;}

Recommended Action

All operations (substitution, comparison, arithmetic, etc.) must be made only between variables of the same data type. Actual arguments and formal arguments in function calls must be of the same data type. If data types are different, the variables need to be explicitly type cast to matching data types.

📁 *Label Description* ✅7 ❌0 ⚠️3 🗒️0
_____

✅ Check uniqueness of Stateflow State and Data names
 jc_0732: Distinction between state name and data item name
 Identify Stateflow State and Stateflow Data that have identical names in a given chart.

Passed
All Stateflow charts pass the check.

✅ Check uniqueness of State names

jc_0730: Independence of state name in charts
Identifies identical State names within a Stateflow Chart.

Passed

All Stateflow charts pass the check.

✅ Check usage of State names

jc_0731: Slash (/) in the state name
Identify state names with '/' at its end.

Passed

No Stateflow states were found.

✅ Check entry formatting in State blocks in Stateflow charts

Identify missing line breaks between entry action (en), during action (du), and exit action (ex) entries in states. Identify missing line breaks after semicolons (;) in statements.

Passed

All state entries found are correctly formatted.

✅ Check indentation of code in Stateflow states

Identify non-uniform indentation in Stateflow blocks.

Passed

All Stateflow blocks have uniform indentation.

✅ Check for usage of text inside states

Identify Stateflow states with text exceeding the boundary of the state

Passed

No Stateflow states found with text exceeding the boundary of the state.

⚠️ Check position of label string in Stateflow transition

Transition labels should be placed near the point of origin of the transition

283

Warning

The following transitions do not have label string placed near the origin of the transition:

-
-
-
-
-
-
-
-
-
-
-
-
-

∧ Less

Recommended Action

Consider placing the label string near the point of origin of the transition.

_____

✅ Check position of comments in transition labels
Identify comments in transition labels that are not positioned uniformly.

Passed
Comments in transition labels are positioned uniformly.

_____

⚠ Check usage of parentheses in Stateflow transitions

　jc_0752: Start new line before and after parentheses for condition actions in Stateflow transitions.

Warning

Following Stateflow Transitions violate the requirement for new line for condition actions:

-
-
-
-
-
-
-
-
-

Recommended Action

Start new line before and after parentheses for condition actions in Stateflow Transitions.

_____

⚠ Check for comments in unconditional transitions

Check for comments in empty unconditional transitions

Warning

Following unconditional transitions without action statements do not have comments:

-
-

-

Recommended Action

Consider adding a comment explaining the unconditional transition.

📁 *Miscellaneous* ✅4 ❌0 ⚠️0 🗒️0

✅ Check return value assignments in Stateflow graphical functions
Identify graphical functions with multiple assignments of return values in Stateflow charts.

Passed
No multiple assignments of return values were found.

✅ Check uniqueness of Stateflow State and Data names
jc_0732: Distinction between state name and data item name
Identify Stateflow State and Stateflow Data that have identical names in a given chart.

Passed
All Stateflow charts pass the check.

✅ Check usage of Simulink functions in Stateflow
Usage of Simulink Functions in Stateflow.

Passed
All Simulink Functions in Stateflow are defined according to the guideline.

✅ Check use of Simulink in Stateflow charts
na_0039: Limitation on Simulink functions in Chart blocks
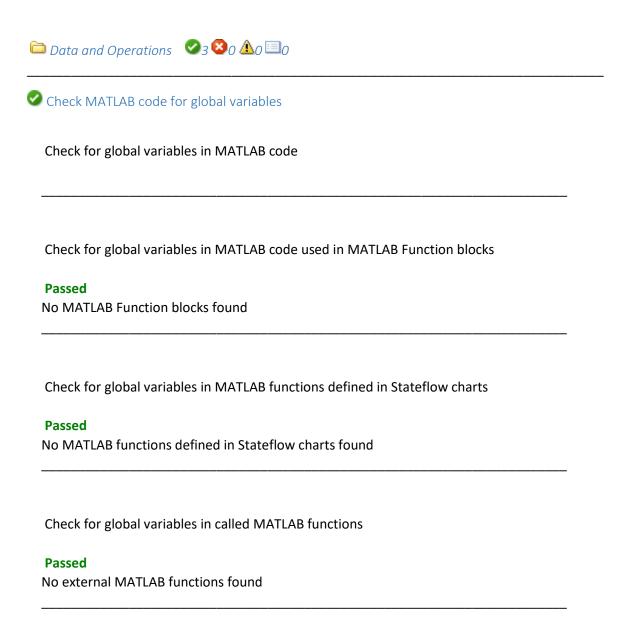
Check use of Stateflow charts nested inside Simulink functions used in Stateflow.

Passed
No Stateflow charts found nested inside Simulink functions used in Stateflow.

📁 MATLAB ✅8 ❌0 ⚠️0 🗒️0

📁 *Data and Operations* ✅3 ❌0 ⚠0 📋0

_____

✅ Check MATLAB code for global variables

Check for global variables in MATLAB code

_____

Check for global variables in MATLAB code used in MATLAB Function blocks

Passed
No MATLAB Function blocks found

_____

Check for global variables in MATLAB functions defined in Stateflow charts

Passed
No MATLAB functions defined in Stateflow charts found

_____

Check for global variables in called MATLAB functions

Passed
No external MATLAB functions found

_____

_____

✅ Check usage of enumerated values
Identify enumeration classes used in the model with no default value specification.

Passed
No enumeration classes found without default value specifications.

_____

✅ Check input and output settings of MATLAB Functions

Identify MATLAB Functions that have inputs, outputs, or parameters with inherited complexity, data type, or size properties.

Passed

No MATLAB Functions found in the model or subsystem.

📁 *Usage* ✅5 ❌0 ⚠0 🗐0

_____

✅ Check MATLAB Function metrics

Identify MATLAB Functions that violate complexity limits.

Passed

No MATLAB Function with metrics violations were found.

The following metrics were determined for the model or subsystem.

Legend:

- LoC : Total lines of code

- ELoC : Effective lines of code

- CLoC : Comment lines of code

- DC : Density of comments

- CYC : Cyclomatic complexity

>

Input Parameters Selection

Name	Value
Maximum effective lines of code per function	60

Minimum density of comments	0.2
Maximum cyclomatic complexity per function	15

_____

✅ Check the number of function calls in MATLAB Function blocks

Checks whether number of function calls in MATLAB Function blocks is less than 3.

Passed

Number of function calls in MATLAB Function blocks is less than 3.

_____

✅ Check usage of character vector inside MATLAB Function block

Checks whether character vectors are being used inside MATLAB Function blocks

Passed

No character vectors found in MATLAB Function block

_____

✅ Check usage of recommended patterns for Switch/Case statements

Checks whether non-constant variables are used in Switch/Case arguments.

Passed

Non-constant variables are not used as Switch/Case arguments

_____

✅ Check for use of C-style comment symbols

Identify usage of C-style comments in CGT Files and MPT Objects.

Passed

C-style comments are not used in CGT Files and MPT Objects.

📁 Modeling Standards for JMAAB   ✅0 ❌0 ⚠0 📋121

📁 Naming Conventions   ✅0 ❌0 ⚠0 📋14

_____

📋 *Check file names*

Not Run

_____

289

Check folder names

Not Run

_____

Check subsystem names

Not Run

_____

Check port block names

Not Run

_____

Check character usage in block names

Not Run

_____

Check usable characters for signal names and bus names

Not Run

_____

Check usable characters for parameter names

Not Run

_____

Check length of model file name

Not Run

_____

Check length of folder name at every level of model path

Not Run

_____

Check length of subsystem names

Not Run

_____

Check length of Inport and Outport names

Not Run

_____

Check length of signal and bus names

Not Run

_____

📋 *Check length of parameter names*
    Not Run

_____

📋 *Check length of block names*
    Not Run

📁 Model Architecture   ✅0 ❌0 ⚠️0 📋1
_____

📋 *Check for mixing basic blocks and subsystems*
    Not Run

📁 Model Configuration Options   ✅0 ❌0 ⚠️0 📋2
_____

📋 *Check Implement logic signals as Boolean data (vs. double)*
    Not Run

_____

📋 *Check diagnostic settings for incorrect calculation results*
    Not Run

📁 Simulink   ✅0 ❌0 ⚠️0 📋53
_____

📋 *Check for Simulink diagrams using nonstandard display attributes*
    Not Run

_____

📋 *Check Model font settings*
    Not Run

_____

📋 *Check position of Inport and Outport blocks*
    Not Run

_____

291

📃 *Check whether block names appear below blocks*

Not Run

_____

📃 *Check the display attributes of block names*

Not Run

_____

📃 *Check for nondefault block attributes*

Not Run

_____

📃 *Check trigger signal names*

Not Run

_____

📃 *Check for unconnected ports and signal lines*

Not Run

_____

📃 *Check usage of Switch blocks*

Not Run

_____

📃 *Check usage of Relational Operator blocks*

Not Run

_____

📃 *Check Indexing Mode*

Not Run

_____

📃 *Check usage of tunable parameters in blocks*

Not Run

_____

📃 *Check signal line labels*

Not Run

_____

📃 *Check for propagated signal labels*

Not Run

292

_____

📋 *Check usage of Discrete-Time Integrator block*

Not Run

_____

📋 *Check settings for data ports in Multiport Switch blocks*

Not Run

_____

📋 *Check usage of fixed-point data type with non-zero bias*

Not Run

_____

📋 *Check input and output datatype for Switch blocks*

Not Run

_____

📋 *Check signs of input signals in product blocks*

Not Run

_____

📋 *Check Signed Integer Division Rounding mode*

Not Run

_____

📋 *Check type setting by data objects*

Not Run

_____

📋 *Check usage of the Saturation blocks*

Not Run

_____

📋 *Check usage of Merge block*

Not Run

_____

📋 *Check usage of Memory and Unit Delay blocks*

Not Run

_____

Check block orientation

Not Run

_____

Check if blocks are shaded in the model

Not Run

_____

Check operator order of Product blocks

Not Run

_____

Check icon shape of Logical Operator blocks

Not Run

_____

Check if tunable block parameters are defined as named constants

Not Run

_____

Check default/else case in Switch Case blocks and If blocks

Not Run

_____

Check usage of Lookup Tables

Not Run

_____

Check for parentheses in Fcn block expressions

Not Run

_____

Check undefined initial output for conditional subsystems

Not Run

_____

Check for avoiding algebraic loops between subsystems

Not Run

_____

Comparing floating point types in Simulink

Not Run

_____

Check duplication of Simulink Data names

Not Run

_____

Check unused data in Simulink Model

Not Run

_____

Check output data type of operation blocks

Not Run

_____

Check Model Description

Not Run

_____

Check for consistency in model element names

Not Run

_____

Check for sample time setting

Not Run

_____

Check usage of Sum blocks

Not Run

_____

Check position of signal labels

Not Run

_____

Check for missing ports in Variant Subsystems

Not Run

_____

Check for cascaded Unit Delay blocks

Not Run

_____

🔲 *Check for usage of Data Store Memory blocks*

Not Run

_____

🔲 *Check fundamental logical and numerical operations*

Not Run

_____

🔲 *Check signal flow in model*

Not Run

_____

🔲 *Check usage of vector and bus signals*

Not Run

_____

🔲 *Check connections between structural subsystems*

Not Run

_____

🔲 *Check position of conditional blocks and iterator blocks*

Not Run

_____

🔲 *Check signal line connections*

Not Run

_____

🔲 *Check scope of From and Goto blocks*

Not Run

_____

📁 Stateflow ✅0 ❌0 ⚠0 🔲49

_____

🔲 *Check transition orientations in flow charts*

Not Run

_____

🔲 *Check return value assignments in Stateflow graphical functions*

Not Run

_____

Check default transition placement in Stateflow charts

Not Run

_____

Check for Strong Data Typing with Simulink I/O

Not Run

_____

Check definition of Stateflow data

Not Run

_____

Check for MATLAB expressions in Stateflow blocks

Not Run

_____

Check for pointers in Stateflow charts

Not Run

_____

Check Stateflow operators

Not Run

_____

Check usage of unary minus operations in Stateflow charts

Not Run

_____

Check usage of Stateflow comments

Not Run

_____

Check prohibited comparison operation of logical type signals

Not Run

_____

Check usage of internal transitions in Stateflow states

Not Run

_____

Check usage of transition conditions in Stateflow transitions

Not Run

📋 *Check uniqueness of Stateflow State and Data names*

Not Run

📋 *Check uniqueness of State names*

Not Run

📋 *Check usage of parentheses in Stateflow transitions*

Not Run

📋 *Check prohibited combination of state action and flow chart*

Not Run

📋 *Check condition actions and transition actions in Stateflow*

Not Run

📋 *Check usable number for first index*

Not Run

📋 *Check usage of State names*

Not Run

📋 *Check execution timing for default transition path*

Not Run

📋 *Check repetition of Action types*

Not Run

📋 *Check for unused data in Stateflow Charts*

Not Run

Check updates to variables used in state transition conditions
Not Run

_____

Check starting point of internal transition in Stateflow
Not Run

_____

Check for parallel Stateflow state used for grouping
Not Run

_____

Check scope of data in parallel states
Not Run

_____

Check indentation of code in Stateflow states
Not Run

_____

Check for usage of text inside states
Not Run

_____

Check for unexpected backtracking in state transitions
Not Run

_____

Check for unconnected objects in Stateflow Charts
Not Run

_____

Check position of label string in Stateflow transition
Not Run

_____

Check Stateflow chart action language
Not Run

_____

Check usable characters for Stateflow data names
Not Run

Check length of Stateflow data name

Not Run

_____

Check usage of transitions to external states

Not Run

_____

Check order of state action types

Not Run

_____

Check usage of numeric literals in Stateflow

Not Run

_____

Check position of comments in transition labels

Not Run

_____

Check terminal junctions in Stateflow

Not Run

_____

Check for implicit type casting in Stateflow

Not Run

_____

Check if state action type 'exit' is used in the model

Not Run

_____

Check for use of C-style comment symbols

Not Run

_____

Check usage of unconditional transitions in flow charts

Not Run

_____

Check for comments in unconditional transitions
Not Run

_____

Check definition of Stateflow events
Not Run

_____

Check Stateflow transition appearance
Not Run

_____

Check for usage of events and broadcasting events in Stateflow charts
Not Run

_____

Check usage of Simulink functions in Stateflow
Not Run

📁 MATLAB Functions ✅0 ❌0 ⚠️0 📋2

_____

Check input and output settings of MATLAB Functions
Not Run

_____

Check MATLAB code for global variables
Not Run

📁 Units Inconsistencies ✅0 ❌0 ⚠️0 📋5

_____

Identify unit mismatches in the model
Not Run

_____

Identify automatic unit conversions in the model
Not Run

_____

📰 Identify disallowed unit systems in the model

Not Run

_____

📰 Identify undefined units in the model

Not Run

_____

📰 Identify ambiguous units in the model

Not Run

📁 Upgrading to the Current Simulink Version ✅0 ❌0 ⚠️0 📰1

_____

📰 Open the Upgrade Advisor

Not Run

📁 Modeling Standards for MISRA C:2012 ✅12 ❌0 ⚠️1 📰0

_____

⚠️ Check configuration parameters for MISRA C:2012

Identify configuration parameters that might impact MISRA C:2012 compliant code generation.

Warning

The model configuration parameters are not set to the recommended values specified in the data file.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Warning	Model Verification block enabling (AssertControl)	UseLocalSettings	DisableAll	

Warning	Generate shared constants (GenerateSharedConstants)	on	off	UtilityFuncGeneration
Warning	Parentheses level (ParenthesesLevel)	Nominal	Maximum	SystemTargetFile
Warning	Casting modes (CastingMode)	Nominal	Standards	SystemTargetFile
Warning	Use division for fixed-point net slope computation (UseDivisionForNetSlopeComputation)	off	on, UseDivisionForReciprocalsOfIntegersOnly	
Warning	Replace multiplications by powers of two with signed bitwise shifts (EnableSignedLeftShifts)	on	off	SystemTargetFile
Warning	Allow right shifts on signed integers (EnableSignedRightShifts)	on	off	SystemTargetFile
Warning	Undirected event broadcasts (SFUndirectedBroadcastEventsDiag)	warning	error	
Warning	Compile-time recursion limit for MATLAB functions (CompileTimeRecursionLimit)	50	0	
Warning	Enable run-time recursion for MATLAB functions (EnableRuntimeRecursion)	on	off	
Warning	MATLAB user comments (MATLABFcnDesc)	off	on	GenerateComments, SystemTargetFile

∧ Less

303

Recommended Action

Modify the configuration parameters listed above to the recommended values.

_____

✅ Check for blocks not recommended for C/C++ production code deployment

Identify blocks not supported by code generation or not recommended for C/C++ production code deployment.

Passed

Blocks not recommended for C/C++ production code deployment were not found in the model or subsystem.

_____

✅ Check for blocks not recommended for MISRA C:2012

Identify blocks that are not recommended for MISRA C:2012 compliant code generation.

Passed

None of the blocks are defined as "not recommended" for MISRA C:2012 compliant code generation.

_____

✅ Check for unsupported block names

Identify block names containing "/".

Passed

No unsupported block names found.

_____

✅ Check usage of Assignment blocks

Identify Assignment blocks with possibly incomplete array initialization that do not have the simulation run-time diagnostic **Action if any output element is not assigned** set to:

- Warning, if Assignment block is in an iterator subsystem

- Error, if Assignment block is not in an iterator subsystem

Passed

All Assignment blocks are configured with block parameter **Action if any output element is not assigned** set to Warning or Error.

_____

✅ Check for switch case expressions without a default case
Identify switch case expressions that do not have a default case.

Passed

All switch case expressions have default cases.

_____

✅ Check for missing error ports in AUTOSAR receiver interfaces
Identify AUTOSAR receiver interface ports that do not have a matching error port.

Passed

Model is not configured as an AUTOSAR target.

_____

✅ Check for bitwise operations on signed integers
Identify bitwise operations on signed integers.

Passed

No bitwise operations on signed integers found.

_____

✅ Check for recursive function calls
Identify function calls that are recursive.

Passed

No recursive function calls found.

_____

✅ Check for equality and inequality operations on floating-point values
Identify equality and inequality operations on floating-point values.

Passed

No equality or inequality operations on floating-point values found.

_____

✅ Check for missing const qualifiers in model functions

Identify missing const qualifiers in model functions.

Passed

Model does not use customized model functions.

_____

✅ Check integer word lengths

Identify integer word length that are not compliant with hardware implementation settings.

Passed

All used integer word length are compliant with hardware implementation settings.

_____

✅ Check bus object names that are used as bus element names

Identify bus object names that are used as bus element names.

Passed

No bus object names are used as bus element names.

📁 Modeling Standards for Secure Coding (CERT C, CWE, ISO/IEC TS 17961) ✅0 ❌0 ⚠0
📋13

_____

📋 Check configuration parameters for secure coding standards

Not Run

_____

📋 Check for blocks not recommended for C/C++ production code deployment

Not Run

_____

📋 Check for blocks not recommended for secure coding standards

Not Run

_____

📋 Check usage of Assignment blocks

Not Run

_____

📋 Check for switch case expressions without a default case

Not Run

_____

📋 Check for bitwise operations on signed integers

Not Run

_____

📋 Check for equality and inequality operations on floating-point values

Not Run

_____

📋 Check integer word lengths

Not Run

_____

📋 Detect Dead Logic

Not Run

_____

📋 Detect Integer Overflow

Not Run

_____

📋 Detect Division By Zero

Not Run

_____

📋 Detect Out Of Bound Array Access

Not Run

_____

📋 Detect Specified Minimum and Maximum Value Violations

Not Run

_____

📁 Frequency Response Estimation   ✅0 ❌0 ⚠0 📋1

_____

📋 Identify time-varying source blocks interfering with frequency response estimation

Not Run