Report Generated by Test Manager

Title: TCF_Debounce MIL Test Report

Author:

Date: 19-Dec-2021 10:00:32

Test Environment

Platform: PCWIN64 MATLAB: (R2020a)

Summary

Name	Outcome	Duration (Seconds)
<u>Debouncer</u>	5 🗸	18.14
EI09_SWRS_Debouncer_01	•	2.78
EI09_SWRS_Debouncer_1	•	2.43
EI09_SWRS_Debouncer_2	•	2.543
EI09_SWRS_Debouncer_3	•	2.606
■ EI09 SWRS Debouncer 4	•	2.51

Debouncer

Test Result Information

Result Type: Test Suite Result

Parent: None

Start Time: 2021-12-18 19:26:07 End Time: 2021-12-18 19:26:25 Outcome: Total: 5, Passed: 5

Test Suite Information

Name: Debouncer

Aggregated Coverage Results

Analyzed Model	Sim Mo	Complexity	Decision	Condition	MCDC	Execution
	de					
SWC_TCF/SWC_TCF_1ms_sys/Debounc	Normal	7	100%	100%	100%	100%
<u>er</u>						

Back to Report Summary

EI09_SWRS_Debouncer_01

Test Result Information

Result Type: Test Case Result Parent: Debouncer

Start Time: 2021-12-18 19:26:07 End Time: 2021-12-18 19:26:10

Outcome: Passed

Test Case Information

Name: EI09_SWRS_Debouncer_01

Type: Baseline Test

Baseline Name: EI09_SWUT_MIL_Debouncer_01

Baseline File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

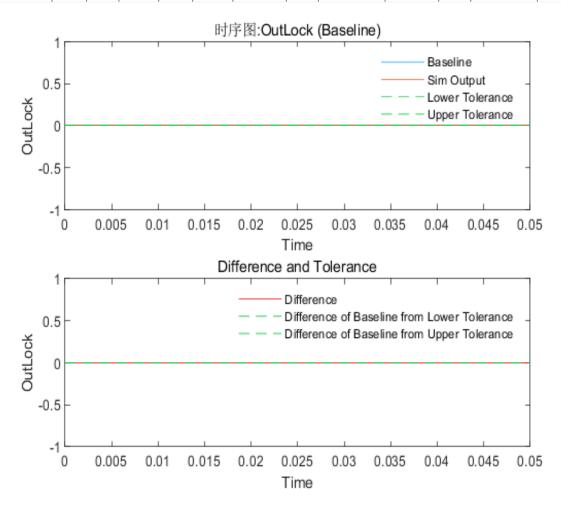
tform Models\01 Platformmodels\FS\TCF\TCF V2

\TestCase_TCF.xlsx

Baseline Comparison

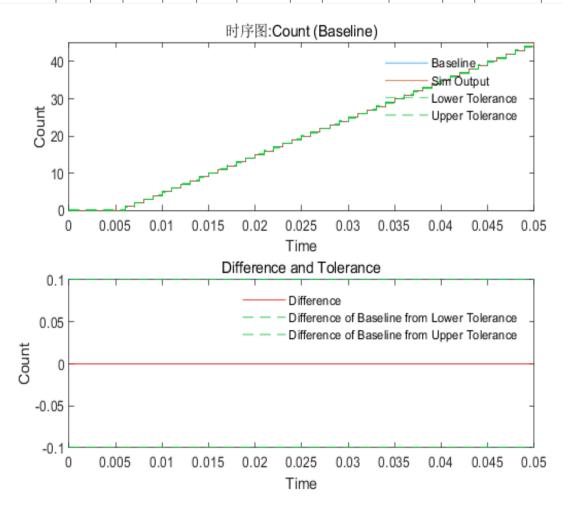
Name	Abs	Rel	Lead	Lag	Max	Data	Units	Sample	Data	Units	Sample	Interp	Symo	Link
	Tol	Tol	Tol	Tol	Diff	Type 1	1	Time 1	Type 2	2	Time 2	inter p	Sylic	to Plot
OutLock	0.1	0	0	0	0	boolean			boolean			zoh	union	<u>Link</u>
Count	0.1	0	$\begin{bmatrix} - & - \\ 0 & \end{bmatrix}$	0	0	int16			int16			zoh	union	<u>Link</u>
Deb	0.1	0	$\begin{bmatrix} - & - \\ 0 & \end{bmatrix}$	0	0	int16			int16			zoh	union	<u>Link</u>
												•		

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data T ype 1	Units 1	Sample Time 1	Data T ype 2	Units 2	Sample Time 2	Interp Sync
OutLock	0.1	0	0	0	0	boolean			boolean	ı		zoh union



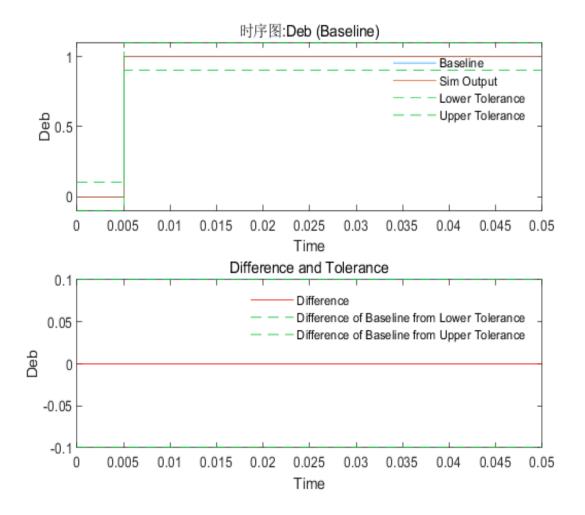
Back to Report SummaryBack to Criteria Results

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data T ype 1	Units 1	 Data T ype 2	Units 2	Sample Time 2	Interp	Sync
Count	0.1	0	0	0	0	int16		int16			zoh	union



Back to Report SummaryBack to Criteria Results

	Name	Abs	Rel	Lead	Lag	Max	Data T	Units	Sample	Data T	Units	Sample	Intorn Sync
Name	Tol	Tol	Tol	Tol	Diff	ype 1	1	Time 1	ype 2	2	Time 2	Interp Sync	
	Deb	0.1	 0	0	0	0	int16			int16			zoh union



Back to Report SummaryBack to Criteria Results

EI09_SWUT_MIL_Debouncer_01

Baseline Information

Baseline Name: EI09_SWUT_MIL_Debouncer_01

Baseline File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

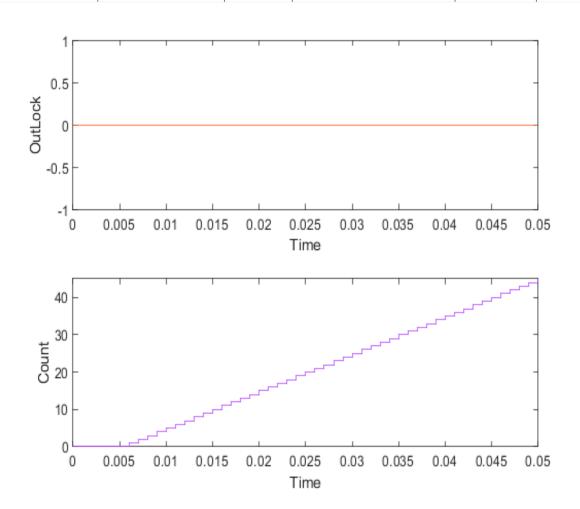
tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\TestCase_TCF.xlsx

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
OutLock	boolean			zoh	union	<u>Link</u>

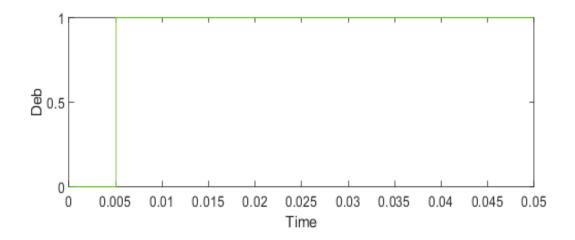


Name	Data Type	Units	Sample Time	Interp	Sync
OutLock	boolean			zoh	union
Count	int16			zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Deb	int16			zoh	union



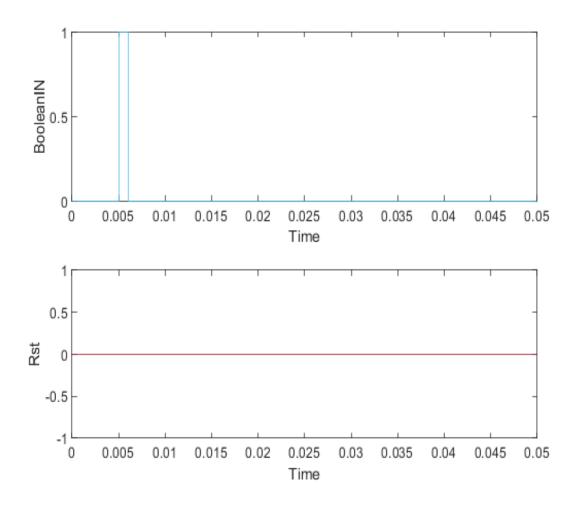
Input Data

Input Information

External Input Na EI09_SWUT_MIL_Debouncer_01 me:

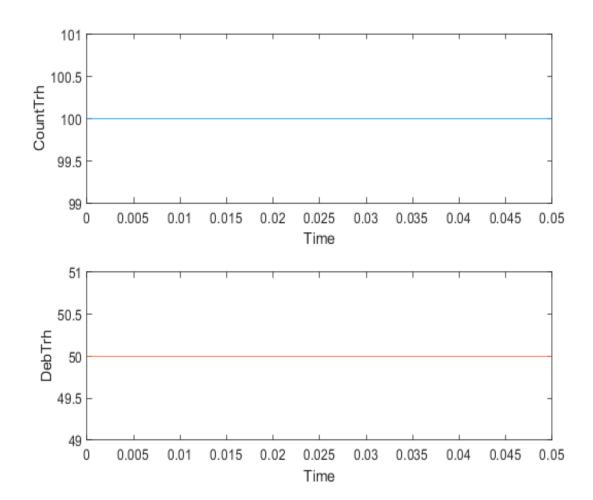
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
BooleanIN	boolean			zoh	union	<u>Link</u>
Rst	boolean			zoh	union	<u>Link</u>
CountTrh	int16			zoh	union	Link
DebTrh	int16			zoh	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
BooleanIN	boolean			zoh	union
Rst	boolean		 	zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
CountTrh	int16			zoh	union
DebTrh	int16			zoh	union



Simulation

System Under Test Information

Model: SWC_TCF

Harness: SWC_TCF_Harness_Debouncer

Harness Owner: SWC_TCF/SWC_TCF_1ms_sys/Debouncer

Simulation Mode: normal

Override SIL or PIL Mod 0

e:

Configuration Set: Configuration 1

External Input Name: EI09_SWUT_MIL_Debouncer_01

External Input File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\TestCase_TCF.xlsx

Start Time: 0

Checksum: 944823193 3446748209 4091885579 3684280232

Simulink Version: 10.1 Model Version: 1.1

Model Author: dongliyuan

Date: Tue Aug 10 16:53:31 2021

User ID: dongliyuan

Model Path: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\SWC_TCF.slx

Machine Name: MC-ZHANGJUNRENB Solver Name: FixedStepDiscrete

Solver Type: Fixed-Step

Fixed Step Size: 0.001

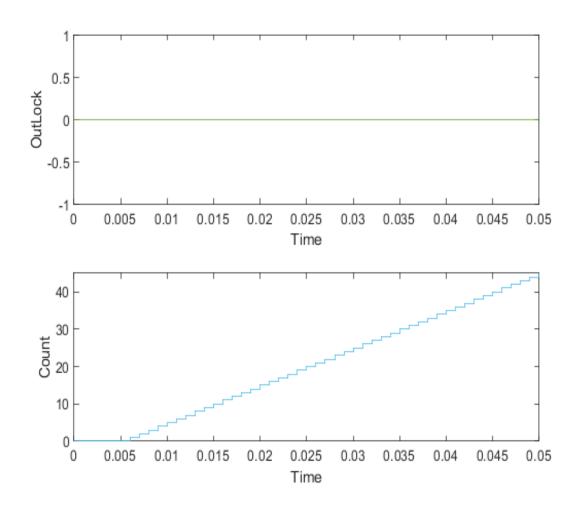
Simulation Start Time: 2021-12-18 19:26:07 Simulation Stop Time: 2021-12-18 19:26:08

Platform: PCWIN64

Simulation Output

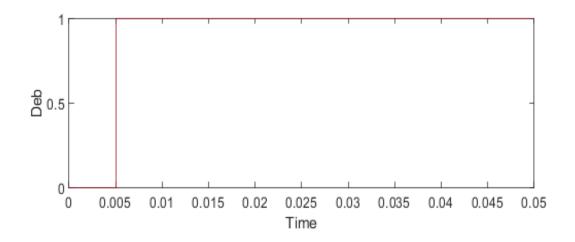
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
OutLock	boolean			zoh	union	<u>Link</u>
Count	int16			zoh	union	<u>Link</u>
Deb	int16			zoh	union	<u>Link</u>

Name	Data Type	Units	Sample Time	Interp	Sync
OutLock	boolean			zoh	union
Count	int16			zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Deb	int16			zoh	union



Simulation Logs:

Simulation stopped at '0.05000000000000003' because there is no input data after this time point.

Back to Report Summary

EI09_SWRS_Debouncer_1

Test Result Information

Result Type: Test Case Result

Parent: Debouncer

Start Time: 2021-12-18 19:26:11 End Time: 2021-12-18 19:26:13

Outcome: Passed

Test Case Information

Name: EI09_SWRS_Debouncer_1

Type: Baseline Test

Baseline Name: EI09_SWUT_MIL_Debouncer_02

Baseline File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

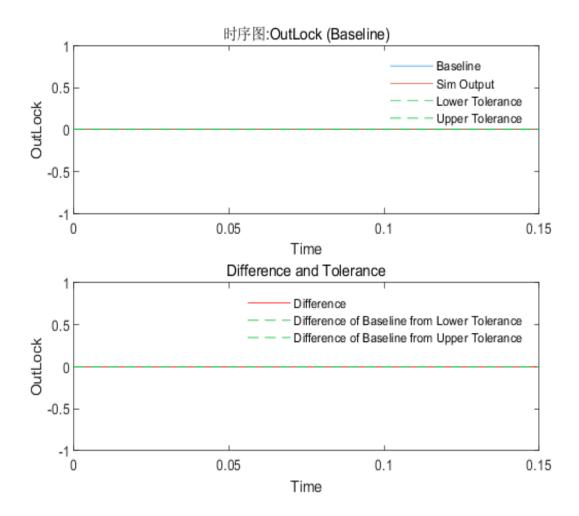
tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\TestCase_TCF.xlsx

Baseline Comparison

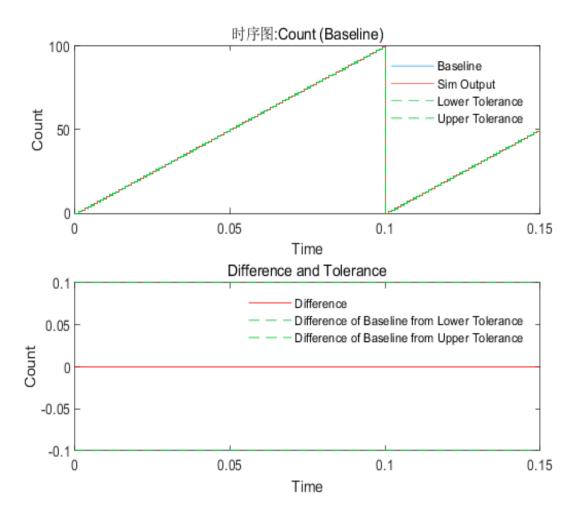
Name	Abs	Rel	Lead	Lag	Max	Data	Units	Sample	Data	Units	Sample	Interp	Sync	Link
Name	Tol	Tol	Tol	Tol	Diff	Type 1	1	Time 1	Type 2	2	Time 2	Inter p	Sync	to Plot
OutLock	0.1	0	0	0	0	boolean			boolean			zoh	union	<u>Link</u>
Count	0.1	0	0	0	0	int16			int16			zoh	union	<u>Link</u>
Deb	0.1	0	0	0	0	int16			int16			zoh	union	<u>Link</u>

	Name	Abs	Rel	Lead	Lag	Max	Data T	Units	Sample	Data T	Units	Sample	Interp Sync
Name	Tol	Tol	Tol	Tol	Diff	ype 1	1	Time 1	ype 2	2	Time 2	inter p Sync	
	OutLock	0.1	0	0	0	0	boolean			boolean			zoh union



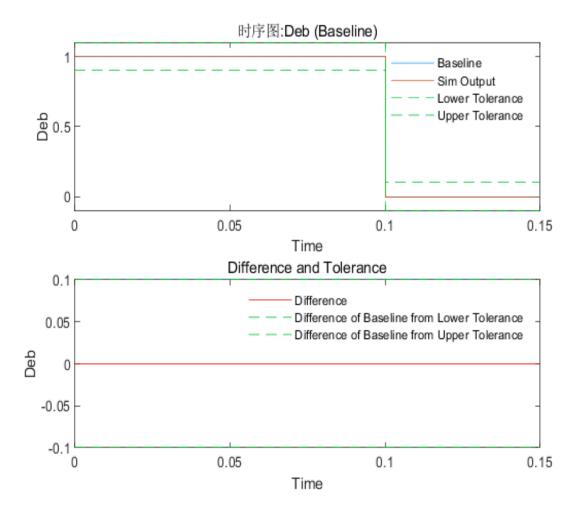
Back to Report SummaryBack to Criteria Results

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data T ype 1	Units 1	Sample Time 1	Data T ype 2	Units 2	Sample Time 2	Interp Sync
Count	0.1	0	0	0	0	int16			int16			zoh union



Back to Report SummaryBack to Criteria Results

	Name	Abs	Rel	Lead	Lag	Max	Data T	Units	Sample	Data T	Units	Sample	Interp Sync
Name	Tol	Tol	Tol	Tol	Diff	ype 1	1	Time 1	ype 2	2	Time 2	Jinter p Sync	
	Deb	0.1	0	0	0	0	int16			int16			zoh union



Back to Report SummaryBack to Criteria Results

EI09_SWUT_MIL_Debouncer_02

Baseline Information

Baseline Name: EI09_SWUT_MIL_Debouncer_02

Baseline File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

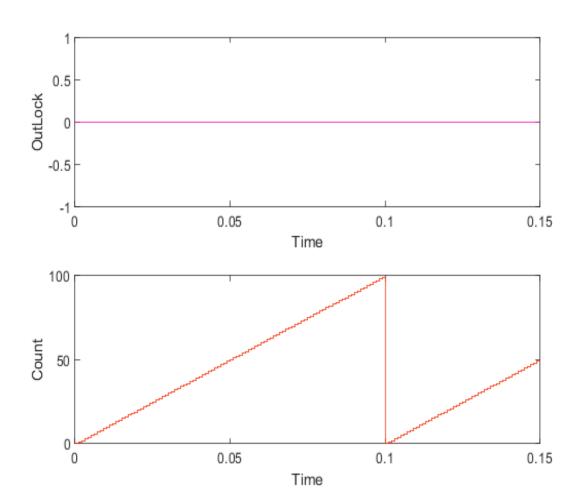
 $tform_Models \\ \label{eq:first} -- \\ TCF_V2$

\TestCase_TCF.xlsx

Name	Data Type	Units	Sample Time	Interp	Sync	Link
						to Plot
OutLock	boolean			zoh	union	<u>Link</u>

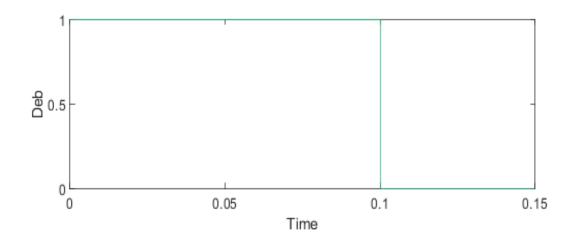


Name	Data Type	Units	Sample Time	Interp	Sync
OutLock	boolean			zoh	union
Count	int16			zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Deb	int16			zoh	union



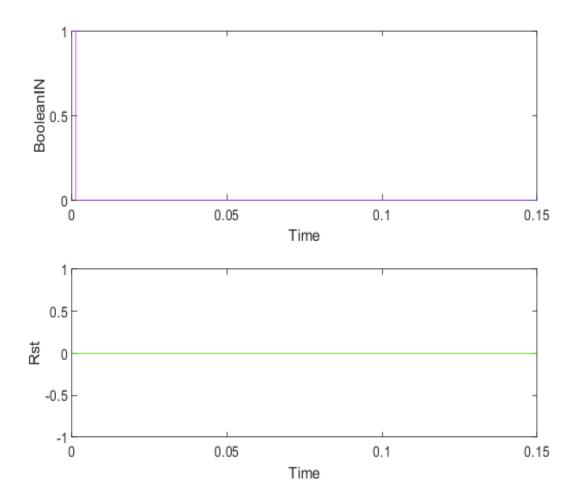
Input Data

Input Information

External Input Na EI09_SWUT_MIL_Debouncer_02 me:

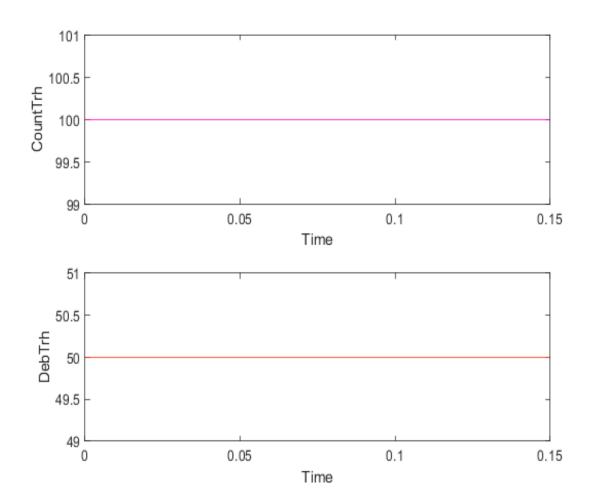
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
BooleanIN	boolean			zoh	union	<u>Link</u>
Rst	boolean			zoh	union	<u>Link</u>
CountTrh	int16			zoh	union	Link
DebTrh	int16			zoh	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
BooleanIN	boolean			zoh	union
Rst	boolean	 _		zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
CountTrh	int16			zoh	union
DebTrh	int16			zoh	union



Back to Report SummaryBack to Signal Summary

Simulation

System Under Test Information

Model: SWC_TCF

Harness: SWC_TCF_Harness_Debouncer

Harness Owner: SWC_TCF/SWC_TCF_1ms_sys/Debouncer

Simulation Mode: normal

Override SIL or PIL Mod 0

e:

Configuration Set: Configuration 1

External Input Name: EI09_SWUT_MIL_Debouncer_02

External Input File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

tform Models\01 Platformmodels\FS\TCF\TCF V2

\TestCase_TCF.xlsx

Start Time: 0

Checksum: 680107186 2724320922 4088178477 3746064011

Simulink Version: 10.1 Model Version: 1.1

Model Author: dongliyuan

Date: Tue Aug 10 16:53:31 2021

User ID: dongliyuan

Model Path: E:\EI09 Project\ei09\03 Controller Models\02 Pla

tform Models\01 Platformmodels\FS\TCF\TCF V2

\SWC_TCF.slx

Machine Name: MC-ZHANGJUNRENB Solver Name: FixedStepDiscrete

Solver Type: Fixed-Step

Fixed Step Size: 0.001

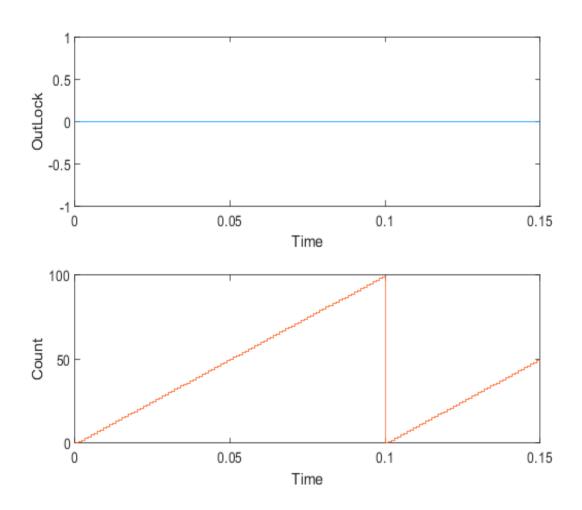
Simulation Start Time: 2021-12-18 19:26:11 Simulation Stop Time: 2021-12-18 19:26:11

Platform: PCWIN64

Simulation Output

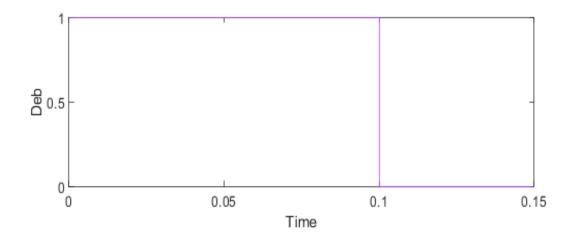
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
OutLock	boolean			zoh	union	<u>Link</u>
Count	int16			zoh	union	<u>Link</u>
Deb	int16			zoh	union	<u>Link</u>

Name	Data Type	Units	Sample Time	Interp	Sync
OutLock	boolean			zoh	union
Count	int16			zoh	union



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Name	Data Type	Units	Sample Time	Interp	Sync
Deb	int16			zoh	union



Back to Report Summary

EI09_SWRS_Debouncer_2

Test Result Information

Result Type: Test Case Result

Parent: Debouncer

Start Time: 2021-12-18 19:26:15 End Time: 2021-12-18 19:26:17

Outcome: Passed

Test Case Information

Name: EI09_SWRS_Debouncer_2

Type: Baseline Test

Baseline Name: EI09_SWUT_MIL_Debouncer_03

Baseline File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

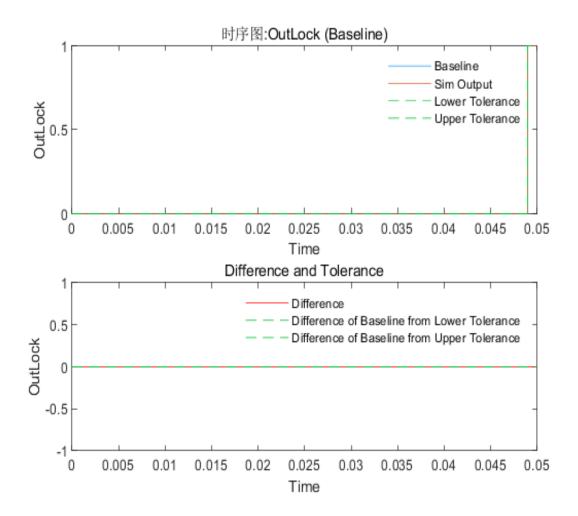
tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\TestCase_TCF.xlsx

Baseline Comparison

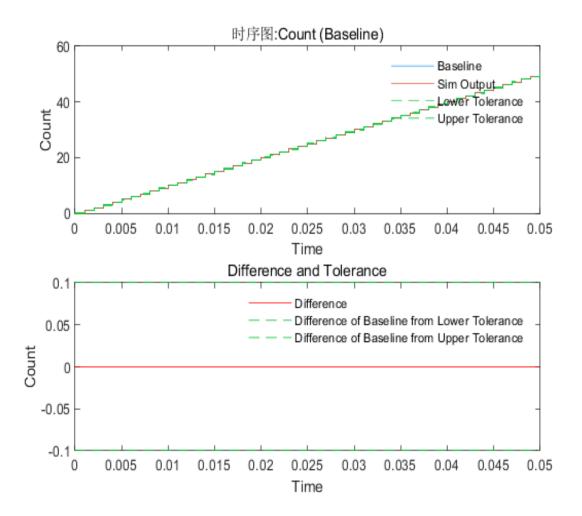
Name	Abs	Rel	Lead	Lag	Max	Data	Units	Sample	Data	Units	Sample	Interp	Sync	Link
Ivallic	Tol	Tol	Tol	Tol	Diff	Type 1	1	Time 1	Type 2	2	Time 2	inter p	Jyne	to Plot
OutLock	0.1	0	0	0	0	boolean			boolean			zoh	union	<u>Link</u>
Count	0.1	0	0	0	0	int16			int16			zoh	union	<u>Link</u>
Deb	0.1	0	0	0	0	int16			int16			zoh	union	<u>Link</u>

	Name	Abs	Rel	Lead	Lag	Max	Data T	Units	Sample	Data T	Units	Sample	Interp Sync
Name	Tol	Tol	Tol	Tol	Diff	ype 1	1	Time 1	ype 2	2	Time 2	liner p by ne	
	OutLock	0.1	0	0	0	0	boolean			boolean			zoh union



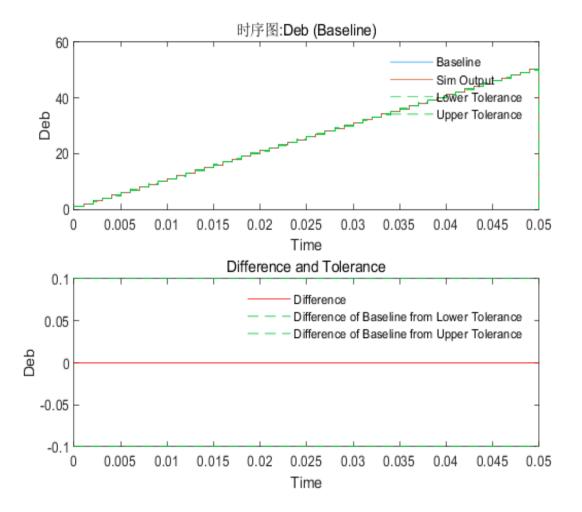
Back to Report SummaryBack to Criteria Results

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data T ype 1	Units 1	Sample Time 1	Data T ype 2	Units 2	Sample Time 2	Interp Sync
Count	0.1	0	0	0	0	int16			int16			zoh union



Back to Report SummaryBack to Criteria Results

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data T ype 1	Units 1	Sample Time 1	Data T ype 2	Units 2	Sample Time 2	Interp Sync
Deb	0.1	0	0	0	0	int16			int16			zoh union



Back to Report SummaryBack to Criteria Results

EI09_SWUT_MIL_Debouncer_03

Baseline Information

Baseline Name: EI09_SWUT_MIL_Debouncer_03

Baseline File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

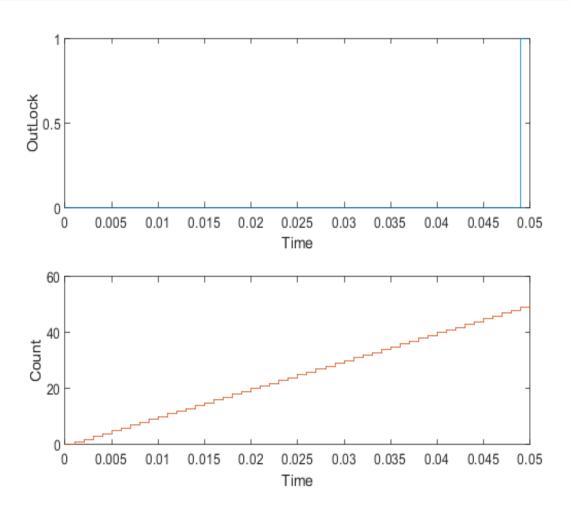
tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\TestCase_TCF.xlsx

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
OutLock	boolean			zoh	union	<u>Link</u>

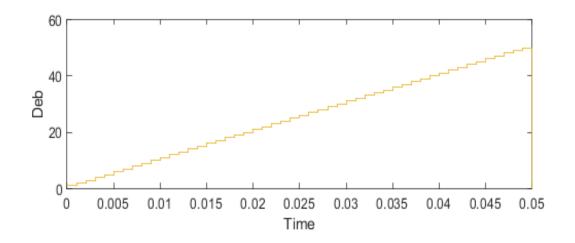


Name	Data Type	Units	Sample Time	Interp	Sync
OutLock	boolean			zoh	union
Count	int16			zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Deb	int16			zoh	union



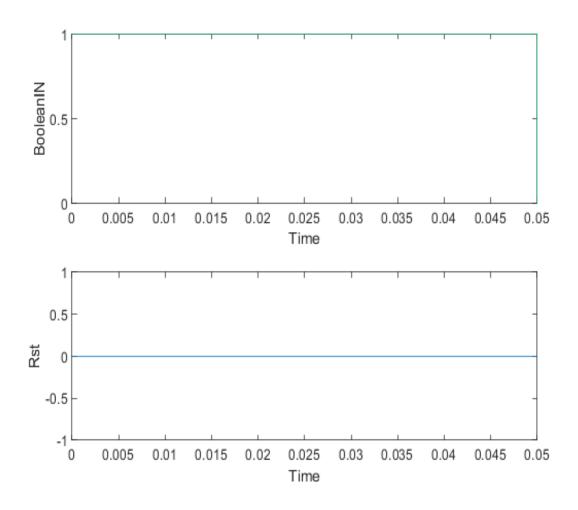
Input Data

Input Information

External Input Na EI09_SWUT_MIL_Debouncer_03 me:

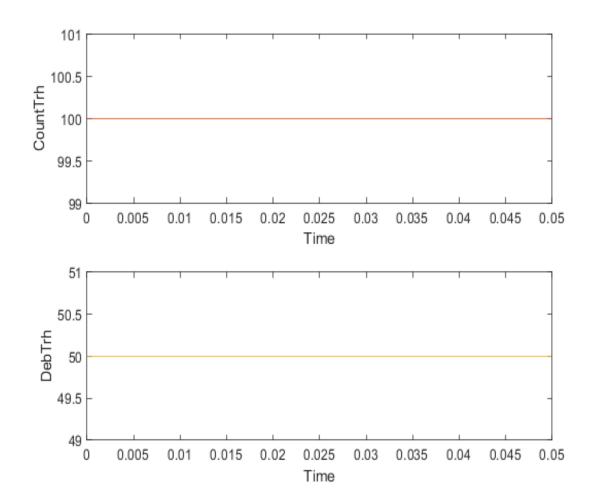
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
BooleanIN	boolean			zoh	union	<u>Link</u>
Rst	boolean	Г — — — — I		zoh	union	<u>Link</u>
CountTrh	int16	Г — — — — I		zoh	union	<u>Link</u>
DebTrh	int16	 		zoh	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
BooleanIN	boolean			zoh	union
Rst	boolean	 _		zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
CountTrh	int16			zoh	union
DebTrh	int16			zoh	union



Simulation

System Under Test Information

Model: SWC_TCF

Harness: SWC_TCF_Harness_Debouncer

Harness Owner: SWC_TCF/SWC_TCF_1ms_sys/Debouncer

Simulation Mode: normal

Override SIL or PIL Mod 0

e:

Configuration Set: Configuration 1

External Input Name: EI09_SWUT_MIL_Debouncer_03

External Input File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

tform Models\01 Platformmodels\FS\TCF\TCF V2

\TestCase_TCF.xlsx

Start Time: 0

Checksum: 944823193 3446748209 4091885579 3684280232

Simulink Version: 10.1 Model Version: 1.1

Model Author: dongliyuan

Date: Tue Aug 10 16:53:31 2021

User ID: dongliyuan

Model Path: E:\EI09 Project\ei09\03 Controller Models\02 Pla

tform Models\01 Platformmodels\FS\TCF\TCF V2

\SWC_TCF.slx

Machine Name: MC-ZHANGJUNRENB Solver Name: FixedStepDiscrete

Solver Type: Fixed-Step

Fixed Step Size: 0.001

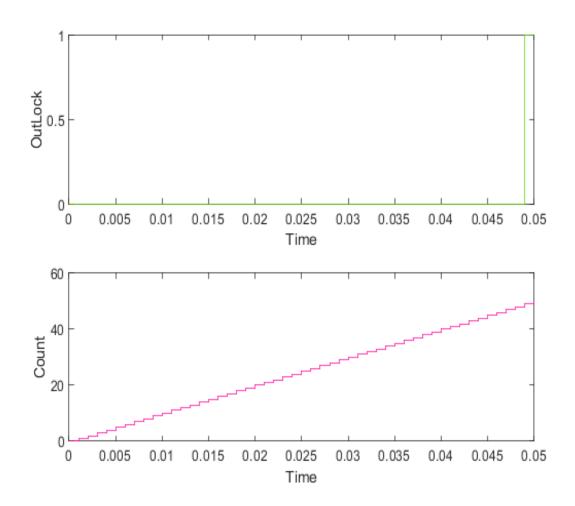
Simulation Start Time: 2021-12-18 19:26:15 Simulation Stop Time: 2021-12-18 19:26:15

Platform: PCWIN64

Simulation Output

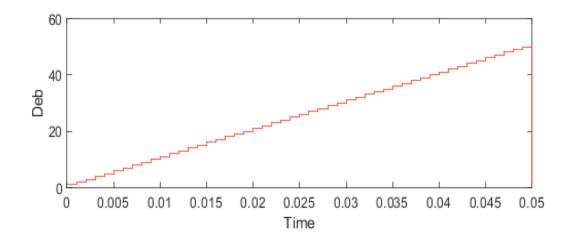
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
OutLock	boolean			zoh	union	<u>Link</u>
Count	int16			zoh	union	<u>Link</u>
Deb	int16	_ _		zoh	union	<u>Link</u>

Name	Data Type	Units	Sample Time	Interp	Sync
OutLock	boolean			zoh	union
Count	int16			zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync	
Deb	int16			zoh	union	



Simulation Logs:

Simulation stopped at '0.05000000000000003' because there is no input data after this time point.

Back to Report Summary

EI09_SWRS_Debouncer_3

Test Result Information

Result Type: Test Case Result

Parent: Debouncer

Start Time: 2021-12-18 19:26:18 End Time: 2021-12-18 19:26:21

Outcome: Passed

Test Case Information

Name: EI09_SWRS_Debouncer_3

Type: Baseline Test

Baseline Name: EI09_SWUT_MIL_Debouncer_04

Baseline File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

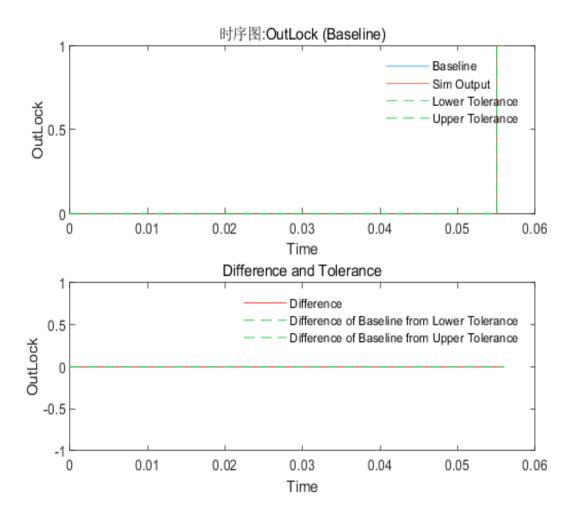
tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\TestCase_TCF.xlsx

Baseline Comparison

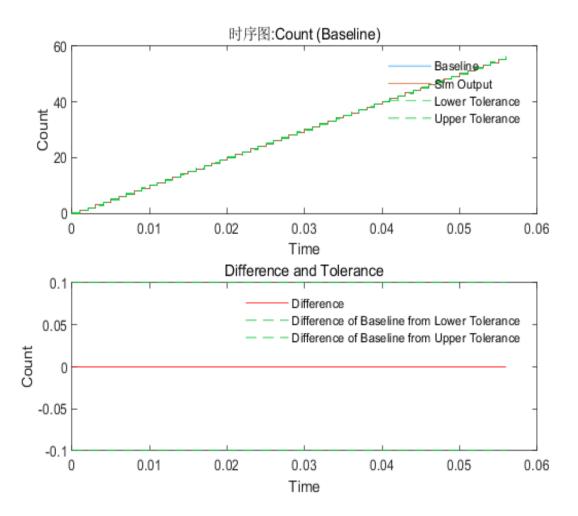
Name	Abs	Rel	Lead	Lag	Max	Data	Units	Sample	Data	Units	Sample	Interp	Sync	Link
Turite	Tol	Tol	Tol	Tol	Diff	Type 1	1	Time 1	Type 2	2	Time 2		J	to Plot
OutLock	0.1	0	0	0	0	boolean			boolean			zoh	union	<u>Link</u>
Count	0.1	0	0	0	0	int16			int16			zoh	union	<u>Link</u>
Deb	0.1	0	0	0	0	int16			int16			zoh	union	<u>Link</u>

1	Name	Abs	Rel	Lead	Lag	Max	Data T	Units	Sample	Data T	Units	Sample	Interp Sync	
	Turic	Tol	Tol	Tol	Tol	Diff	ype 1	1	Time 1	ype 2	2	Time 2	interp by ne	
	OutLock	0.1	0	0	0	0	boolean			boolean			zoh union	



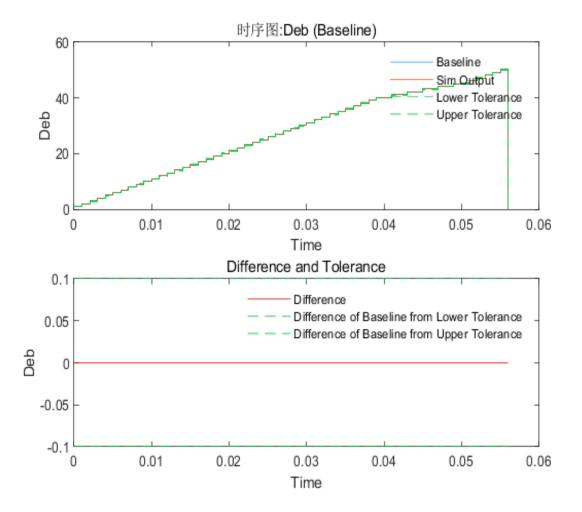
Back to Report SummaryBack to Criteria Results

Name	Abs	Rel	Lead	Lag	Max	Data T	Units	_	Data T	Units	•	Interp Sync
	Tol	Tol	Tol	Tol	Diff	ype 1	1	Time 1	ype 2	2	Time 2	
Count	0.1	0	0	0	0	int16			int16			zoh union



Back to Report SummaryBack to Criteria Results

Name	Abs	Rel	Lead	Lag	Max	Data T	Units	Sample	Data T	Units	Sample	Interp Sync
runic	Tol	Tol	Tol	Tol	Diff	ype 1	1	Time 1	ype 2	2	Time 2	interp bytic
Deb	0.1	0	0	0	0	int16			int16			zoh union



Back to Report SummaryBack to Criteria Results

EI09_SWUT_MIL_Debouncer_04

Baseline Information

Baseline Name: EI09_SWUT_MIL_Debouncer_04

Baseline File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

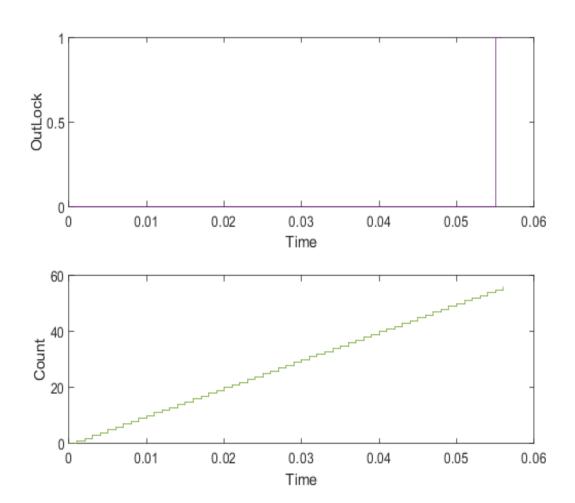
tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\TestCase_TCF.xlsx

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
OutLock	boolean			zoh	union	<u>Link</u>

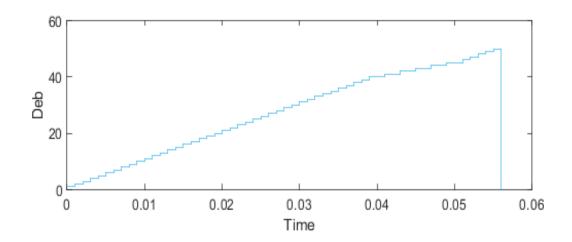


Name	Data Type	Units	Sample Time	Interp	Sync
OutLock	boolean			zoh	union
Count	int16			zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Deb	int16			zoh	union



Input Data

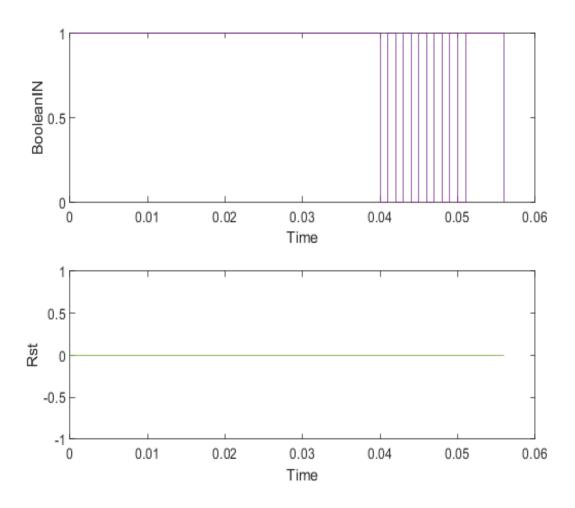
Input Information

External Input Na EI09_SWUT_MIL_Debouncer_04 me:

 $\label{limit} External\ Input\ File: E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\CFS\TCF\TCF_V2 \\ \ \ \TestCase_TCF.xlsx \\$

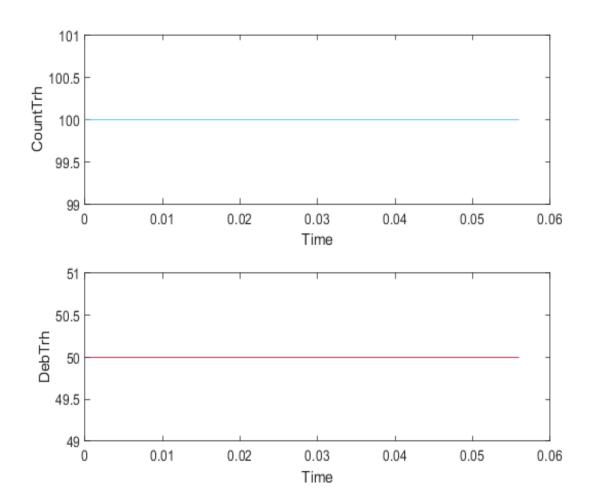
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
BooleanIN	boolean			zoh	union	<u>Link</u>
Rst	boolean			zoh	union	<u>Link</u>
CountTrh	int16			zoh	union	Link
DebTrh	int16			zoh	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
BooleanIN	boolean			zoh	union
Rst	boolean	 _		zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
CountTrh	int16			zoh	union
DebTrh	int16			zoh	union



Simulation

System Under Test Information

Model: SWC_TCF

Harness: SWC_TCF_Harness_Debouncer

Harness Owner: SWC_TCF/SWC_TCF_1ms_sys/Debouncer

Simulation Mode: normal

Override SIL or PIL Mod 0

e:

Configuration Set: Configuration 1

External Input Name: EI09_SWUT_MIL_Debouncer_04

External Input File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

tform Models\01 Platformmodels\FS\TCF\TCF V2

\TestCase_TCF.xlsx

Start Time: 0

Stop Time: 0.056000000000000001

Checksum: 2423774204 2916392130 1180571948 1936751900

Simulink Version: 10.1 Model Version: 1.1

Model Author: dongliyuan

Date: Tue Aug 10 16:53:31 2021

User ID: dongliyuan

Model Path: E:\EI09 Project\ei09\03 Controller Models\02 Pla

tform Models\01 Platformmodels\FS\TCF\TCF V2

\SWC_TCF.slx

Machine Name: MC-ZHANGJUNRENB Solver Name: FixedStepDiscrete

Solver Type: Fixed-Step

Fixed Step Size: 0.001

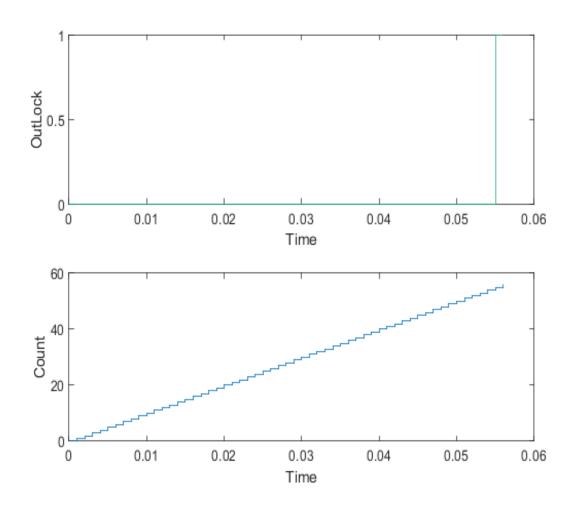
Simulation Start Time: 2021-12-18 19:26:18 Simulation Stop Time: 2021-12-18 19:26:19

Platform: PCWIN64

Simulation Output

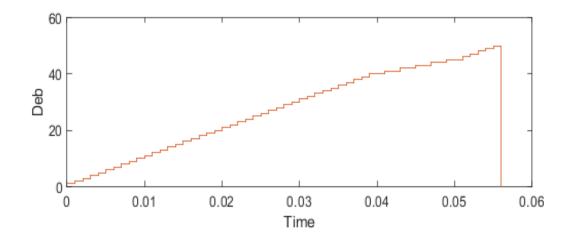
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
OutLock	boolean			zoh	union	<u>Link</u>
Count	int16			zoh	union	<u>Link</u>
Deb	int16			zoh	union	<u>Link</u>

Name	Data Type	Units	Sample Time	Interp	Sync
OutLock	boolean			zoh	union
Count	int16			zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync	
Deb	int16			zoh	union	



Simulation Logs:

Simulation stopped at '0.05600000000000001' because there is no input data after this time point.

Back to Report Summary

EI09_SWRS_Debouncer_4

Test Result Information

Result Type: Test Case Result

Parent: Debouncer

Start Time: 2021-12-18 19:26:22 End Time: 2021-12-18 19:26:25

Outcome: Passed

Test Case Information

Name: EI09_SWRS_Debouncer_4

Type: Baseline Test

Baseline Name: EI09_SWUT_MIL_Debouncer_05

Baseline File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

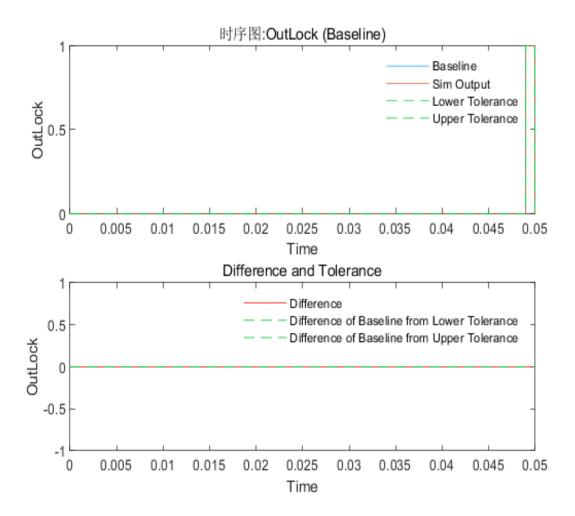
tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\TestCase_TCF.xlsx

Baseline Comparison

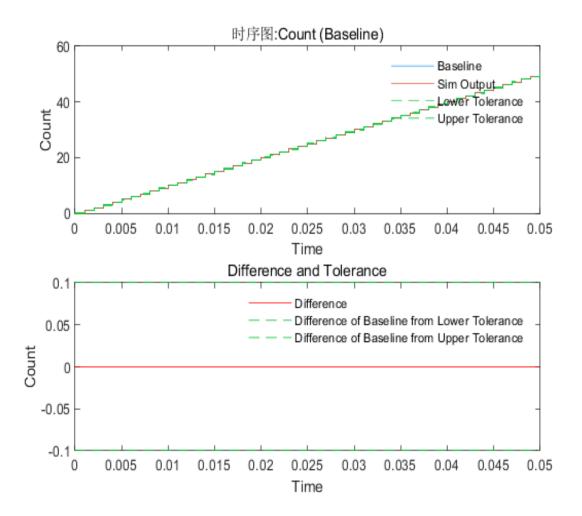
Name	Abs	Rel	Lead	Lag	Max	Data	Units	Sample	Data	Units	Sample	Interp	Sync	Link
Name	Tol	Tol	Tol	Tol	Diff	Type 1	1	Time 1	Type 2	2	Time 2	Intel p	Sync	to Plot
OutLock	0.1	0	0	0	0	boolean			boolean			zoh	union	<u>Link</u>
Count	0.1	0	0	0	0	int16			int16			zoh	union	<u>Link</u>
Deb	0.1	0	0	0	0	int16			int16			zoh	union	<u>Link</u>

Name	Abs	Rel	Lead	Lag	Max	Data T	Units	Sample	Data T	Units	Sample	Interp Sync
Turic	Tol	Tol	Tol	Tol	Diff	ype 1	1	Time 1	ype 2	2	Time 2	liner p by ne
OutLock	0.1	0	0	0	0	boolean			boolean			zoh union



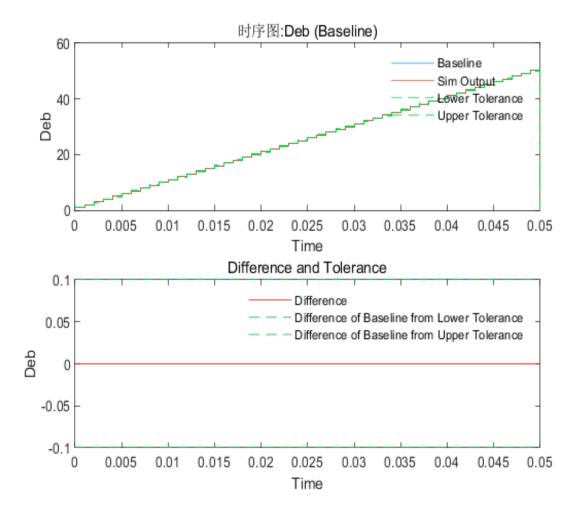
Back to Report SummaryBack to Criteria Results

Name	Abs	Rel	Lead	Lag	Max	Data T	Units	_	Data T	Units	•	Interp Sync
	Tol	Tol	Tol	Tol	Diff	ype 1	1	Time 1	ype 2	2	Time 2	
Count	0.1	0	0	0	0	int16			int16			zoh union



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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data T ype 1	Units 1	Sample Time 1	Data T ype 2	Units 2	Sample Time 2	Interp Sync
Deb	0.1	0	0	0	0	int16			int16			zoh union



Back to Report SummaryBack to Criteria Results

EI09_SWUT_MIL_Debouncer_05

Baseline Information

Baseline Name: EI09_SWUT_MIL_Debouncer_05

Baseline File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

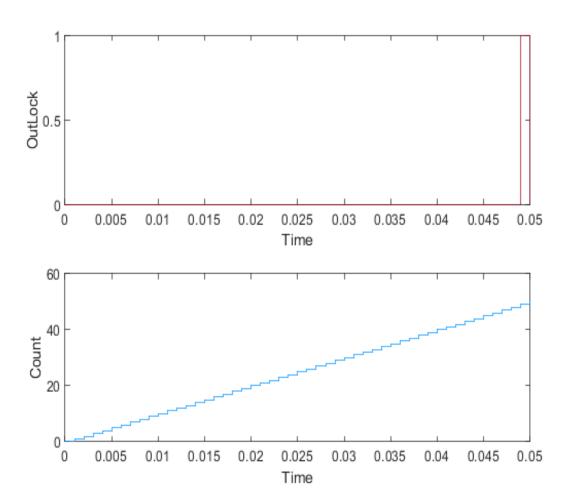
tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\TestCase_TCF.xlsx

Name	Data Type	Units	Sample Time	Interp	Sync	Link
			<u> </u>	L		to Plot
OutLock	boolean			zoh	union	<u>Link</u>

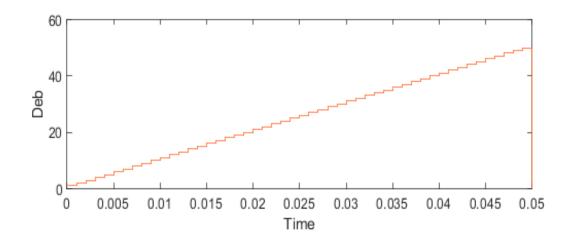


Name	Data Type	Units	Sample Time	Interp	Sync
OutLock	boolean			zoh	union
Count	int16			zoh	union



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Name	Data Type	Units	Sample Time	Interp	Sync
Deb	int16			zoh	union



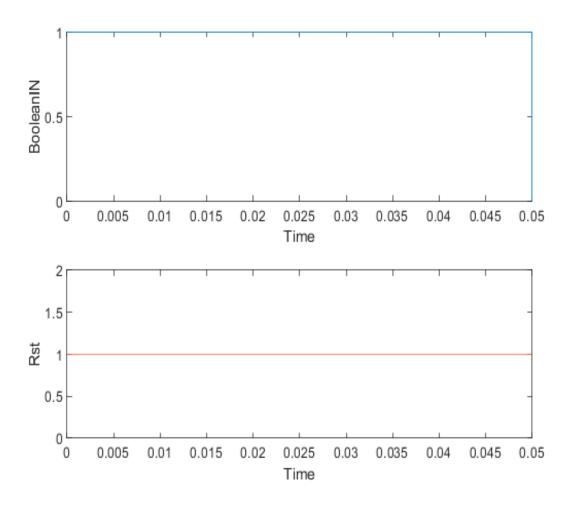
Input Data

Input Information

External Input Na EI09_SWUT_MIL_Debouncer_05 me:

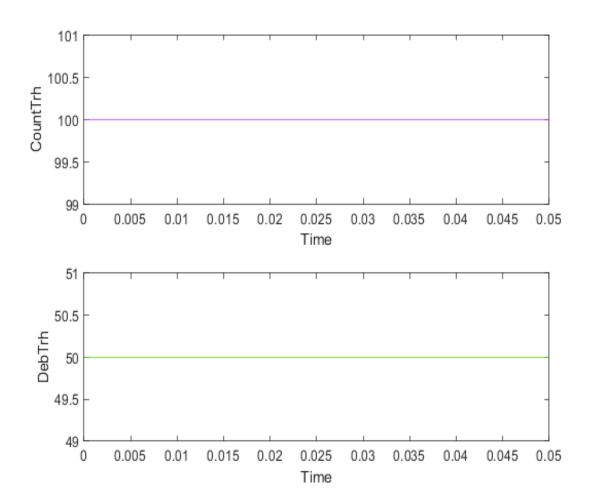
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
BooleanIN	boolean			zoh	union	<u>Link</u>
Rst	boolean	Г — — — — I		zoh	union	<u>Link</u>
CountTrh	int16	Г — — — — I		zoh	union	<u>Link</u>
DebTrh	int16	 		zoh	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
BooleanIN	boolean			zoh	union
Rst	boolean	 _		zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
CountTrh	int16			zoh	union
DebTrh	int16			zoh	union



Simulation

System Under Test Information

Model: SWC_TCF

Harness: SWC_TCF_Harness_Debouncer

Harness Owner: SWC_TCF/SWC_TCF_1ms_sys/Debouncer

Simulation Mode: normal

Override SIL or PIL Mod 0

e:

Configuration Set: Configuration 1

External Input Name: EI09_SWUT_MIL_Debouncer_05

External Input File: E:\EI09_Project\ei09\03_Controller_Models\02_Pla

tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\TestCase_TCF.xlsx

Start Time: 0

Checksum: 944823193 3446748209 4091885579 3684280232

Simulink Version: 10.1 Model Version: 1.1

Model Author: dongliyuan

Date: Tue Aug 10 16:53:31 2021

User ID: dongliyuan

Model Path: E:\EI09 Project\ei09\03 Controller Models\02 Pla

tform_Models\01_Platformmodels\FS\TCF\TCF_V2

\SWC_TCF.slx

Machine Name: MC-ZHANGJUNRENB Solver Name: FixedStepDiscrete

Solver Type: Fixed-Step

Fixed Step Size: 0.001

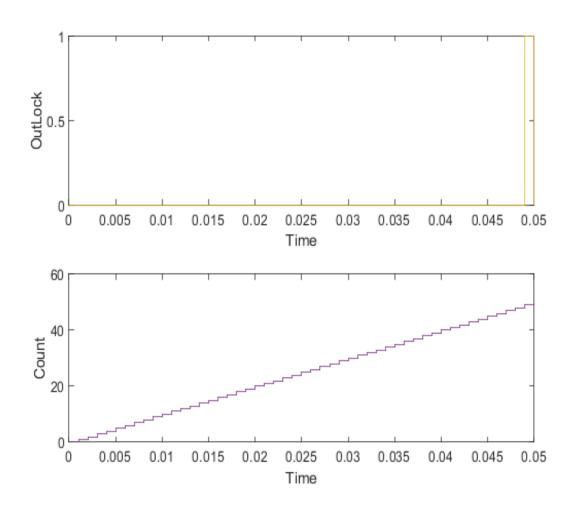
Simulation Start Time: 2021-12-18 19:26:22 Simulation Stop Time: 2021-12-18 19:26:23

Platform: PCWIN64

Simulation Output

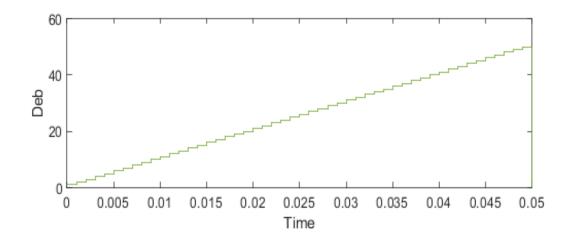
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
OutLock	boolean			zoh	union	<u>Link</u>
Count	int16			zoh	union	<u>Link</u>
Deb	int16			zoh	union	<u>Link</u>

Name	Data Type	Units	Sample Time	Interp	Sync
OutLock	boolean			zoh	union
Count	int16			zoh	union



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Name	Data Type	Units	Sample Time	Interp	Sync
Deb	int16			zoh	union



Back to Report Summary