Report Generated by Test Manager

Title:	**STM_TrqMonEnable MIL Test Report**
Author:	
Date:	**19-Dec-2021 10:14:25**

Test Environment

Platform:	PCWIN64
MATLAB:	(R2020a)

Summary

Name	Outcome	Duration (Seconds)
📁 [TrqMonEnable](#)	✅	2.321
📄 [New Test Case 1](#)	✅	2.323

TrqMonEnable

Test Result Information

Result Type:	Test Suite Result
Parent:	None
Start Time:	2021-12-19 10:10:51
End Time:	2021-12-19 10:10:54
Outcome:	Total: 1, Passed: 1

Test Suite Information

Name:	TrqMonEnable

Aggregated Coverage Results

Analyzed Model	Sim Mode	Complexity	Condition	Execution
SWC_STM/SWC_STM_1ms_sys/TrqMonProc/TrqMonEnable	Normal	1	100%	100%

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New Test Case 1

Test Result Information

Result Type:	Test Case Result
Parent:	TrqMonEnable
Start Time:	2021-12-19 10:10:51
End Time:	2021-12-19 10:10:54
Outcome:	Passed

Test Case Information

Name:	New Test Case 1
Type:	Baseline Test
Baseline Name:	EI09_SWUT_MIL_TrqMonEnable_01
Baseline File:	E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\STM\STM_V2\TestCase_STM.xlsx

Baseline Comparison

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync	Link to Plot
✅ Enable	0.1	0	0	0	0	boolean			boolean			zoh	union	Link

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✅ Enable	0.1	0	0	0	0	boolean			boolean			zoh	union
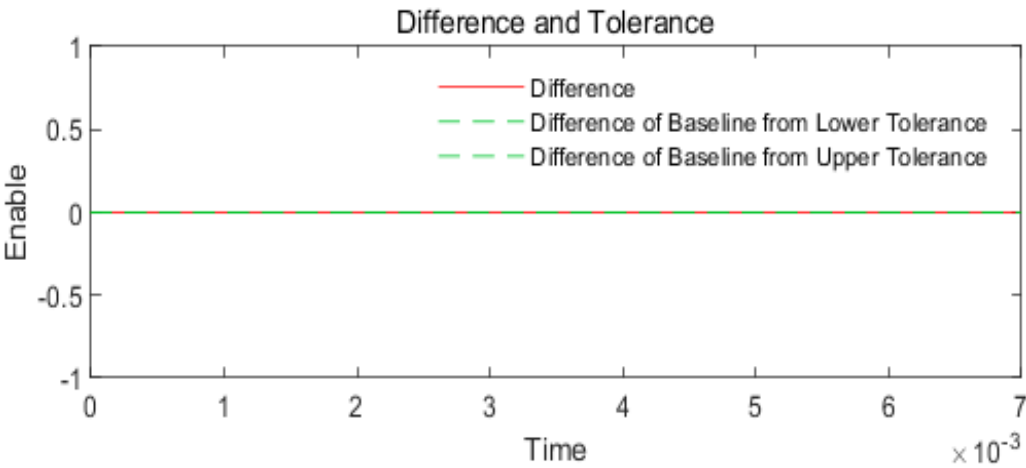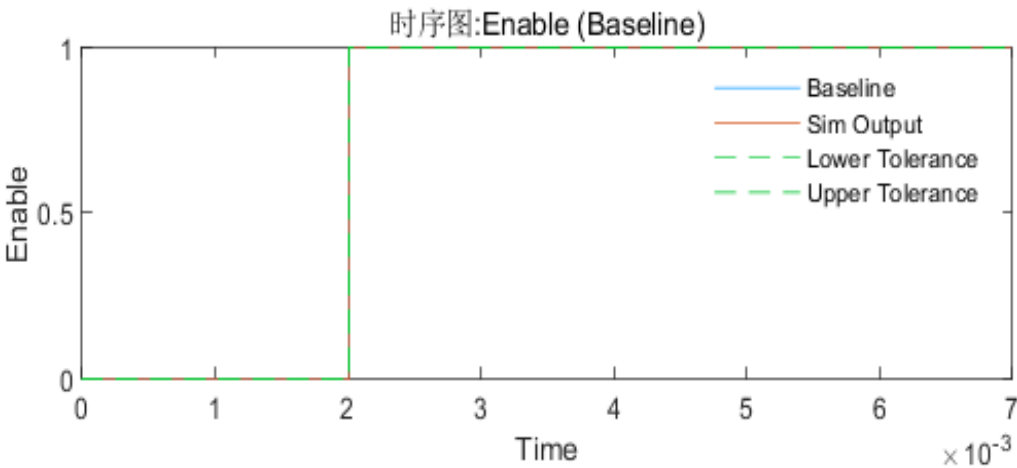
[Back to Report Summary](#)[Back to Criteria Results](#)
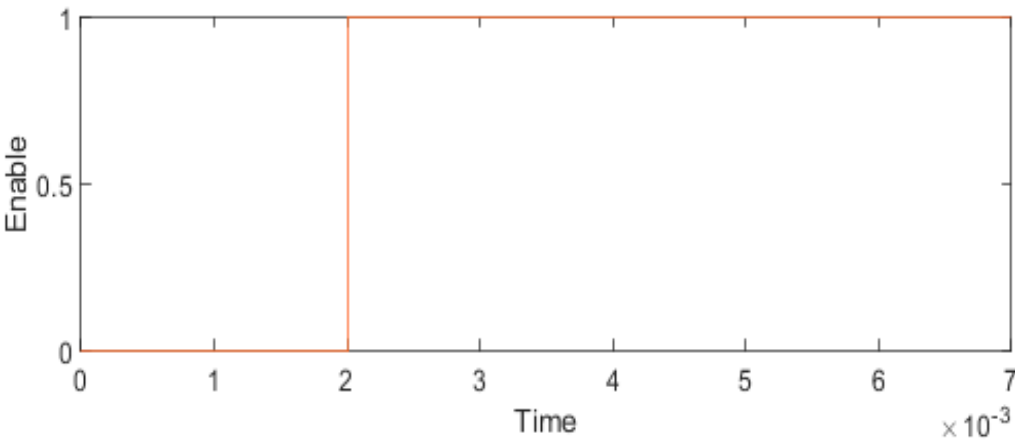
EI09_SWUT_MIL_TrqMonEnable_01

Baseline Information

Baseline Name: EI09_SWUT_MIL_TrqMonEnable_01
Baseline File: E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\STM\STM_V2\TestCase_STM.xlsx

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
Enable	boolean			zoh	union	Link

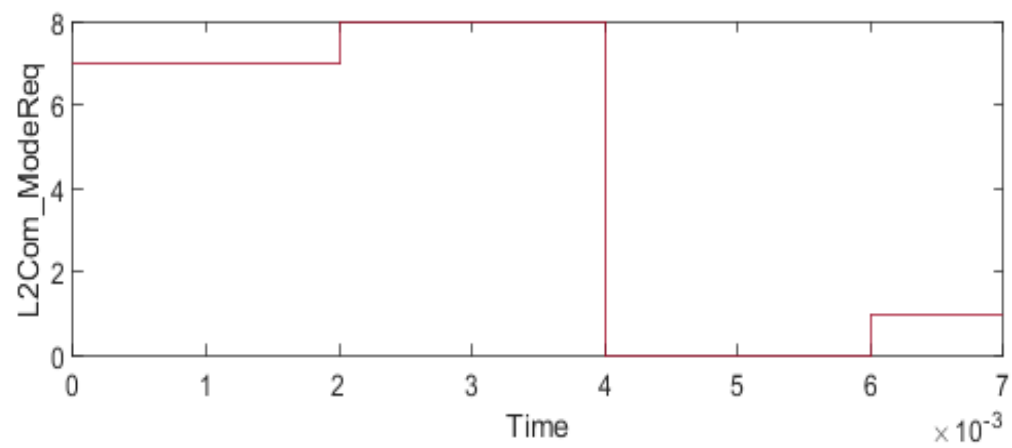| Name	Data Type	Units	Sample Time	Interp	Sync
Enable	boolean			zoh	union

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Input Data

Input Information

External Input Name: EI09_SWUT_MIL_TrqMonEnable_01

External Input File: E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\STM\STM_V2\TestCase_STM.xlsx

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
L2Com_ModeReq	uint8			zoh	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
L2Com_ModeReq	uint8			zoh	union

Simulation

System Under Test Information

Model: SWC_STM
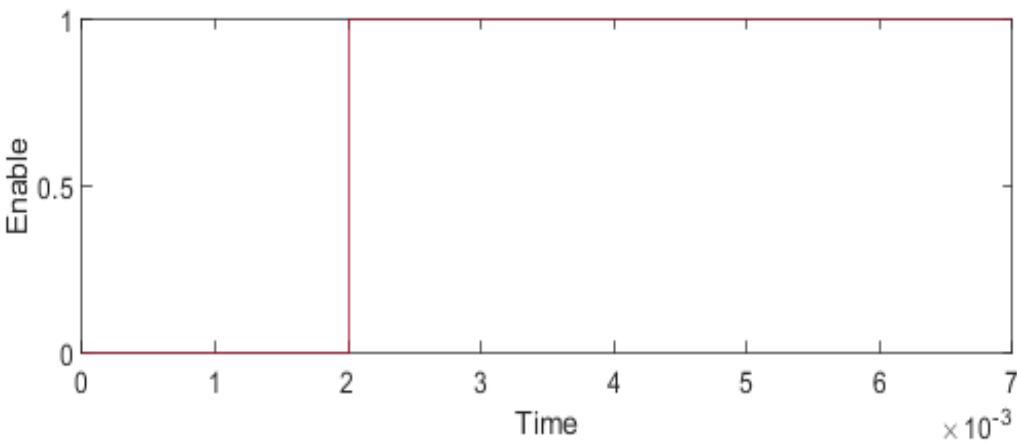Harness: SWC_STM_Harness_TrqMonEnable

Harness Owner:	SWC_STM/SWC_STM_1ms_sys/TrqMonProc/TrqMonEnable
Simulation Mode:	normal
Override SIL or PIL Mode:	0
Configuration Set:	Configuration2
External Input Name:	EI09_SWUT_MIL_TrqMonEnable_01
External Input File:	E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\STM\STM_V2\TestCase_STM.xlsx
Start Time:	0
Stop Time:	0.0070000000000000001
Checksum:	3830738242 64312031 791950402 1987613069
Simulink Version:	10.1
Model Version:	1.1
Model Author:	dongliyuan
Date:	Thu Dec 02 18:52:01 2021
User ID:	dongliyuan
Model Path:	E:\EI09_Project\ei09\03_Controller_Models\02_Platform_Models\01_Platformmodels\FS\STM\STM_V2\SWC_STM.slx
Machine Name:	MC-ZHANGJUNRENB
Solver Name:	FixedStepDiscrete
Solver Type:	Fixed-Step
Fixed Step Size:	0.001
Simulation Start Time:	2021-12-19 10:10:51
Simulation Stop Time:	2021-12-19 10:10:52
Platform:	PCWIN64

Simulation Output

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
Enable	boolean			zoh	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
Enable	boolean			zoh	union

Simulation Logs:
Simulation stopped at '0.0070000000000000001' because there is no input data after this time point.

Symbol 'CAL_STM_SpeedCtlMode_u8' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'CAL_STM_UxptTrqLim_f32' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'CAL_STM_UxptTrqMonCountTrh_s16' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'CAL_STM_UxptTrqMonDebTrh_s16' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'CAL_STM_UxptTrqMonErrRst_b' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'GLB_STM_MotorFw_u8' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'GLB_STM_MotorStop_u8' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'GLB_STM_PosvTrq_u8' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'GLB_STM_ZeroTrq_u8' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'VAR_STM_UintndTrqErr_b' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'VAR_STM_UintndTrqMonRslt_b' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'boolean' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'float32' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'float64' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'sint16' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'sint32' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'sint64' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'sint8' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'uint16' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'uint32' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'uint64' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.

Symbol 'uint8' is defined in SWC_STM_DataDictionary.sldd and base workspace. The definition in SWC_STM_DataDictionary.sldd is used.