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Questions

Multiplexing divides ___ line into ___ channels.

In ___, the medium is divided into a number of channels, each with a frequency bandwidth.

___ is a analog multiplexing technique.

___ is a digital multiplexing technique.

___ is a intelligent multiplexing technique.

Multiplexing divides the physical line(medium) into logical segments called ___.

A ___ separates the signal meant for different destinations and sends them appropriately.

The ___ is responsible for both multiplexing and demultiplexing.

If input signals are ___, they need to be passed through both modems and multiplexers.

We can use data compression technique in ___.

Three physical lines are divided into five logical channels in Multiplexing.

In FDM, the medium is divided into a number of channels, each with a frequency bandwidth.

WDM is a analog multiplexing technique.

Digital multiplexing can be implemented by FDM technique.

Statistical TDM, a multiplexing technique, monitors a machine for idealness to allocate the time slices.

Multiplexing divides the physical line(medium) into physical segments called partitions.

A demultiplexer collects various signals from different sources for different destinations and sends them appropriately.

Both multiplexing and demultiplexing can be performed using the MUX.

Digital input signals need to be passed through both modems and multiplexers.

We can implement data compression technique in WDM.

___ is caused because the signals at different frequencies travel at different speeds along the medium.

As a signal travels through any medium, its strength decreases due to ___.

Some electromagnetic energy can get inserted somewhere during transmission, which is normally called ___.

Overhead bits are added to data in case of ___.

The receiver sends a ___ back to the sender if everything was ok.

In ___ sender sends one frame and waits for an acknowledgement before sending the next frame.

The ___ defines how much data the sender can send before it must wait to receive an acknowledgement from the receiver.

Delay distortion is caused because the signals at different frequencies travel at ___ speeds along the medium.

Attenuation ___ with distance.

CRC calculation is based on ___ portion of data.

The receiver sends a ___ acknowledgement if there is any error in the received data.

In ___ method, sender must send all the frames again starting from negative acknowledged frame.

Delay distortion is caused because the signals at different frequencies travel at different speeds along the medium.

Attenuation decreases with distance.

CRC calculation is based on specific portion of data.

The receiver sends a negative acknowledgement if there is any error in the received data.

In the sliding window method, only when the first byte is acknowledged by the receiver then the sender would send more frames.

Single-bit-errors caused when one bit of the data unit changes.

Noise reduces the strength of a signal as it travels through any medium.

Some electromagnetic energy can get inserted somewhere during transmission, which is normally called Distortion.

Parity check technique is also called as VRC.

The receiver sends a signal NAK back to the sender if everything was ok.

In Go Back N technique, sender sends one frame and waits for an acknowledgement before sending the next frame.

The stop and wait, sender retransmits all the frames from an errorful frame.

MCQ

[illegible]

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In _____, either a 0 bit changes to 1 or a 1 bit changes to 0.
In burst errors, _____ two bits get changed during data transmission.
In _____ errors, two or more bits get changed due to any errorful reason.
Burst errors has only 1 bit change in data.
Some mathematical process is applied on the data to calculate _____.
_____ is the position dependent checksum method.
In _____, original data is arranged as rows to calculate checksum.
A single bit error effectively changes a 0 bit to 1 or a 1 bit to 0.
CRC is calculated using some mathematical process on data.
_____ is the checksum algorithm.
In Go Back N, either a 0 bit changes to 1 or a 1 bit changes to 0.
Stop and wait technique applies some mathematical process on data.
Burst errors changes multiple bits during transmission of data bits.
In _____, parity bit is calculated for each column of n-bit sized different rows of a list and new row of n bits is created.
Parity bit can be either 0 or 1.
Parity bit must be 1 for odd scheme and 0 for even scheme.

Single bit error	1 bit error	multiple bits error	only bit error
exactly	at least	maximum	None of the above
multiple	single-bit	burst	None of the above
TRUE	FALSE	----	----
checksum	CRC	VRC	LRC
VRC	CRC	LRC	None of the above
VRC	CRC	LRC	None of the above
TRUE	FALSE	----	----
TRUE	FALSE	----	----
parity check	modular sum	position dependent	All of the above
TRUE	FALSE	----	----
TRUE	FALSE	----	----
TRUE	FALSE	----	----
VRC	CRC	LRC	None of the above
TRUE	FALSE	----	----
TRUE	FALSE	----	----