

000 UPPER CHANNEL IDENTIFICATION LABEL (R)-UPPER CHANNEL INDICATOR LIGHT (RED LED) UPPER CHANNEL DETECTOR NUMBER (h UPPER CHANNEL TEST SWITCH (SEE NOTE 7) INPUT FILE SLOT NUMBER 0.375"-LOWER CHANNEL TEST SWITCH (SEE NOTE 7) 0.375 LOWER CHANNEL DETECTOR NUMBER LOWER CHANNEL INDICATOR LIGHT (RED LED) 000 LOWER CHANNEL IDENTIFICATION LABEL TYPICAL VERTICAL

SECTION DETAIL

DETAIL

Α

BILL

ΒY

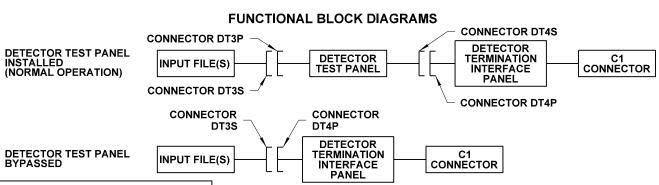
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 (18) (19) (20) (21) (22) (23) (24) (25) (26) (27) (28) (29) (30) (31) (32) (33) 34) 35) 36) 37) 38) 39) 40) 41) 42) 43) 44) 45) 46) 47) 48) 49) 50)

DD50 D-SUB CONNECTOR PINS

PLUG (MALE) CONNECTOR SHOWN ~ MIRROR FOR SOCKET (FEMALE) CONNECTOR ~

NOTES

- 1. Upper and lower channel identification labels shall match the detector channels shown in the Contract
- 2. Connectors DT3S, DT3P, DT4S, and DT4P are Type DD50 D-Sub connectors with pin layouts and assignments as shown. The suffix "S" indicates a socket (female connector) and the suffix "P" indicates a plug (male connector).
- 3. Detector Termination Interface Panel terminals not shown due to variations in arrangement and numbering between manufacturers.
- 4. Connectors DT3P and DT4S shall be installed in one of the following arrangements:
 - a) Mounted to the back of the Detector Test Panel. Connectors shall use a spring latch (bail) to secure the connection.
 - b) Mounted on a cable, within six inches of the back of the Detector Test Panel. Connectors shall use thumb-screws to secure the connection.
- 5. Connectors DT3S and DT4P shall be designed such that they can be connected directly, bypassing the **Detector Test Panel**.
- 6. The Detector Termination Interface Panel shall be installed electrically between the Detector Test Panel and the C1 connector. A second additional terminal block may be installed electrically between the Input File(s) and the Detector Test Panel.
- 7. Test switches shall be three position switches with the "Test" position being a momentary contact with spring return to the "OFF" position. Test switch position functions shall be as described in Standard Specification section 9-29.13(10).
- 8. Location of the Display On/Off switch is approximate. This switch shall be located to the right of all of the individual channel test switches and clear of the mounting rack.



CONNECTOR PIN ASSIGNMENTS (SEE NOTE 3) **CONNECTOR DT3S CONNECTOR DT4P CONNECTOR DT3P CONNECTOR DT4S** PIN CONNECT FUNCTION TO TO TO TO TO TO TO то I-1F J-3F DET. 23 I1U - IN J3U - IN 1 | I1U - OUT 26 J3U - OU C1 - 56 C1 - 64 DET. 23 DET. 25 2 I2U - IN C1 - 48 DET 25 I-2F DET. 3 J-4F 27 J4U - IN 2 I2U - OUT 27 J4U - OUT C1 - 39 DET. 3 27 I-3F DET. 5 28 J-5F DET. 27 3 | I3U - IN 28 J5U - **IN** 3 | I3U - OUT 28 J5U - OUT 3 C1 - 63 DET. 5 C1 - 57 DET. 27 I-4F J-2W DET. 22 4 I4U - IN J2L - IN 4 | I4U - OUT 29 J2L - OUT C1 - 47 DET. 7 C1 - 44 DET. 22 5 DET. 24 I-1W 5 I1L - OUT 30 J3L - OUT C1 - 77 DET. 2 J-3W DET. 24 I1L - IN 30 J3L - IN C1 - 56 DET. 2 DET. 26 31 J4L - IN 6 I2L - OUT 31 J4L - OUT DET. 26 I-2W DFT 4 31 J-4W 6 I2I - IN C1 - 43 DFT 4 31 C1 - 48 1-3W DET. 6 32 J-5W DET. 28 7 | I3L - IN 32 J5L - IN 7 | I3L - OUT 32 J5L - OUT C1 - 76 DET 6 32 C1 - 57 DET 28 DET. 8 J-6F DET 29 33 J6U - IN 8 I4L - OUT 33 J6U - OUT DET. 8 DET. 29 I-4W 8 | I4L - IN 8 C1 - 47 C1 - 42 34 J7U - OUT .J-7F 9 I5U - IN DET. 31 1-5F DET. 31 34 J7U - **I**N 9 | I5U - OUT C1 - 58 DFT 9 C1 - 66 I-6F DFT 11 .J-8F DET. 33 10 I6U - IN 35 J8U - **IN** 10 I6U - OUT 35 J8U - OUT C1 - 41 DFT 11 C1 - 50 DET. 33 11 I7U - IN I-7F DET. 13 J-9F DET. 35 36 J9U - **I**N 11 I7U - OUT 36 J9U - OUT 11 C1 - 65 DET. 13 C1 - 59 DET. 35 J-6W DET. 30 12 I8U - IN 12 I8U - OUT 37 J6L - OUT 37 C1 - 46 DET. 30 I-8F DFT 15 J6I - IN 12 C1 - 49 DET. 15 I-5W DET. 10 J-7W DET. 32 I5L - IN J7L - IN 13 I5L - OUT 38 J7L - OUT C1 - 58 DET. 10 38 C1 - 79 DET. 32 I-6W DET. 12 39 J-8W DET. 34 14 I6L - IN 39 J8L - IN 14 I6L - OUT 39 J8L - OUT C1 - 45 DET. 12 39 C1 - 50 DET. 34 15 1-7W DET. 14 40 J-9W DET. 36 | 15 | I7L - IN 40 J9L - IN 15 | I7L - OUT 40 J9L - OUT C1 - 78 DET 14 40 C1 - 61 DET. 36 16 I8L - OUT 1-8W DET. 16 18L - IN NC C1 - 49 DET. 16 NC NA 16 NC NA 41 NC NA 17 I9U - OUT DET. 17 NC NA 19U - IN NC NA NC NA C1 - 60 DET. 17 NC NA 18 I12U - IN Ø2 PED I-12F Ø2 PED NC NA NC NA 18 112U - OUT 43 NC NA C1 - 67 NC NA I-13F Ø6 PED NC NA 19 I13U - IN NC NA 19 113U - OUT 44 NC NΑ C1 - 68 Ø6 PED NC NA J-1F DET. 19 45 NC NA 20 J1U - IN NC NA 20 J1U - OUT 45 NC NΑ C1 - 55 DET. 19 NC NA 21 I9L - OUT 46 I-9W DET. 18 NC NA 21 19L - IN NC NA 46 NC NΑ C1 - 62 DET. 18 NC NA Ø4 PED NC 22 | I12L - IN NC 22 I12L - OUT NA Ø4 PED 47 NC NA I-12W NA NA NC. C1 - 69 NC 23 I13L - OUT C1 - 70 Ø8 PED 48 NA I-13W Ø8 PED 48 NA 23 | I13L - IN NC NA 48 NC NA NC 24 J1L - OUT 25 J2U - OUT POWER 24 J1L - IN GROUND 25 J2U - IN POWER POWER 24 C1 - 55 DET. 20 25 C1 - 40 DET. 21 DET. 20 J-1W DET. 20 49 115-1 +24 VDC NC NA 49 NC NA J-2F DET. 21 50 I15-2 50 LOGIC GND GROUND NC NA NA

PIN TABLE EXAMPLES:

J1F: Input File J, Slot 1, Terminal F

DET. 14: Detector #14

19U - IN: Detector Test Panel Position 19, Upper Channel Input Terminal

C1 - 58: C1 Connector, Pin 58

N/A: Not Applicable

NC: Not Connected



TYPE 332 SIGNAL CABINET DETECTOR TEST PANEL

STANDARD PLAN J-80.15-00

SHEET 1 OF 1 SHEET

APPROVED FOR PUBLICATION

