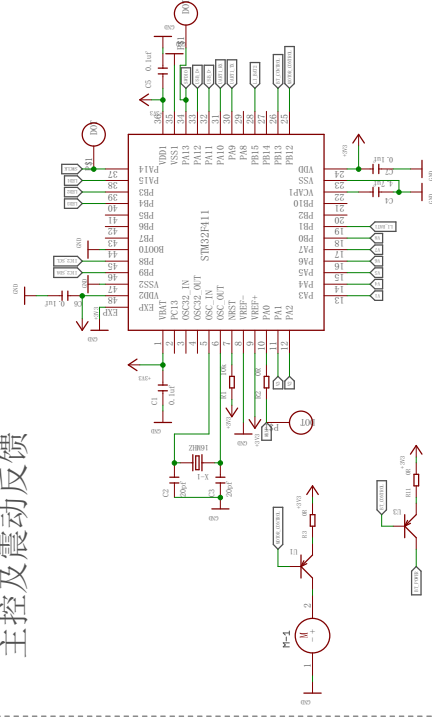
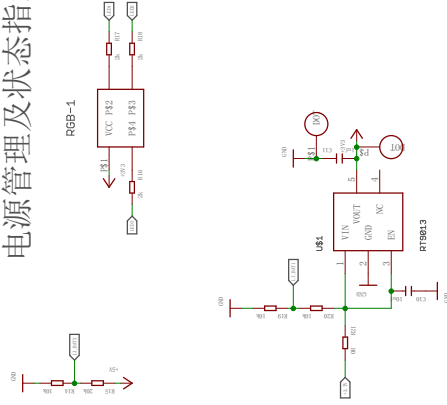


主控及震动反馈



The schematic diagram illustrates the BLE module circuit. At the center is the nRF52832 microcontroller. The power supply section includes a 3.3V regulator (LDO) and a 1.8V regulator (LDO), both connected to the VDD and VDD1 pins of the microcontroller. The microcontroller's pins are connected to various components: VDD to 3.3V, VDD1 to 1.8V, GND to ground, and various other pins to the antenna, USB-to-UART bridge, and other components. The antenna is connected to the ANTENNA pin. The USB-to-UART bridge is connected to the UART pins. The microcontroller's pins are also connected to various other components, including a 10k pull-up resistor, a 10k pull-down resistor, and a 10k pull-up resistor. The microcontroller's pins are also connected to various other components, including a 10k pull-up resistor, a 10k pull-down resistor, and a 10k pull-up resistor.

[illegible]