

Greedy Algorithm

Greedy Algorithm **Scheduling**

	Start	End
CIS 675	2	3.25
CIS 412	1	4
CIS 411	3	4
CIS 310	3.5	4.75
CIS 320	4	5.25
CIS 121	4.5	6
CIS 660	5	6.5
CIS 230	7	8

Problem Statement

(a_1, \dots, a_n)

(s_1, s_2, \dots, s_n)

(f_1, f_2, \dots, f_n) (sorted) $s_i < f_i$

Find largest subset of activities $C = \{a_i\}$ such that

For any two $a_i, a_j \in C$, such that $i < j$

$$f_i \leq s_j$$

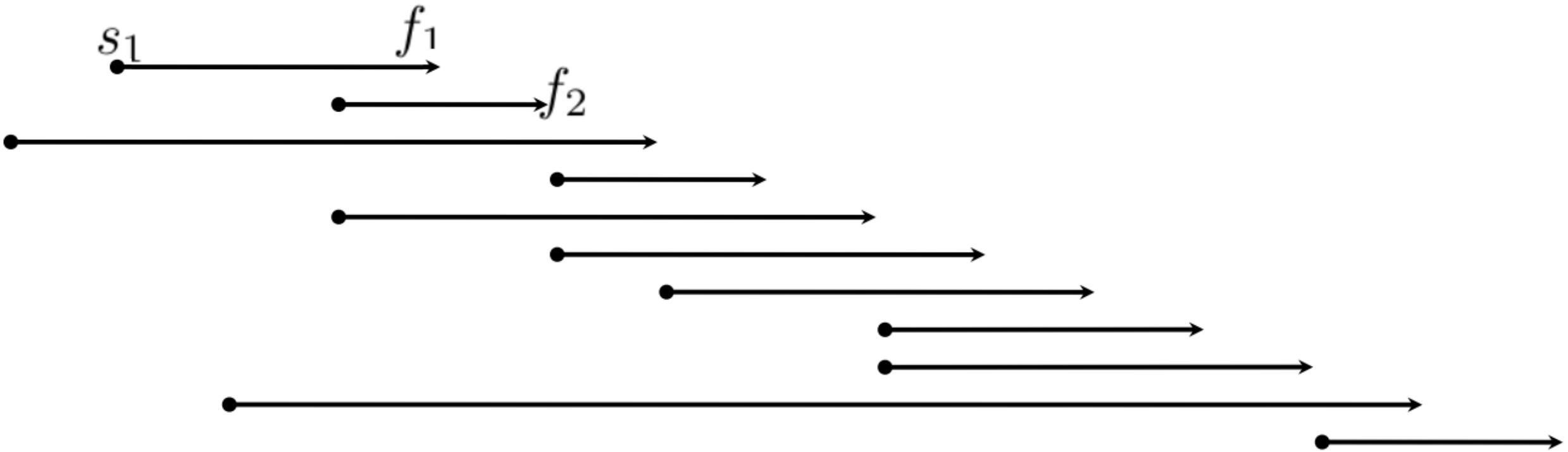
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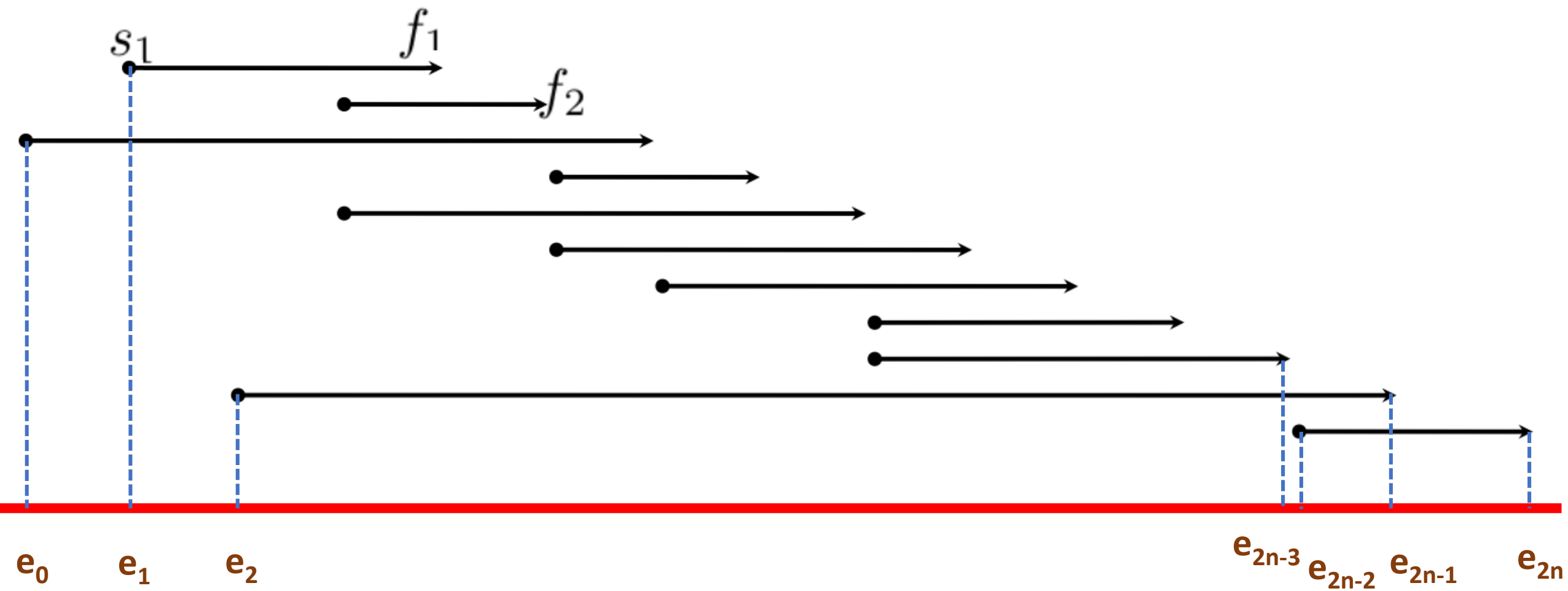
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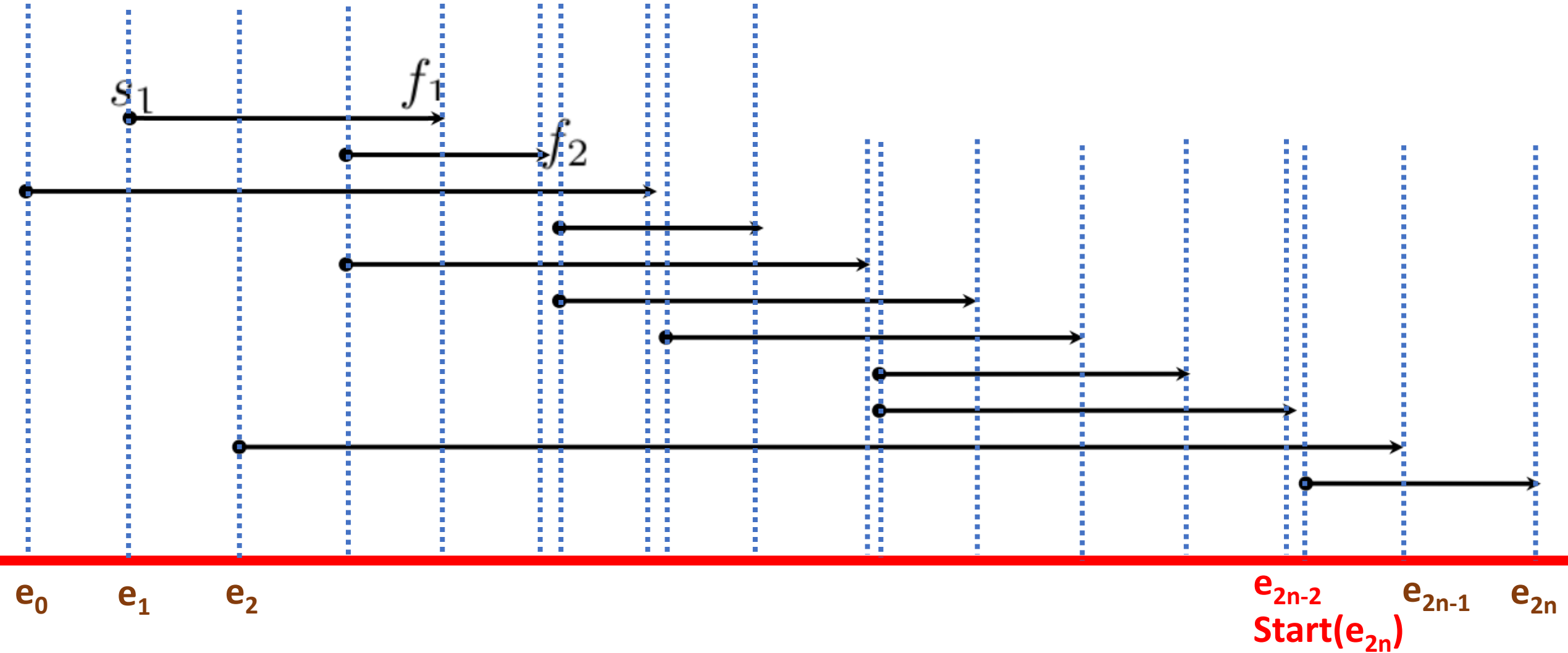
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DP Solution

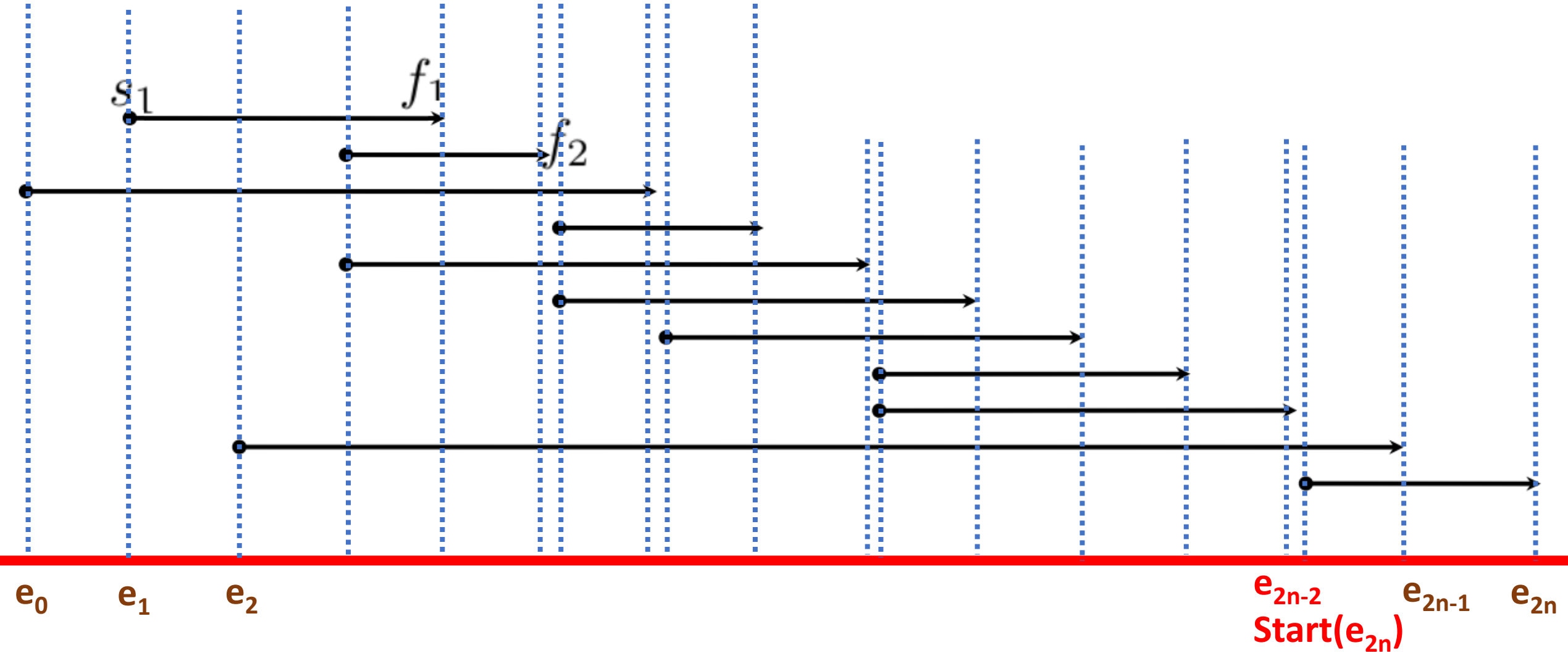


DP Solution



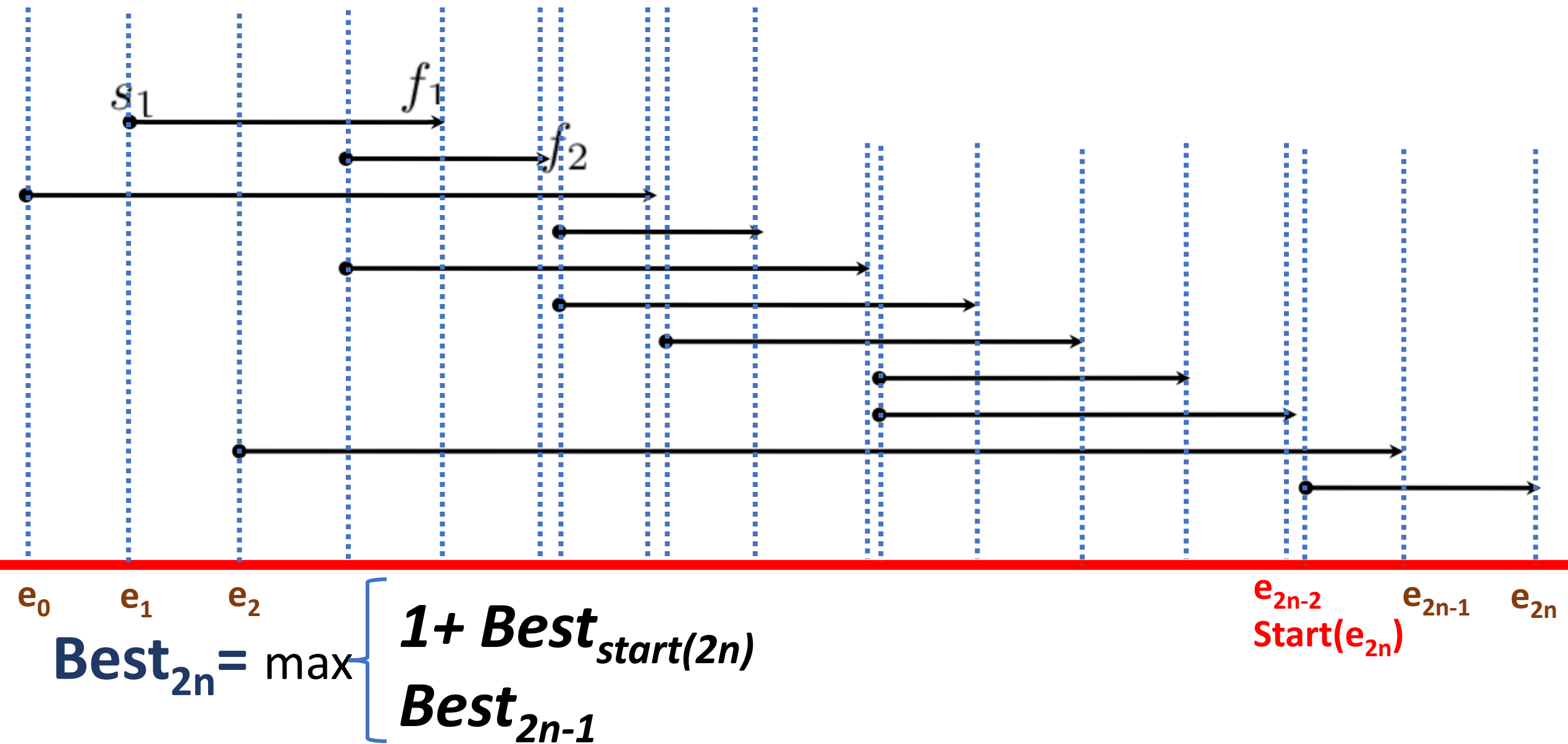
Best_n = maximal number of activities that can occur before event n

DP Solution

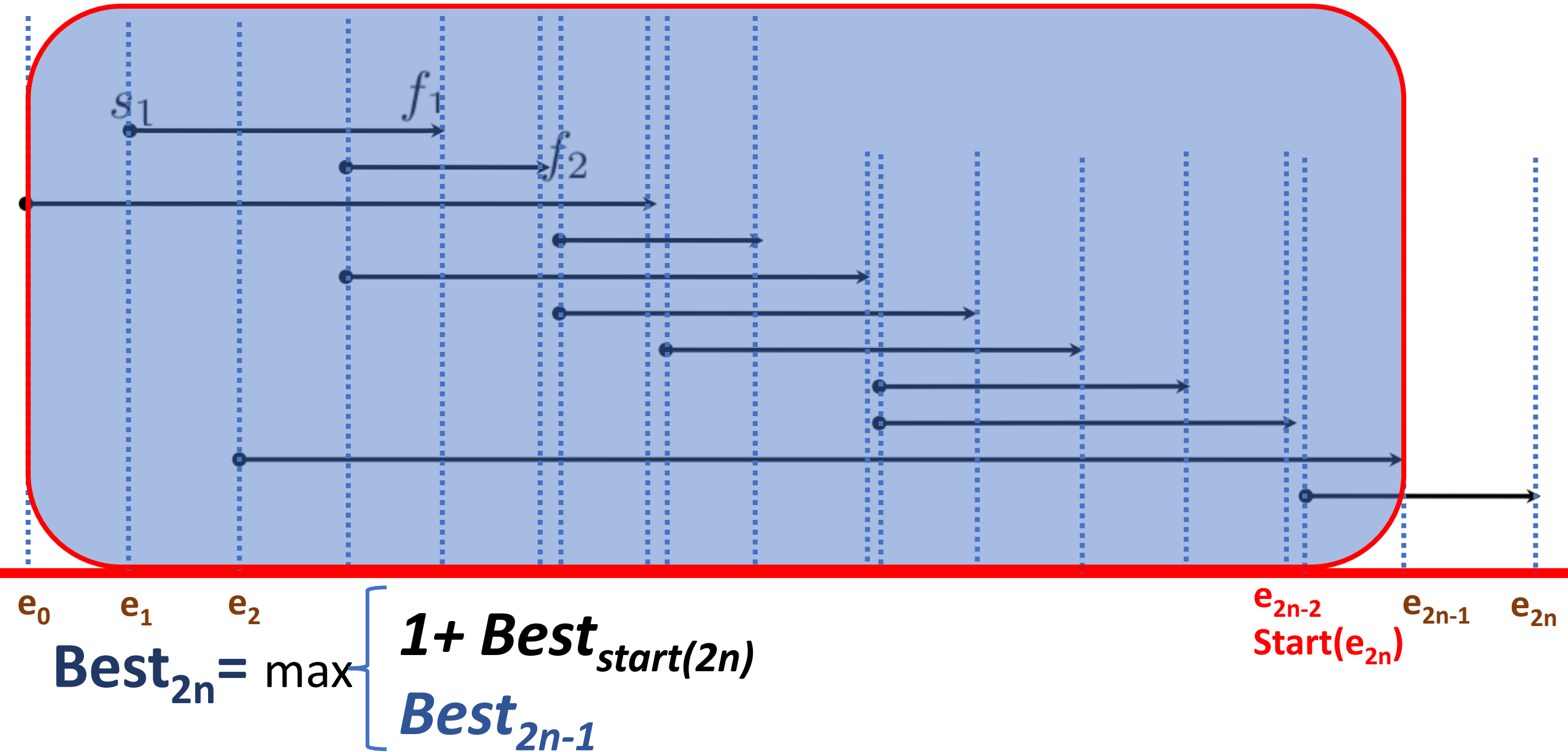


Best_{2n} = maximal number of activities that can occur before event 2n

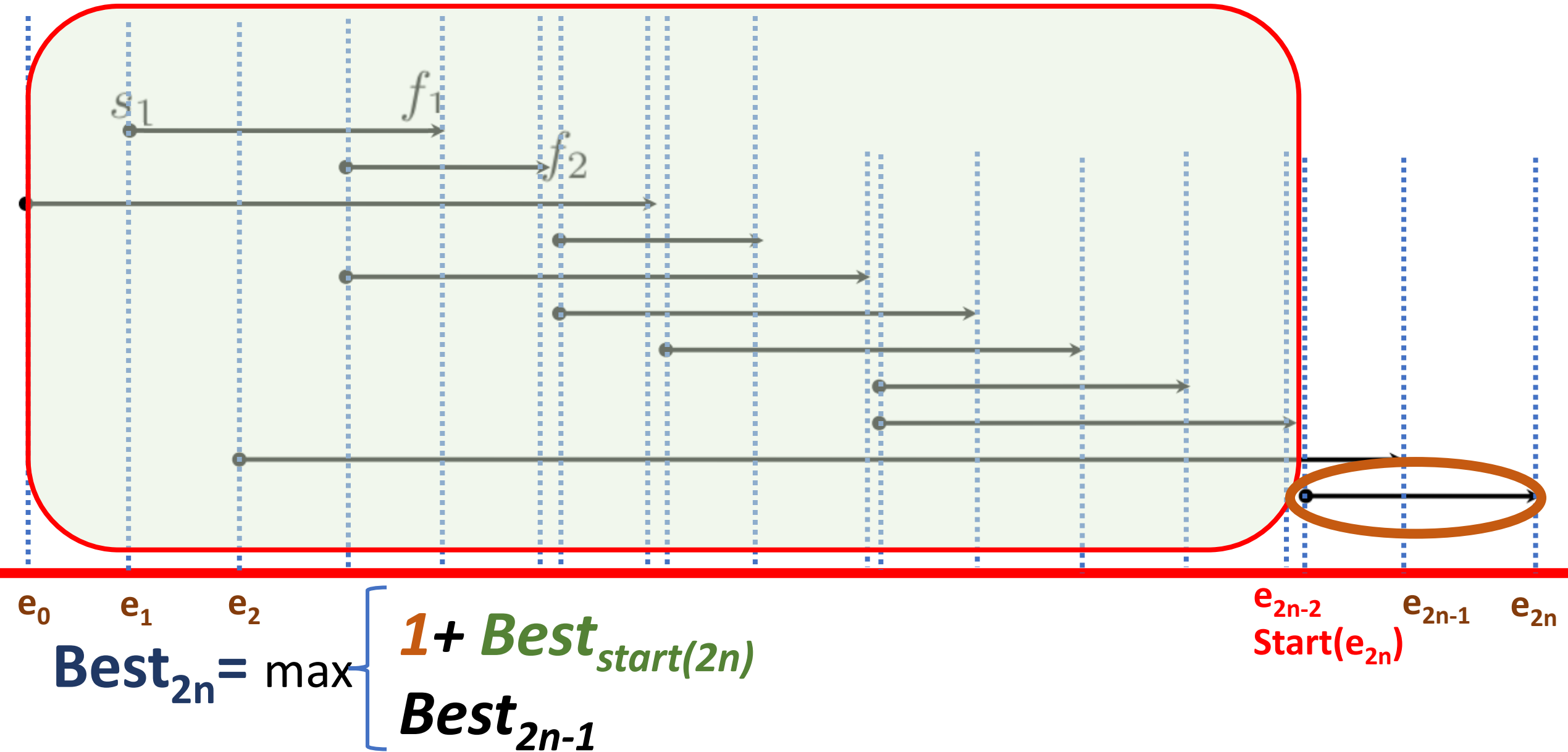
DP Solution



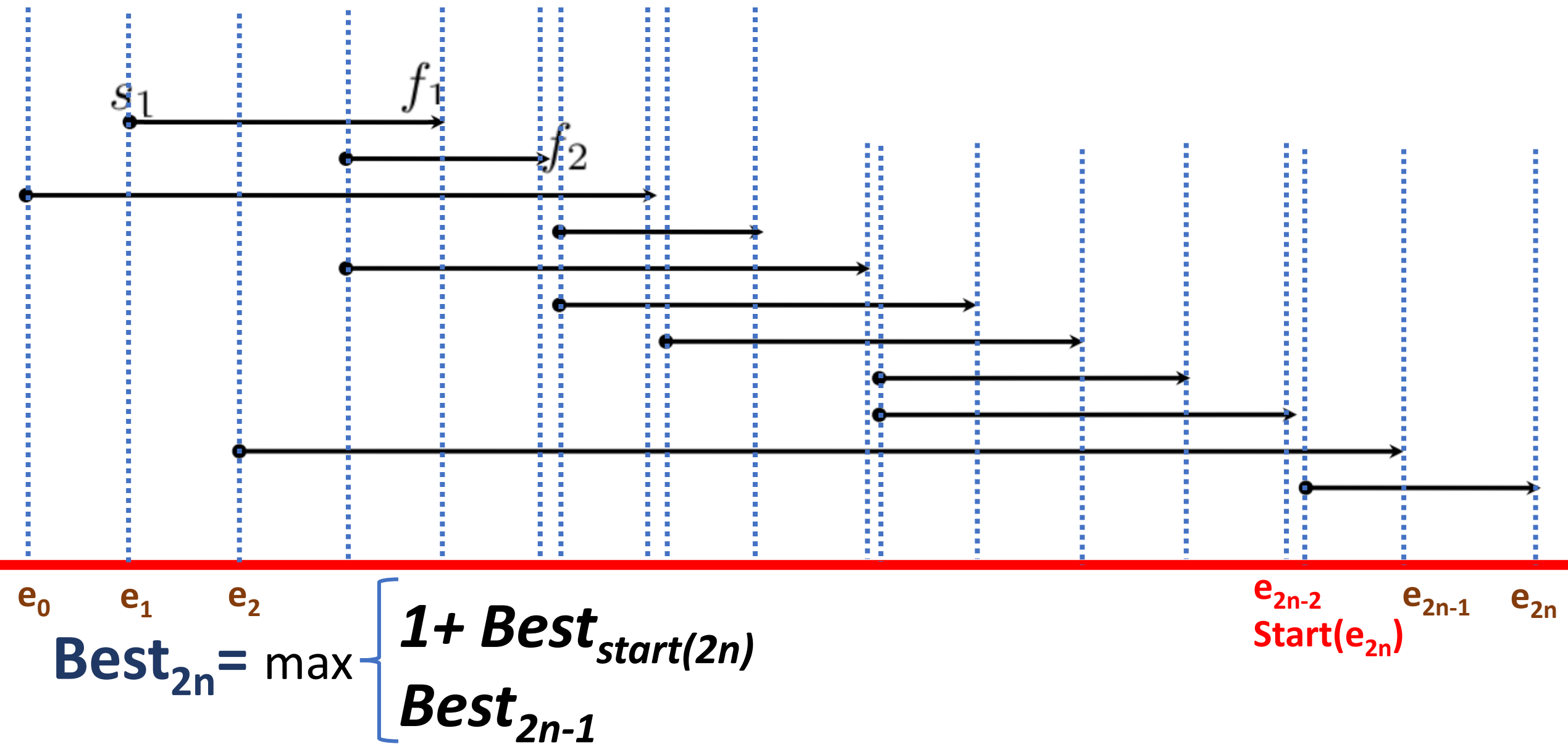
DP Solution



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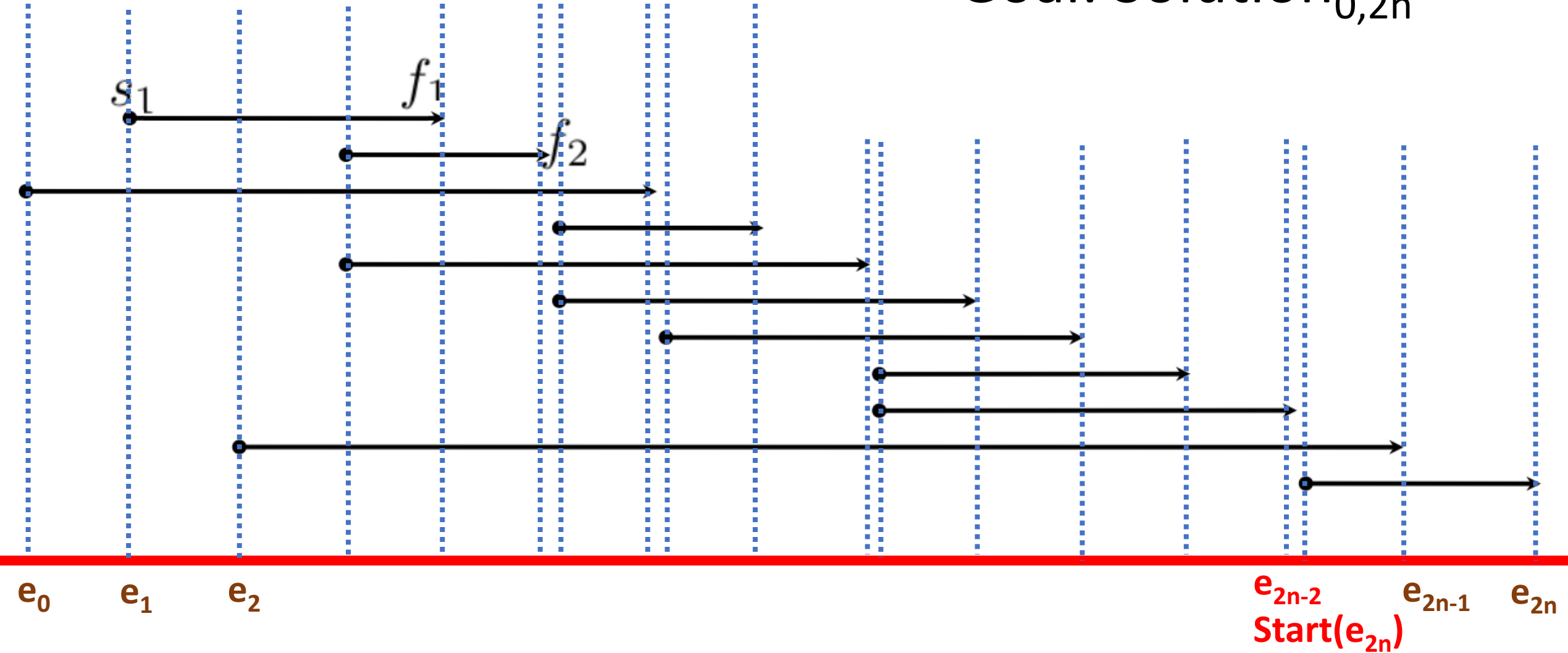


DP Solution



Greedy Solution

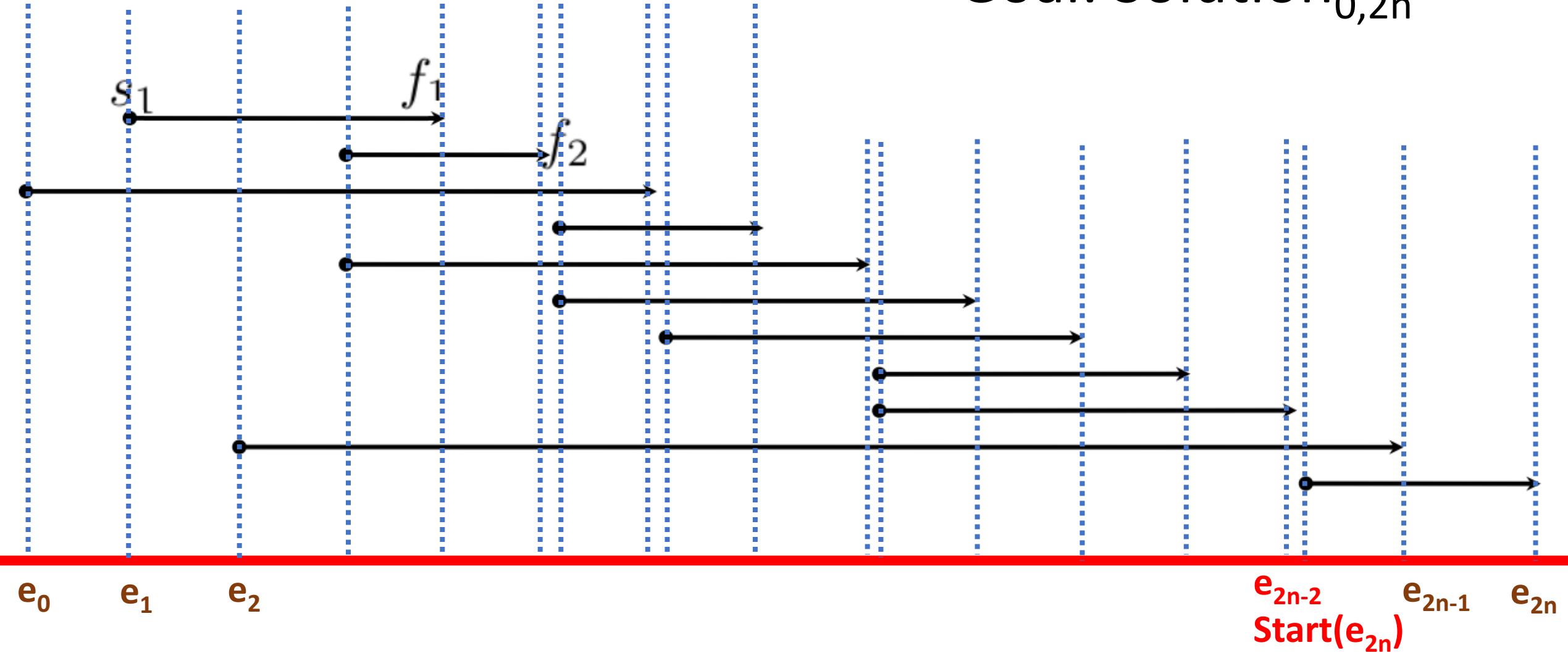
Goal: Solution_{0,2n}



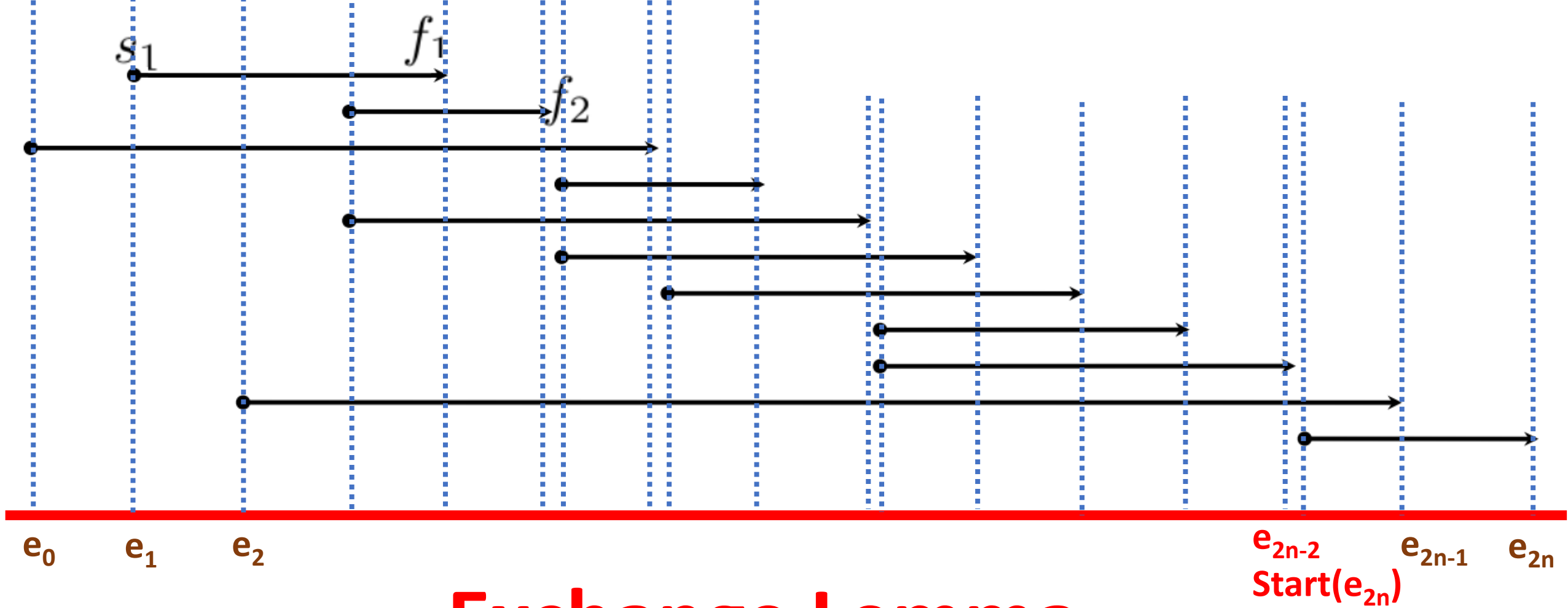
Solution_{i,j} = maximum number of activities that occur between events i, j

Greedy Solution

Goal: Solution_{0,2n}



Solution_{i,j} = maximum number of activities that occur between events i,j

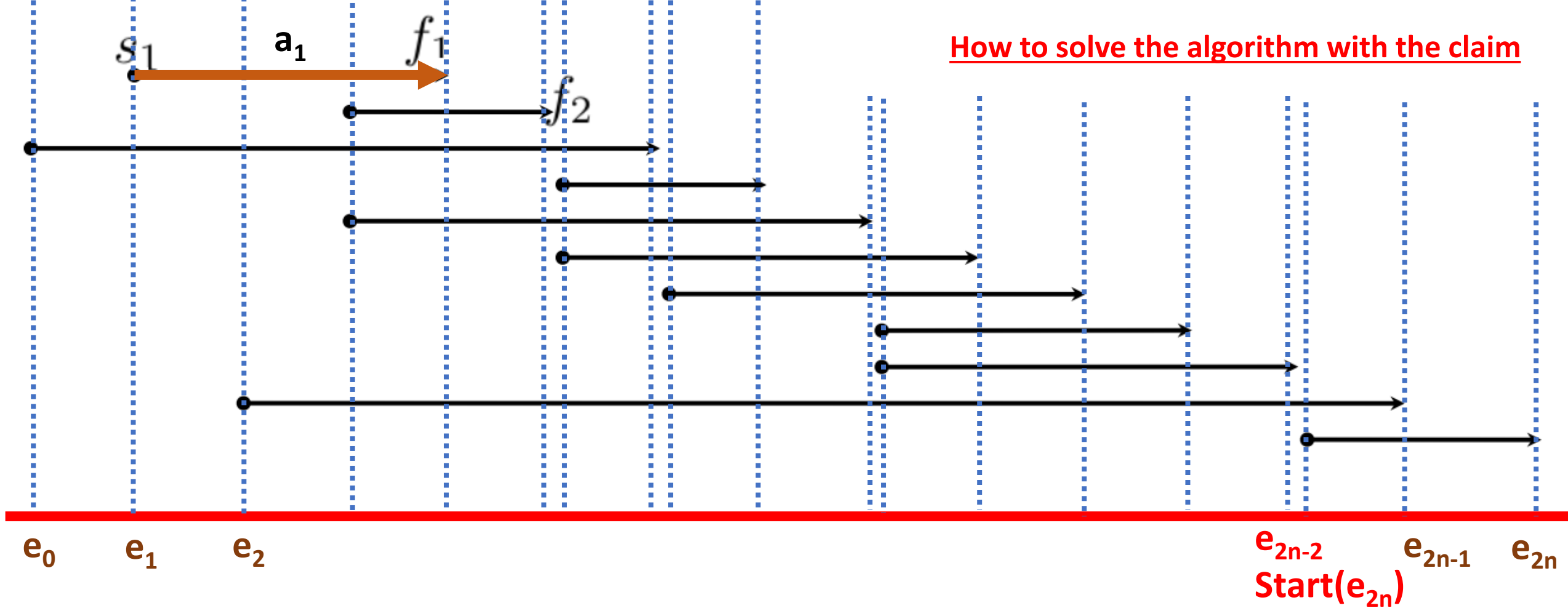


Exchange Lemma

Claim:

the first action to finish in $e[i,j]$ is always part of some optimal Solution _{i,j}

How to solve the algorithm with the claim

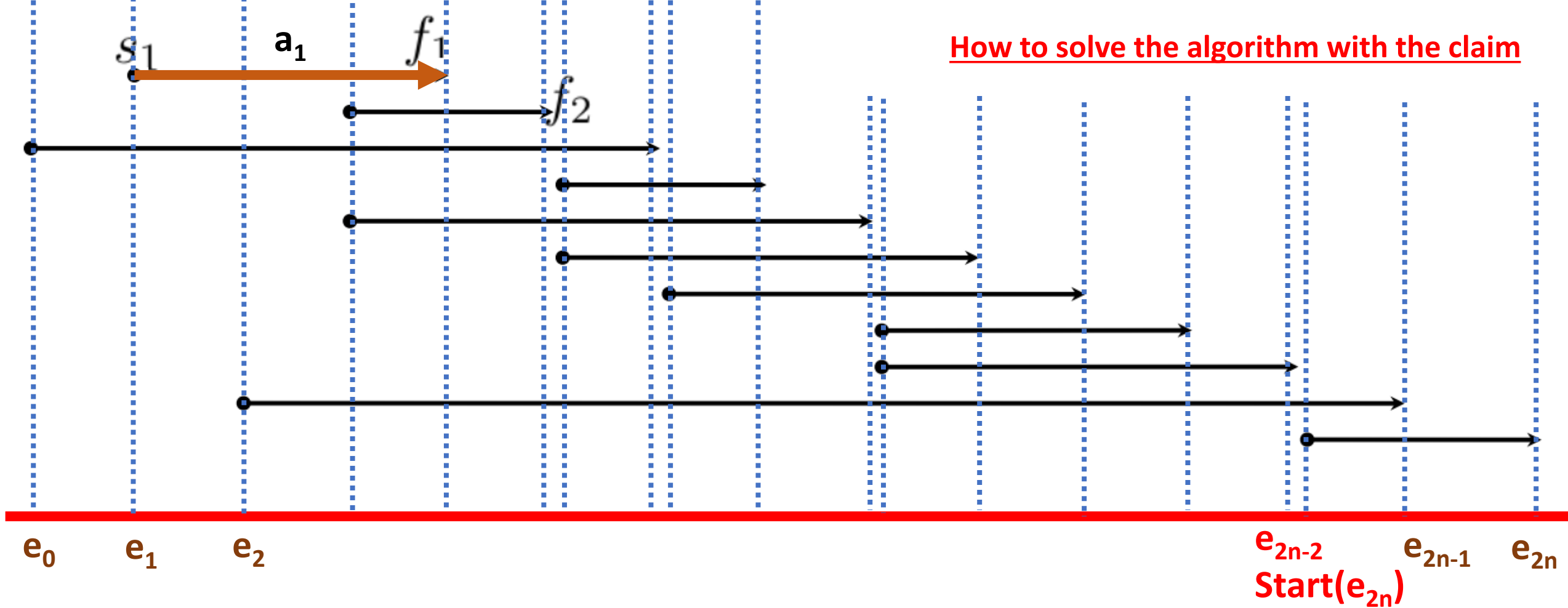


Claim:

the first action to finish in $e[i,j]$ is always part of some optimal Solution $_{i,j}$

a_1 : will always be part of at least one optimal solution

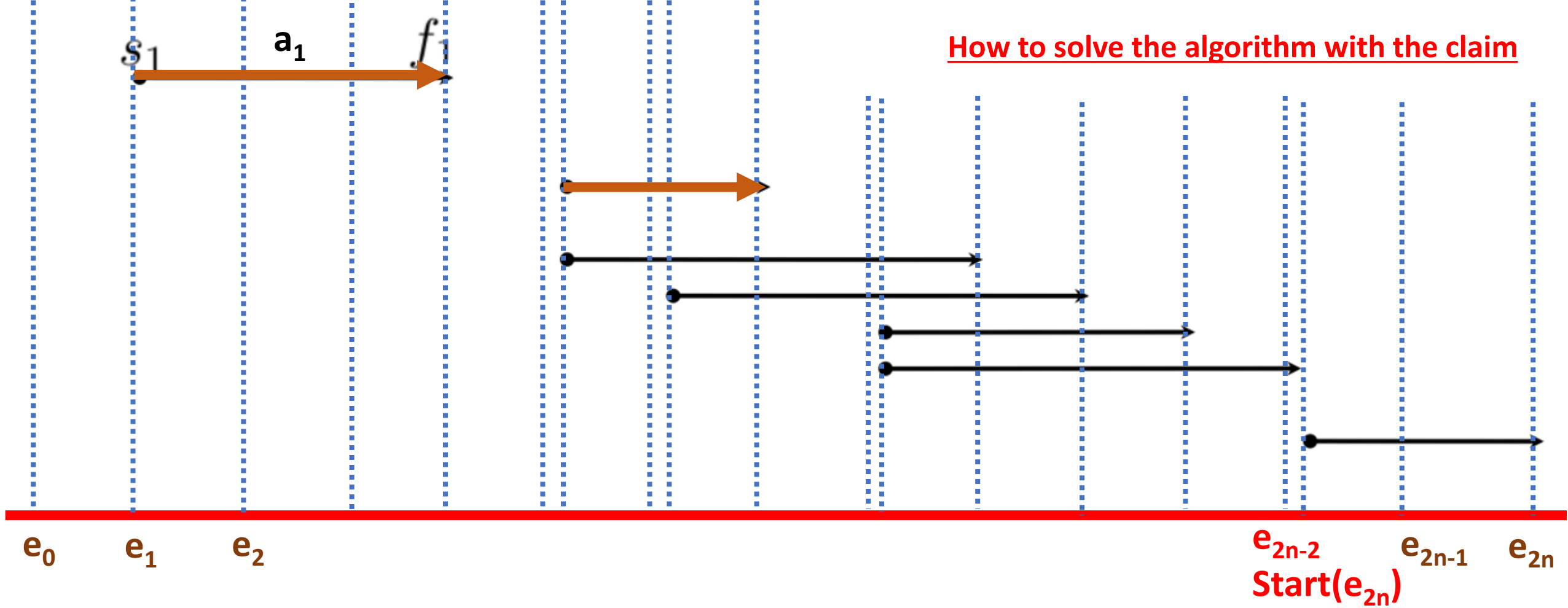
How to solve the algorithm with the claim



Algorithm: find first event to finish. add to solution. remove conflicting events. continue.

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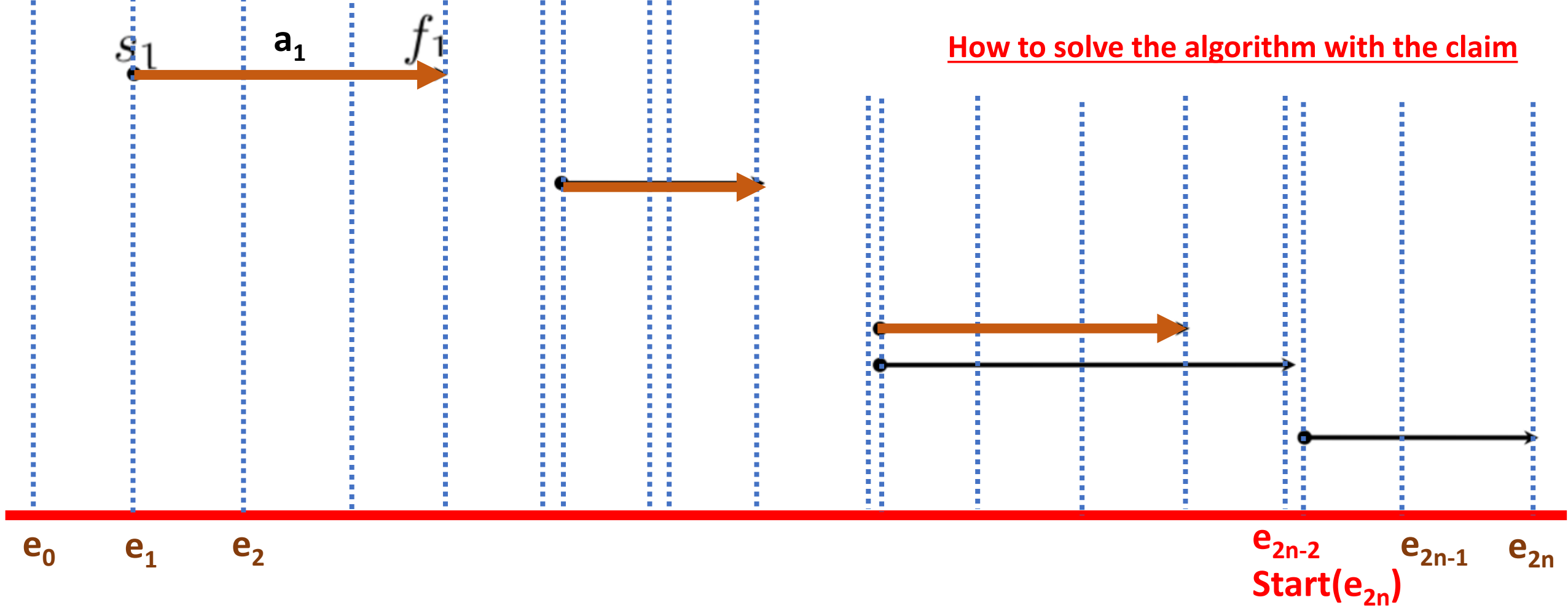
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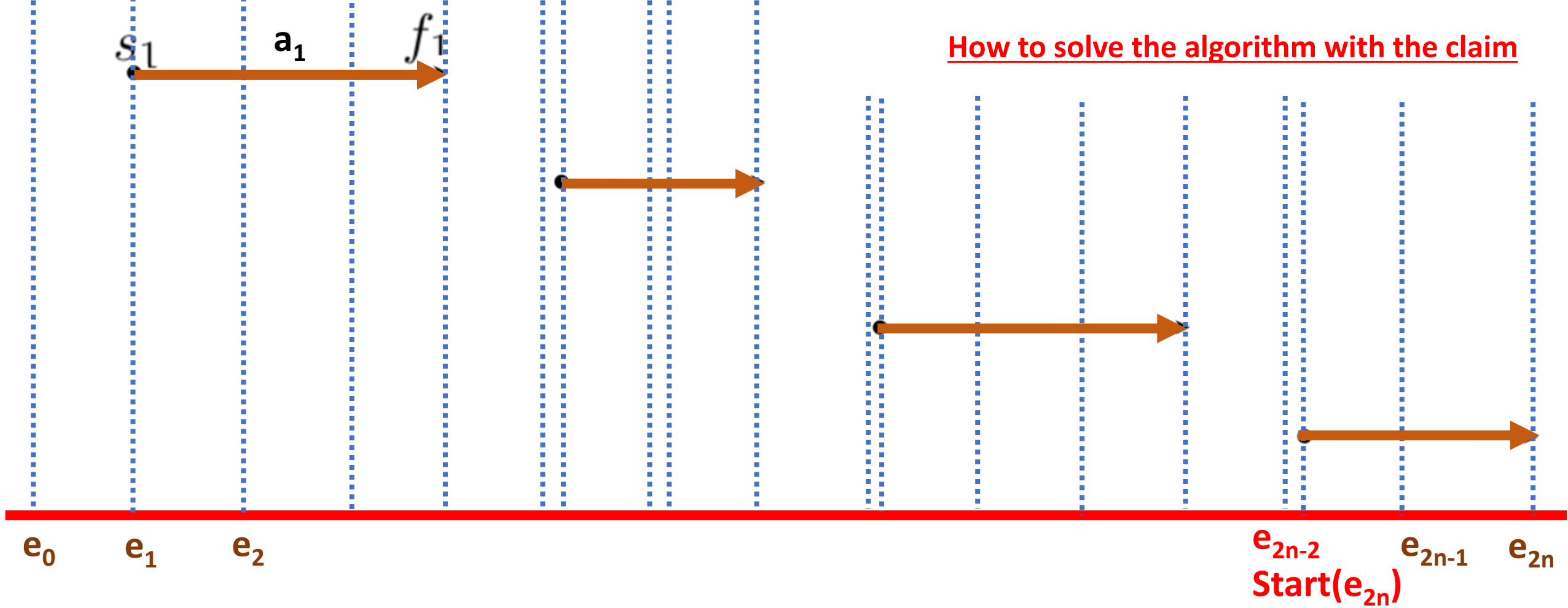
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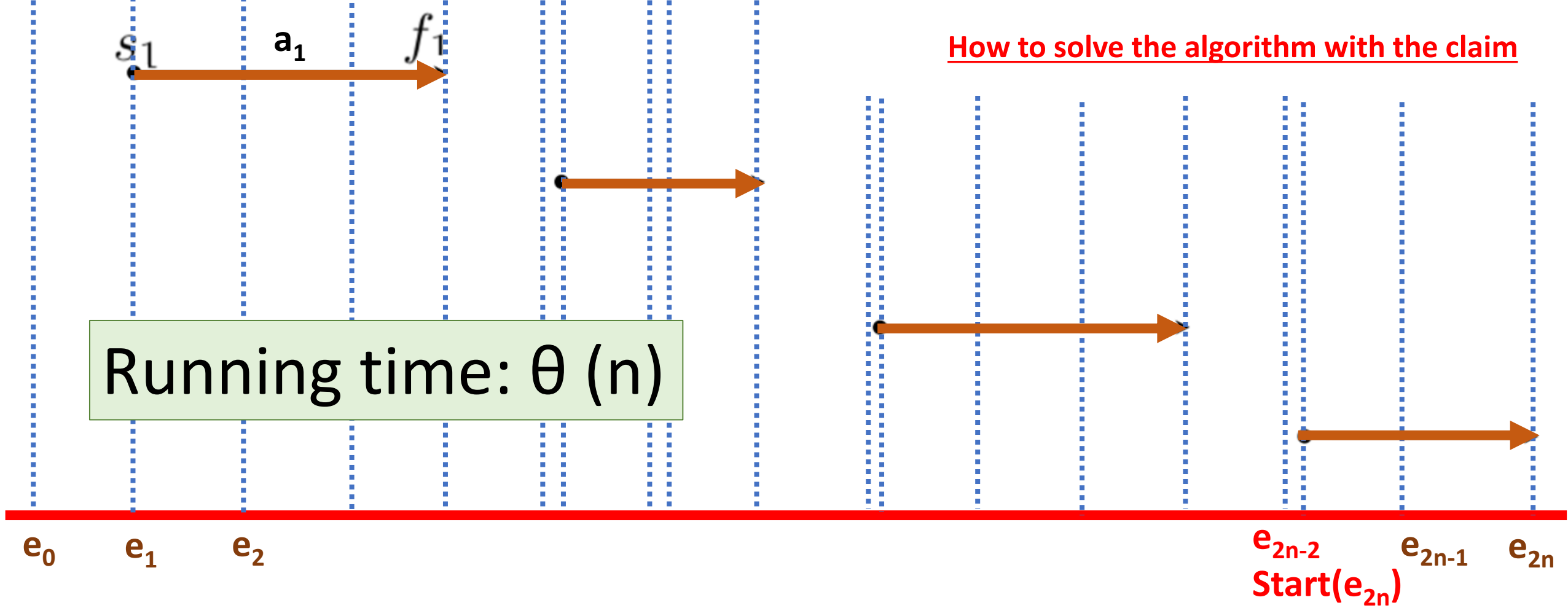
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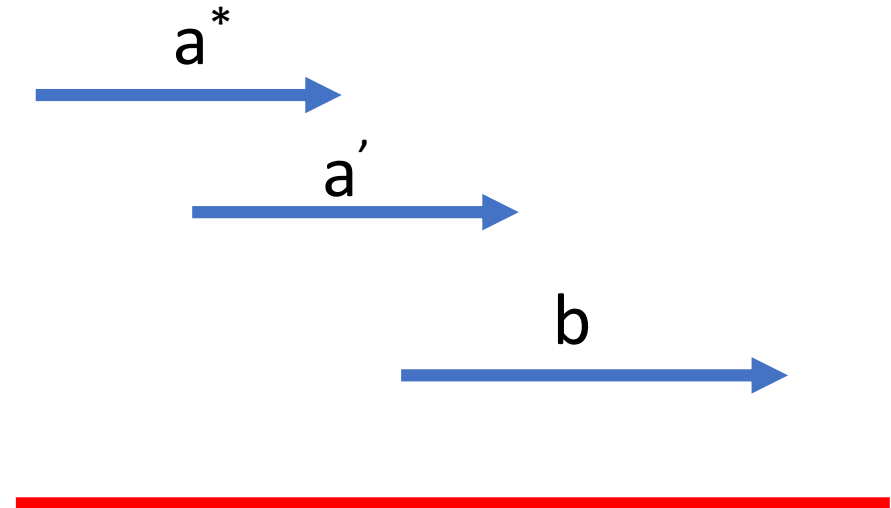
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Proof: Consider $\text{SOLUTION}_{i,j}$ and let A^* be the first to activity to finish in $[i,j]$

1. If $a^* \in \text{SOLUTION}_{i,j}$ then the claim follows.
2. Suppose that a^* is not in $\text{SOLUTION}_{i,j}$. Let activity a' be the first activity to finish the solution.



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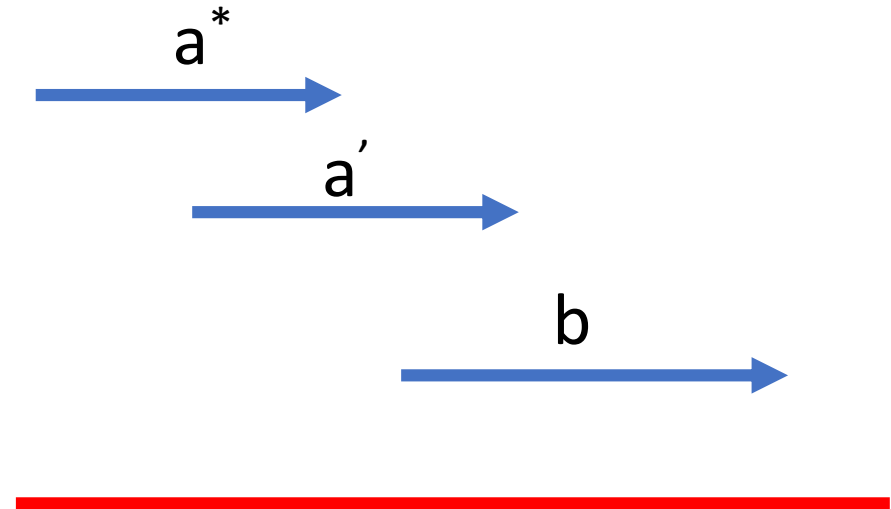
a^* finishes before a' , $f_{a^*} < f_{a'}$

So therefore,

$S = \text{SOLUTION}_{i,j} - \{a'\} + \{a^*\}$

Then, $S = \text{SOLUTION}_{i,j}$

So, S is optimal too. Therefore lemma follows.



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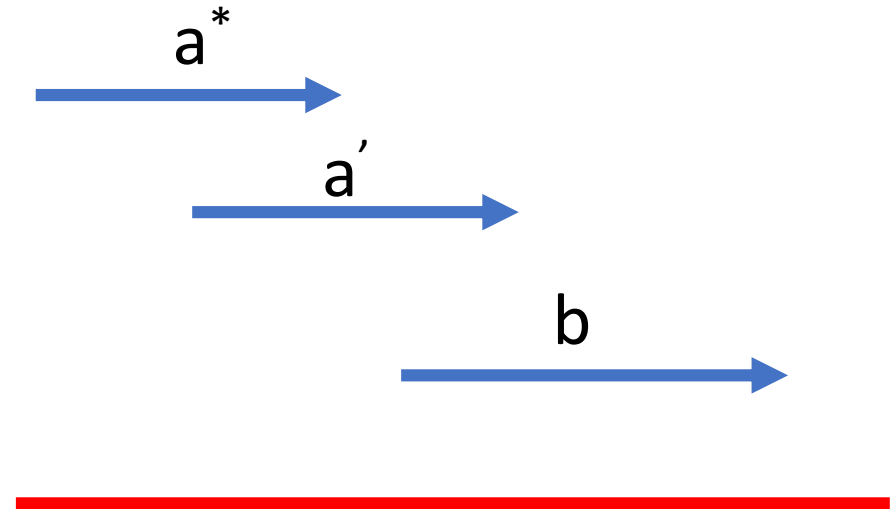
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Caching

Cache

Main
Memory

Virtual
Memory

CPU

load r2, addr a
store r4, addr b

Question of problem:

1. How can we manage a cache in order to minimize number of cache misses
2. Simplify the assumption that we know all memory accesses beforehand
3. Cache is fully associative, meaning, you know which data is in which cache address in $\theta(1)$ time.

problem statement

input: K , the size of the cache
 d_1, d_2, \dots, d_m memory accesses

output: schedule for that cache that minimizes # of cache misses while satisfying requests

cache is fully associative, line size is 1

Belady evict rule

*Evict the item from the cache that is accessed **farthest in the future.***

Example

cache



a b c d a d e a d b a e c e a

Example

cache

a
b
c

a
b
d

a b c d a d e a d b a e c e a

Example

cache

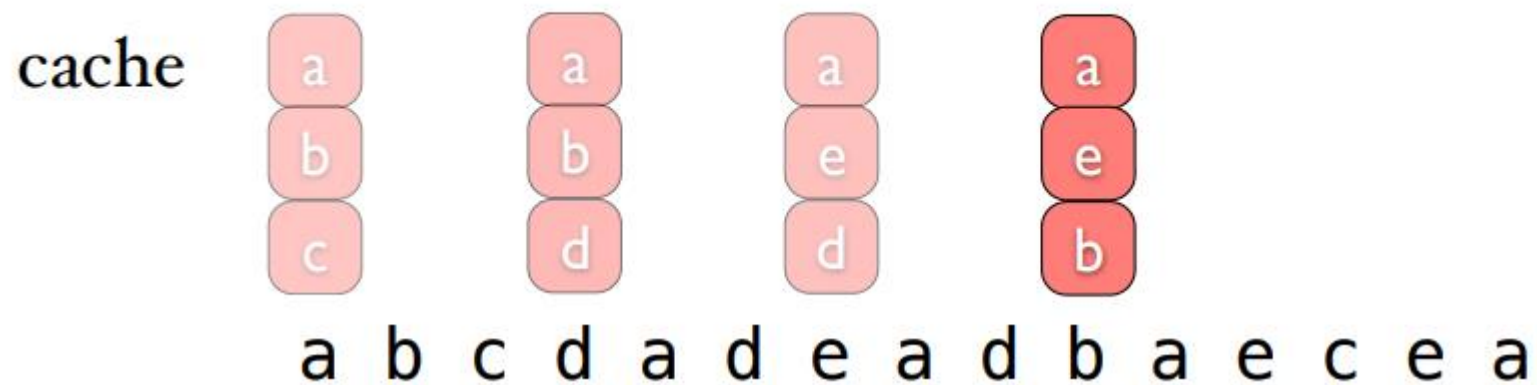
a
b
c

a
b
d

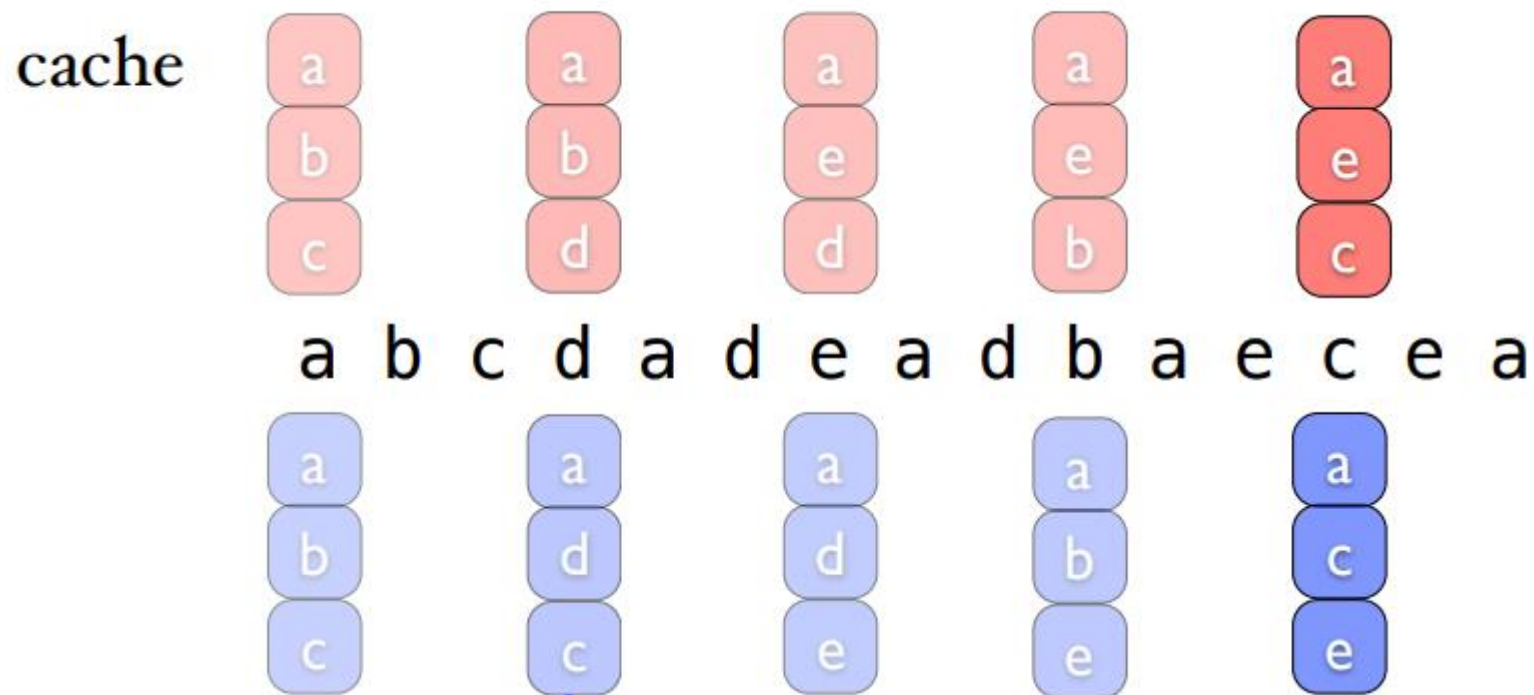
a
e
d

a b c d a d e a d b a e c e a

Example



Example



Belady evict rule

*Evict the item from the cache that is accessed **farthest in the future.***




Gives optimal solution

How to proof it?

Which lemma we would need?

S_{ff}

Some definition

Schedule for access pattern d_1, d_2, \dots, d_n : 

cache

Step	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Access	a	b	c	d	a	d	e	a	d	b	a	e	c	e	a
Cache (Red)	a, b, c	a, b, d	a, e, d	a, e, b	a, e, c										
Cache (Blue)	a, b, c	a, d, c	a, d, e	a, b, e	a, c, e										

Either the 'nop' operation or 'evict x for y' operation

Each step

**** at step i , element d_i must be in the cache

Reduce Lazy Schedule:

Schedule for which of the operation 'evict x for y'

Only occurs at a step i , if $y=d_i$

Exchange Lemma:

Let S be a reduced schedule that agrees with S_{ff} on the first j items. There exists a reduced schedule S' that agrees with S_{ff} on the first $j+1$ items and has the same or fewer #misses as S .

$$\text{Number of misses } (S') \leq \text{Number of misses } (S)$$

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Optimal
schedule

 S_0^* S_{ff}

S_0^* agrees with S_{ff} on first $i=0$

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Optimal
schedule

S_0^*

S_1

S_{ff}

S_1 agrees with S_{ff} on first $i=1$

S_0^* agrees with S_{ff} on first $i=0$

$$\# \text{ misses } (S_1) \leq \# \text{ misses } (S_1^*)$$

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Optimal
schedule

S_0^*

S_1

S_2

S_{ff}

S_2 agrees with S_{ff} on first $i=2$

S_1 agrees with S_{ff} on first $i=1$

S_0^* agrees with S_{ff} on first $i=0$

misses (S_2) \leq # misses (S_2^*)

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Optimal
schedule

S_0^* S_1 S_2 S_3 S_{ff-1} S_{ff}

S_2 agrees with S_{ff} on first $i=2$

S_1 agrees with S_{ff} on first $i=1$

S_0^* agrees with S_{ff} on first $i=0$

misses (S_2) \leq # misses (S_2^*)

Exchange Lemma: **Proof**

Let S be a reduced schedule that agrees with S_{ff} on the first j items. There exists a reduced schedule S' that agrees with S_{ff} on the first $j+1$ items and has the same or fewer #misses as S .

Proof: Since S agrees with S_{ff} on the first j operations, then the state of the cache at operation $j+1$ will be the same. Let d be the addresses accessed at the operation $j+1$.

State of the cache after j operations under the two schedules



S



S_{ff}

Proof of lemma

State of the cache after j operations under the two schedules



S



S_{ff}

Easy Case 1: $d \in$ cache.

Then $S' = S$ Because both S and S_{ff} issue 'nop' operation

Easy Case 1: $d \notin$ cache.

But, both S, S', S_{ff} 'evict e for d' or 'evict f for d'

Proof of lemma

State of the cache after j operations under the two schedules



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S_{ff}

Easy Case 1: $d \in \text{cache}$. **Then $S' = S$ Because both S and S_{ff} issue 'nop' operation**

Easy Case 1: $d \notin \text{cache}$. **But, both S, S', S_{ff} 'evict e for d ' or 'evict f for d '**

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Proof of lemma

State of the cache after j operations under the two schedules



S 'evict e for d'



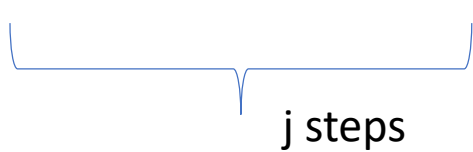
S_{ff} 'evict f for d'

Case 3: $d \notin \text{cache}$.

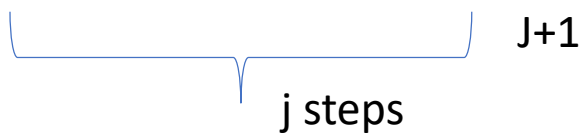
We need to construct a schedule S' that satisfy:

1. agrees with S_{ff} on $j+1$ step
2. has same number of misses as S , up to $j+1$ step.

S_{ff}

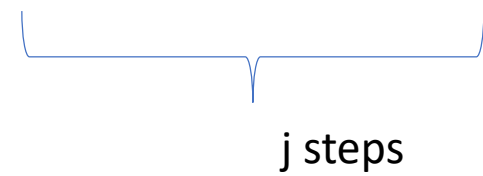


S'



J+1

S



S_{ff}



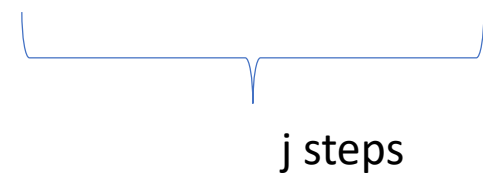
First operation in S that involves either e or f

S'



J+1

S





Let access t be the first operation in S after j+1 that involves either e or f

Either, t=e

t=f

t is something else



Let access t be the first operation in S after $j+1$ that involves either e or f

Either, $t=e$

$t=f$

t is something else



Let access t be the first operation in S after $j+1$ that involves either e or f

Either, $t=e$

S must load e . 'Evict x for e '

**S' can issue the operation:
'Evict x for f '**

**As a result S' and S will have the same state of
The cache and same # of misses.**



Let access t be the first operation in S after $j+1$ that involves either e or f

Either, $t=f$

Can not happen!!!! Why?

S_{ff} uses the farthest in the future rule. So, e would have been evicted in S_{ff} , not f .



Let access t be the first operation in S after $j+1$ that involves either e or f

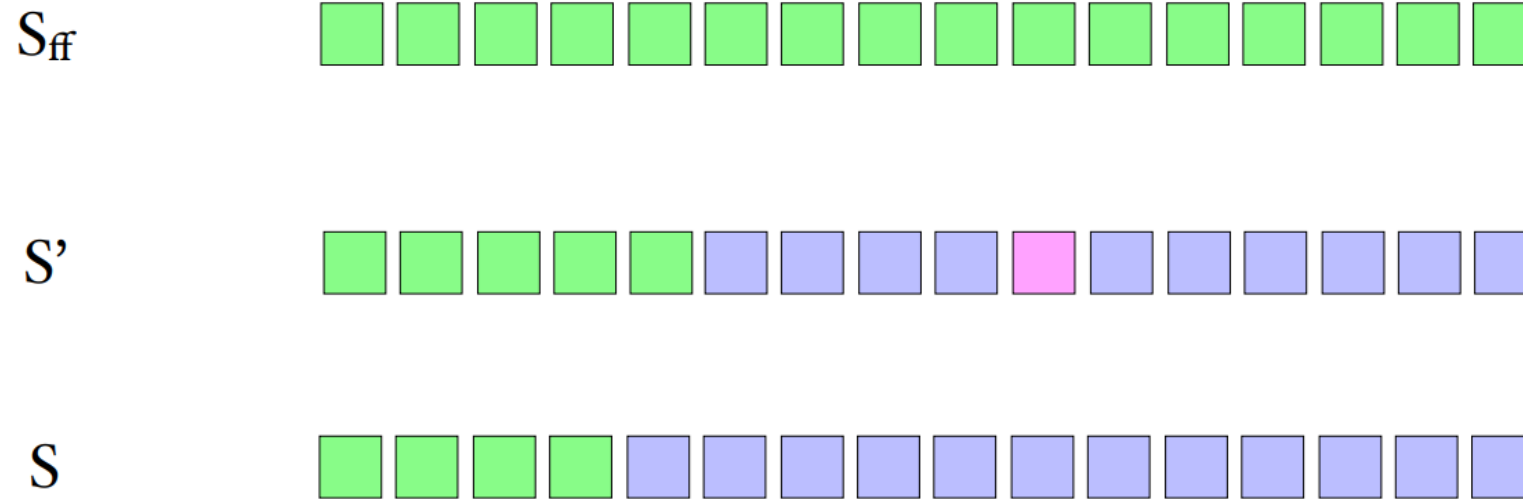
'Evict f for t '

'Evict e for t '

Either, $t =$ t is something else

Same state of cache. Same # of misses!!!!

So what we have shown?



Let S be a reduced sched that agrees with S_{ff} on the first j items.
There exists a reduced sched S' that agrees with S_{ff} on the first $j+1$ items and has the same or fewer #misses as S .