This XML file does not appear to have any style information associated with it. The document tree is shown below.

```
-<root>
 -<simulation>
     <result generated time/>
     2020-08-18 10:58:09
   -<laver>
       <layer spec/>
       {'B': 1, 'K': 384, 'C': 192, 'OY': 13, 'OX': 13, 'IY': 15, 'IX': 15, 'FY': 3, 'FX': 3,
       'SY': 1, 'SX': 1, 'SFY': 1, 'SFX': 1}
       <total_MAC_operation/>
       112140288
       <total data size element/>
       {'W': 663552, 'I': 43200, 'O': 64896}
     </laver>
   -<hw spec>
     -<PE array>
         cision bit/>
         {'W': 16, 'I': 16, 'O partial': 16, 'O final': 16}
         <array_size/>
         {'Col': 14, 'Row': 12}
       </PE array>
     -<memory_hierarchy>
       -<mem_name_in_the_hierarchy>
           <W/>
           ['rf512', 'sram128kb', 'sram16Mb']
           ['rf512', 'sram128kb', 'sram16Mb']
           <0/>
           ['rf512', 'sram16kb', 'sram16Mb']
         </mem name in the hierarchy>
         <mem size bit/>
         {'W': [512.0, 524288.0, 16777216], 'I': [512.0, 524288.0, 16777216], 'O':
         [512.0, 65536.0, 16777216]}
         <mem bw bit per cycle or mem wordlength/>
         {'W': [[32.0, 32.0], [128.0, 128.0], [128, 128]], 'I': [[32.0, 32.0], [128.0,
         128.0], [128, 128]], 'O': [[32.0, 32.0], [128.0, 128.0], [128, 128]]}
         <mem access energy per word/>
         {'W': [[1.858, 2.573], [38.176, 41.952], [240, 240]], 'I': [[1.858, 2.573],
         [38.176, 41.952], [240, 240]], 'O': [[1.858, 2.573], [16.356, 17.804], [240,
```

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```
240]]}
     <mem type/>
     {'W': ['dp_sb', 'dp_sb', 'dp_sb'], 'I': ['dp_sb', 'dp_sb', 'dp_sb'], 'O': ['dp_sb',
     'dp sb', 'dp sb']}
     <mem share/>
     {1: [('O', 'sram16Mb'), ('I', 'sram16Mb'), ('W', 'sram16Mb')]}
     <mem area single module/>
     {'W': [2847.0, 127880.0, 1000000], 'I': [2847.0, 127880.0, 1000000], 'O':
     [2847.0, 20636.0, 1000000]}
     <mem unroll/>
     {'W': [156, 1, 1], 'I': [156, 1, 1], 'O': [156, 1, 1]}
   </memory hierarchy>
 </hw spec>
-<results>
 -<basic info>
    -<spatial unrolling>
       <W/>
       [[], [[('OY', 13)], [('FY', 3), ('C', 4)]], [], []]
       [[], [[('OY', 13)], [('FY', 3), ('C', 4)]], [], []]
       <0/>
       [[], [[('OY', 13)], [('FY', 3), ('C', 4)]], [], []]
     </spatial_unrolling>
    -<temporal mapping>
       <W/>
       [[('OX', 13), ('K', 4), ('C', 2), ('FX', 3)], [('K', 6), ('C', 12)], [('K', 16), ('C',
       2)]]
       <I/>
       [[('OX', 13), ('K', 4), ('C', 2), ('FX', 3), ('K', 6)], [('C', 12), ('K', 16)], [('C',
       2)]]
       <0/>
       [[], [('OX', 13), ('K', 4), ('C', 2), ('FX', 3), ('K', 6), ('C', 12)], [('K', 16),
       ('C', 2)]]
     </temporal mapping>
   </basic info>
  -<energy>
     <total energy/>
     2254581115.6
    -<energy breakdown>
       <mem energy_W/>
       [8867377.2, 6646136.8, 19906560.0]
       <mem energy I/>
       [105067556.4, 3524947.2, 1296000.0]
       <mem_energy_O/>
```

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```
[269006931.6, 40180358.4, 5840640.0]
      </energy_breakdown>
      <mac_energy/>
      1794244608
    </energy>
   -<performance>
      <mac_array_utilization/>
      0.4644
      <latency/>
      1437432
    </performance>
    <area/>
    2711284.0
   </results>
   <elapsed_time_second/>
   425.125
 </simulation>
</root>
```

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