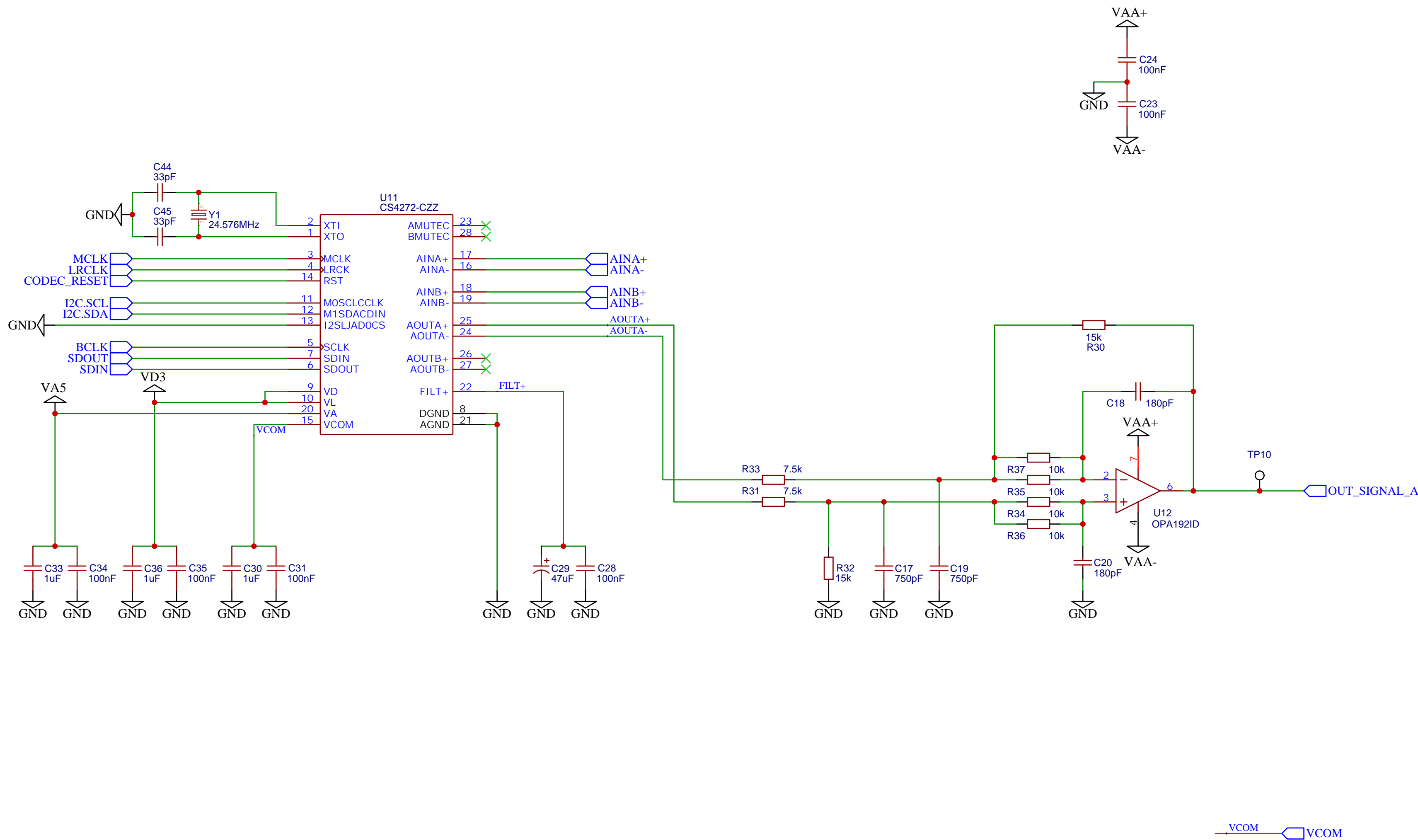
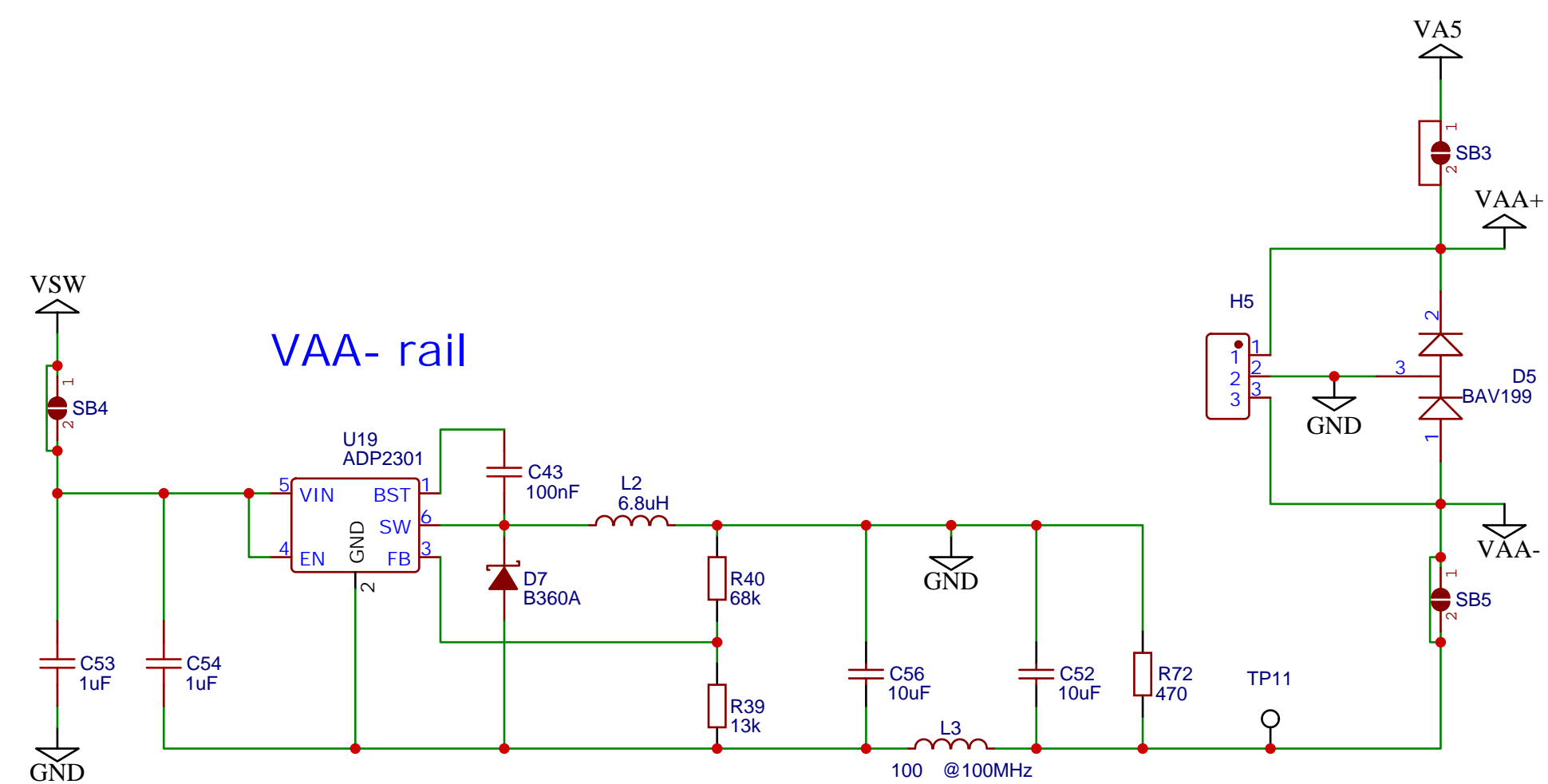
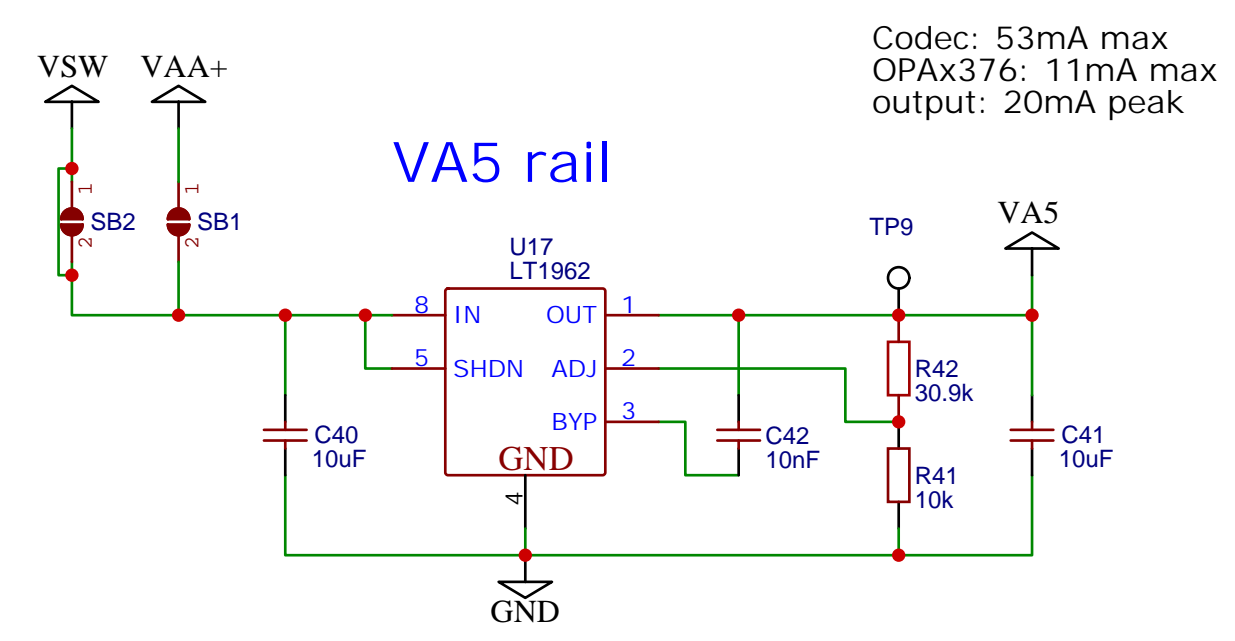
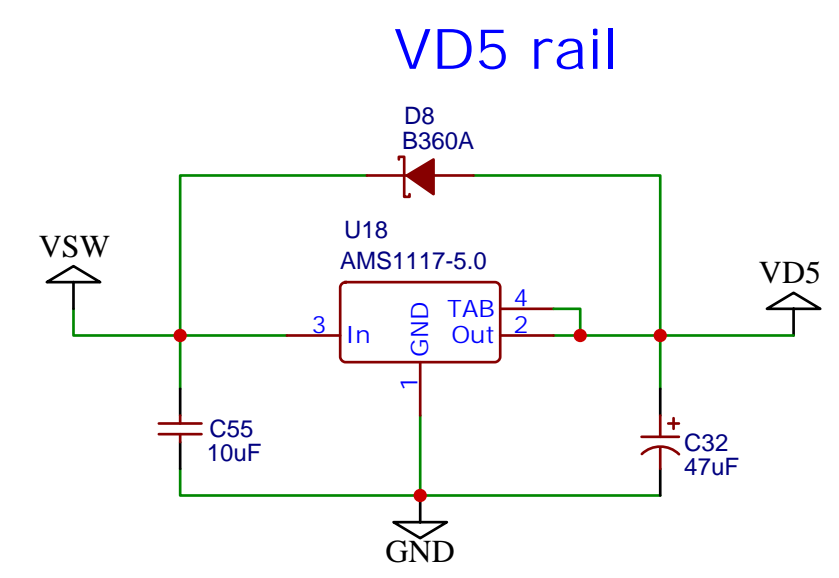
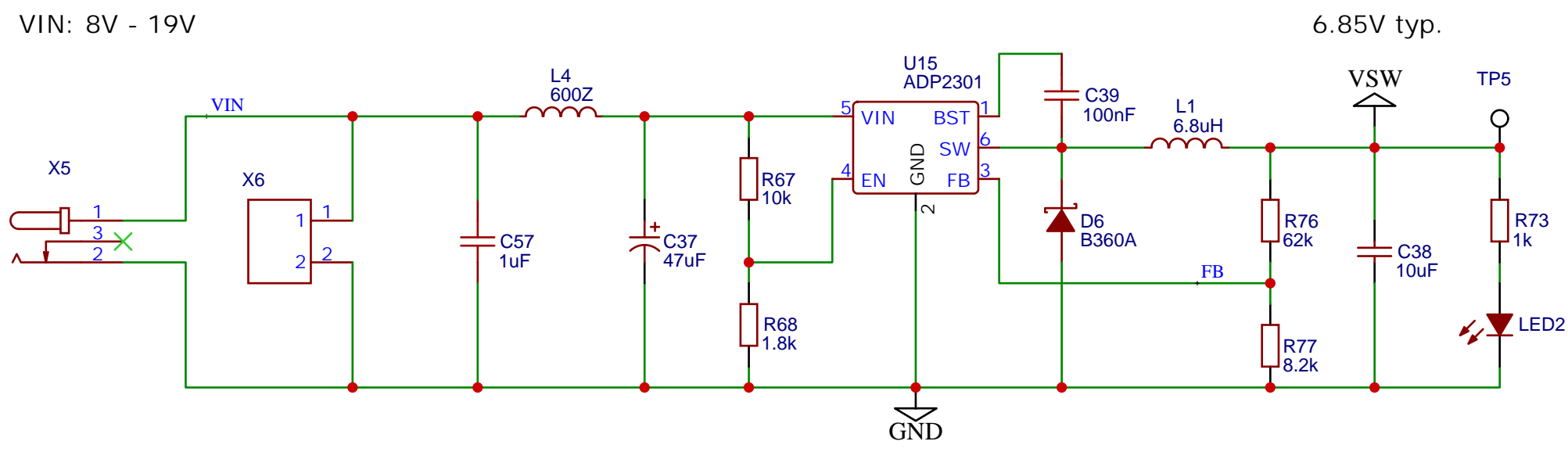


| RANGE_SEL | | Res. |
|-----------|---|--------|
| A | B | |
| L | L | 1000hm |
| H | L | 1k |
| L | H | 10k |
| H | H | 100k |

| | |
|-------------|-------------|
| RANGE_SEL_A | RANGE_SEL_A |
| RANGE_SEL_B | RANGE_SEL_B |
| PGA_V_A | PGA_V_A |
| PGA_V_B | PGA_V_B |
| PGA_I_A | PGA_I_A |
| PGA_I_B | PGA_I_B |

| PGA | | Gain |
|-----|---|------|
| A | B | |
| L | L | 1 |
| L | H | 5 |
| H | H | 25 |
| H | L | 101 |

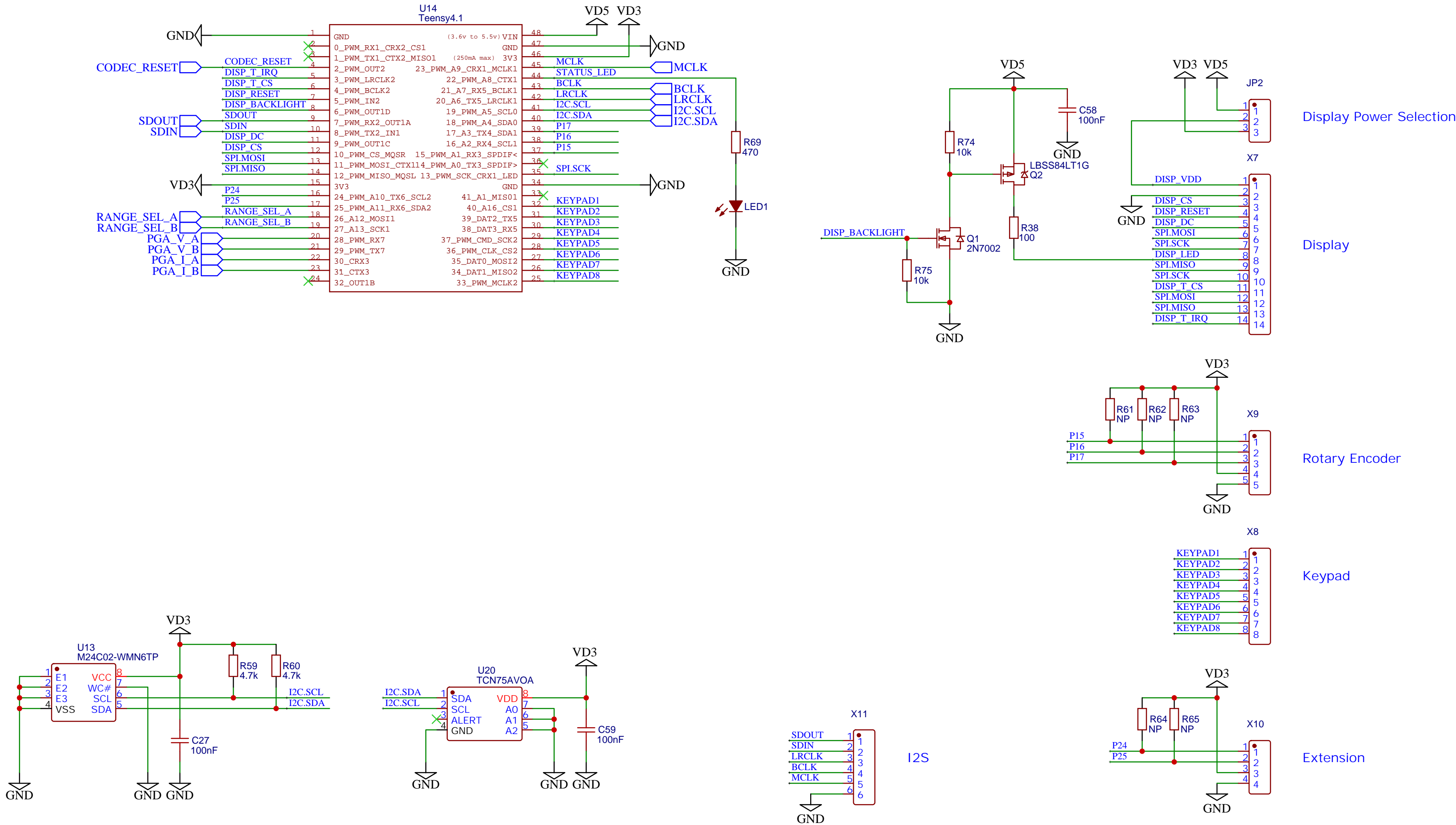


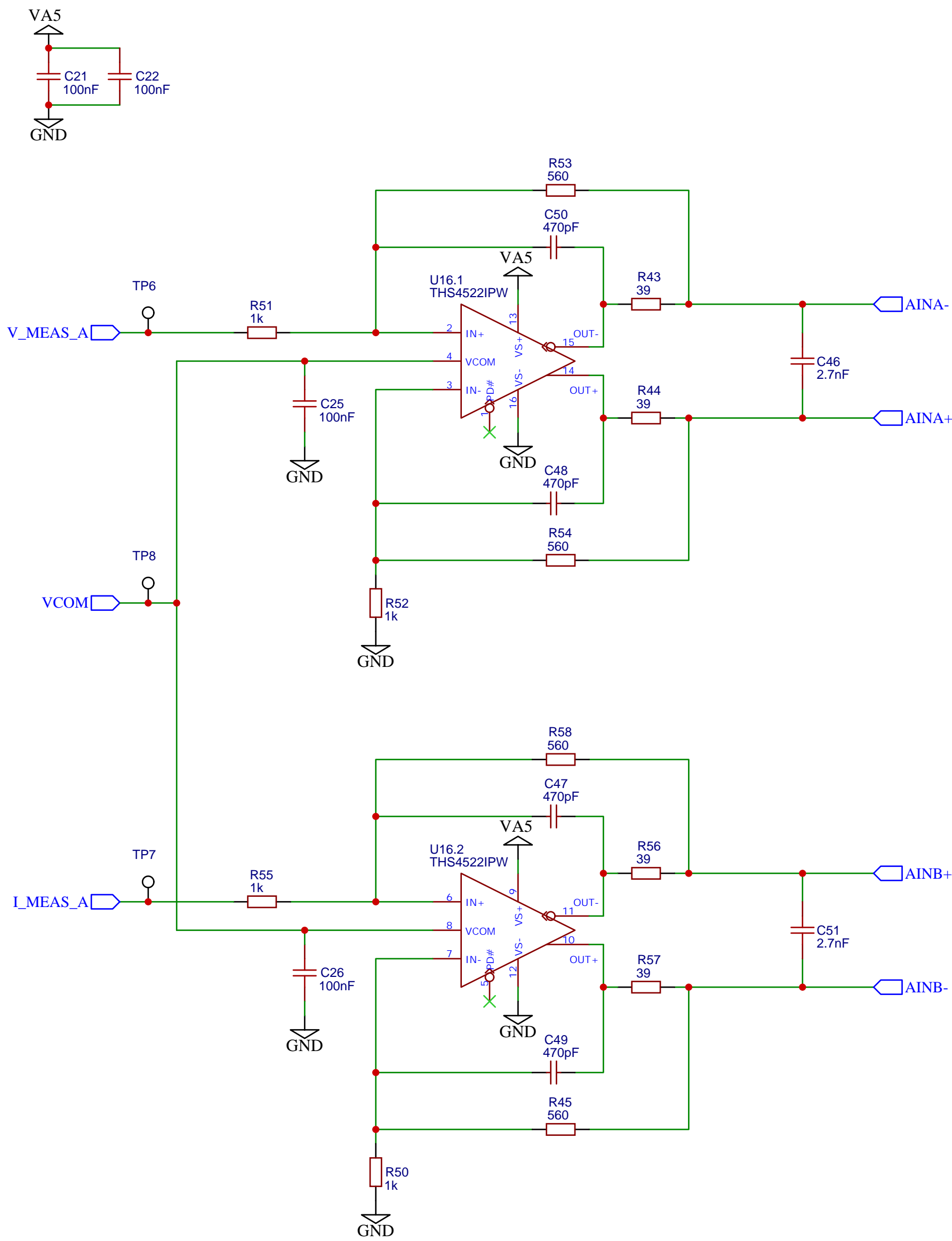


Using +-5V rails (default)
* Connect SB2, SB3, SB4, SB5
* Disconnect SB1

Using external analog rails
* Connect SB1
* Disconnect SB2, SB3, SB4, SB5

Cut VIN-VBUS connection on Teensy board!





| | | |
|---------------------|----------------------------------|------------|
| TITLE: Input Filter | | REV: 1.1 |
| | Company: | Sheet: 1/1 |
| | Date: 2023-08-26 Drawn By: WeTec | |