Deep Root-Cause Performance Analysis: Integration and Navigation from a Load Testing Tool

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ABSTRACT

Software performance testing helps testers to validate and verify software quality in terms of response time, reliability, and scalability. Many performance-testing tools can measure client-side response time and correlated it with collected application transaction monitoring data. Such correlation can break down clientside response time into its constituent parts on the various tiers of the server(s) under test. To avoid high overhead, some other performance tools for Java applications perform runtime profiling by using JVM profiling with statistical sampling. However, such an imprecise profiling mechanism poses challenges for performance analysis. It is challenging to identify the correlation between a transaction or time-range on the client side and the profiling information at a server's JVM. Therefore, it is difficult to provide user-friendly drill-down navigation from client-side response time or time-range to determine problem spots and bottlenecks in the JVM(s) on the server sides because of a lack of correlation between the transactions or time-range driven by a load testing tool at the client side and the runtime sample data collected by the JVM profiling tools at the server sides. In the paper, we have developed a new technique and tool to help to address the preceding issues in deep root-cause performance analysis. We evaluate the accuracy, efficiency, and effectiveness of our approach by conducting experiments with several real-world web applications.

1. INTRODUCTION

Performance testing and analysis tools help testers to identify and diagnose the presence and cause of application or system bottlenecks. In performance testing, testers create loads to expose problems and observe failures. In the analysis and diagnosis against the testing results, they try to get the location of the root causes of the observed failures. Usually, deep root-cause performance analysis requires correlation of test results from the client side and traces/events information from the server side.

Many performance-testing tools [5] [4] can measure client-side response time and correlated it with collected application transaction monitoring data from the server side. Application Response Measurement (ARM) [1] used by the tools provides a common

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way to get profiling information and allows to measure application availability, application performance, and end-to-end transaction response time.

For non ARM-instrumented servers running Java applications, Java Virtual Machine Tool Interface (JVMTI) [3] feature of Eclipse Test and Performance Tools Platform (TPTP) [6] allows performance-testing tool to collect full traces from the non ARM-instrumented servers. One problem is here is that the collection of full traces is expensive and slow. To reduce the cost of the collection of full traces from the servers, a selective instrumentation should be conducted. However, such an imprecise profiling mechanism poses challenges for performance analysis because it is difficult to identify the correlation between a transaction (or time-range) on the client and the profiling information at a server's JVM.

In our approach, we show that a technique to recover information that is available in full traces and necessary for performance analysis from sampled traces. With the sampled traces, we show a robust technique for matching the sampled method calls with the start and end points identified

We have developed a new tool, an Eclipse plug-in, for reading in performance tests and traces in the XML format and identifying the start and end point of a transaction or a request among the method execution on the server side. The tool helps testers to perform deep root-cause analysis by integrating with a load-testing tool and navigating the performance results.

In the evaluation, we compared the precision and recall of the set of we compared the runtime cost of the full traces with the cost of the sampled traces. The result is.

Contributions of our work are the followings:

- We provide an approach for supporting performance analysis in the absence of ARM.
- We suggest a technique for supporting performance analysis with sampled traces rather than fully collected traces.

The remainder of this paper is organized as follows. Section 3 presents our approach.

2. EXAMPLE

3. APPROACH

The high-level overview of our approach is shown in Figure **FIX** prepare for the Figure **FIX**. Our approach consists of two major phases: Correlating Trace Data and Exploiting Statistically Sampled Trace Data. In the first phase, we focus on correlating (fully-collected) traces from the server side with the client-side performance test data without requiring the involved servers to be ARM-instrumented. In the second phase, we focus on using statistically sampled traces in the techniques from the first phase instead of full trace data.

3.1 Correlating Trace Data

In order for performance analyst to navigate from the client-side runtime information to the server-side runtime information in performance analysis, correlating client-side transactions or time-ranges with server-side extrapolated runtime information should be done. For an ARM-instrumented server, we explore ARM hooks for providing the information on when transactions begin and end. Each ARM instrumented server generates a correlator to map parent transactions to their respective child transactions across multiple processes or servers. Thus, the correlation mechanism can trace the path of a distributed transaction through multiple processes or servers. We further correlate with the runtime information collected via JVM profiling with the information collected via ARM hooks; therefore, we can establish the correlation between the JVM profiling information and the information from the load testing tool at the client side.

Next, we show how to correlate the traces ${\bf FIX}$ add more explanations ${\bf FIX}$ A model generated by RPT is in the format of Eclipse Modeling Framework (EMF[2]) model. As the traces generated by TPTP are stored as EMF model, the model generated by our approach is also an EMF model.

In case of absence of ARM, we develop learning techniques to discover with transactions begin and end at the server code. There will be three phases in the learning process: training, validation, and analysis.

3.2 Exploiting Statistically Sampled Trace Data

4. EVALUATION

Our evaluation is ...

5. CONCLUSTION

Our conclusion is ...

6. REFERENCES

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