Instruction	PCSRC	L/H	regdst1	regdst2	ALUsrc	ACCwrite	mem-to-reg	reg write	mem read	mem write	branch	jal	Lwra	ALU (3bits)
Add	0	L	0	0	0	1	x	0	0	0	0	0	0	Add
Nand	0	L	0	0	0	1	x	0	0	0	0	0	0	Nand
Slt	0	L	0	0	0	1	x	0	0	0	0	0	0	Slt
Move	0	L	x	x	x	0	1	1	0	0	0	0	0	No-op
Srl	0	L	x	x	1	1	x	0	0	0	0	0	0	Srl
Jrra	2	L	1	x	x	0	x	0	0	0	0	0	0	No-op
Jal	3	L->H	x	x	x	0	x	1	0	0	0	1	0	No-op
Sw	0	L->H	1	1	3	0	x	0	0	1	0	0	0	Add
Addi	0	L->H	1	x	2	1	x	0	0	0	0	0	0	Add
Lw	0	L->H	x	1	3	0	0	1	1	0	0	0/1	0/1	Add
Beq	1	L->H	1	1	0	0	x	0	0	0	1	0	0	Subtract

ALU		
Bits	Operation	
000	Add	
001	Sub	
010	Nand	
011	Slt	
100	Srl	
101	No-op	