

# Joshua Yan

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## Objectives

- I'm a highly motivated and hardworking Design Verification Engineer at Hewlett Packard Enterprise (HPE), and a 2024 graduate from Texas A&M University with a degree in Computer Engineering. My experience includes Design Verification for ASIC development using SystemVerilog and UVM, as well as coursework in C++, Object Oriented Programming, Verilog, circuit design and implementation, and mathematics from calculus to linear algebra.

## Skills

- Logic design and verification with Verilog, SystemVerilog, UVM, Verdi, and Eclipse
- Professional communications, people/organization managing, event planning, and teamwork
- Basic Object Oriented Programming and advanced mathematics

## Experience

- **ASIC Design Verification Engineer at Hewlett Packard Enterprise (July 2024-Present)**
  - Part of the High Performance Computing (HPC) and Fabric ASICs and Software Technologies (FAST) team at HPE, focusing on the verification of Packet and Connection Tracking block on a network interface controller (NIC) chip of the Slingshot 3 network
  - Utilizing tools like DVT Eclipse, Perforce, and Verdi to develop, debug, and verify SystemVerilog code and ensure compliance with the code base and versions
- **Design Verification Intern at Hewlett Packard Enterprise (June - August 2023)**
  - Worked on translating transactions of different formats between multiple interfaces of the Slingshot 3 network interface controller (NIC), as well as handling retrying data requests in case of failures
  - Received return offer for full time position
- **Retail Sales Associate at Nike (Summer 2021 & 2022)**
  - Performed transactions and routinely exceeded store goals in securing online orders
  - Processed and sorted merchandise in an efficient manner on the floor and in inventory

## Education

- **Texas A&M University (College Station, TX): 2020 - 2024; B.S. in Computer Engineering**
  - **Major-related Coursework:** Python Coding Lab, C++ Program Design and Concepts, Digital System Design, Experimental Engineering and Physics Lab - Electricity and Magnetism, C++ Data Structures and Algorithms, Discrete Math, Circuit Theory, Computer Architecture, Computer Systems, Software Engineering, Microcomputer Systems, Electronics, Digital Analog Circuit Design, Machine Learning, Database Systems
  - **Major-related projects:**
    - Smart Kitchen Scale: Senior capstone project; developed a proof of concept Smart Kitchen Scale, capable of using machine learning and computer vision to identify food names and extract digits from a 7 segment display, and then use these labels to return detailed nutritional information in a connected mobile app. Presented project for peers and faculty, as well as industry professionals

## Accomplishments and Leadership Roles

- 4 year Texas A&M President's Endowed Scholarship recipient; National Merit Scholar
- President (2021-2024) of TAMU Freestyle Underground Street Dancers
  - We promote fun as well as education about the Hip Hop dance culture in the Bryan/College Station area through various disciplines of freestyle dance, mainly breakdancing, while improving members of the organization through events such as competitions, performances, social events, and workshops.