

6. multiple index

14. (a)

$\langle T_2, Y, 0 \rangle, \langle T_2, abort \rangle$

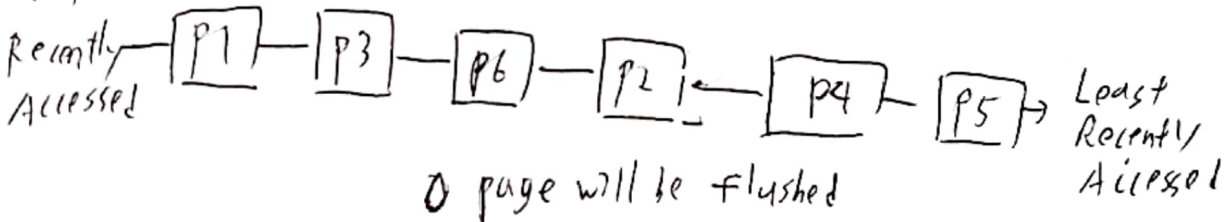
(b) just Y once

3. (a)



only one page (P1) will be flushed

(b)



0 page will be flushed

9.

(a) w(B) should be after R(B), w(A) should be after R(A)

so T1's unable to swap instruction of series

so this schedule is not conflict serializable

(b)

T1	T2	T3
R(A)	R(B)	R(B)
w(B)	R(A)	w(A)

It is view-serializable

10,

$T_1$	$T_2$
L(A)	
R(A)	
W(A)	
U(A)	
	L(A)
	R(A)
	U(A)
	L(B)
	R(B)
	U(B)
	commit
L(B)	
R(B)	
W(B)	
U(B)	
commit	

11.

- (a) SIX: no, IX: no, X: no, IS: yes, S: yes
- (b) SIX: no, IX: yes, X: yes, IS: no, S: no
- (c) SIX: no, IX: no, X: no, IS: no, S: no
- (d) SIX: yes, IX: yes, X: no, IS: yes, S: no
- (e) SIX: yes, IX: yes, X: yes, IS: yes, S: yes