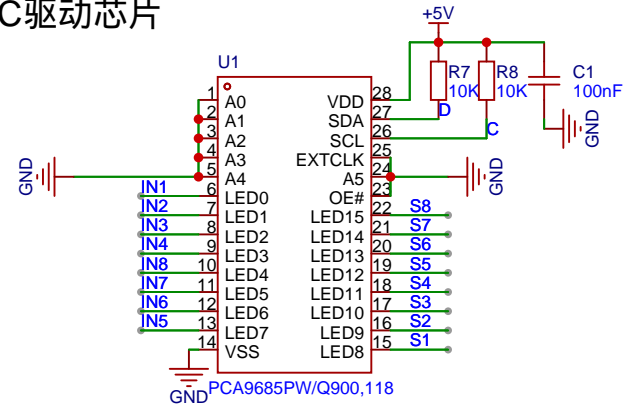
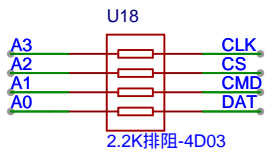
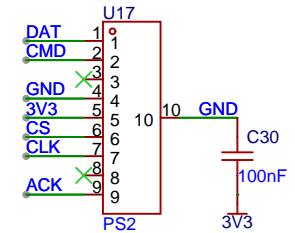
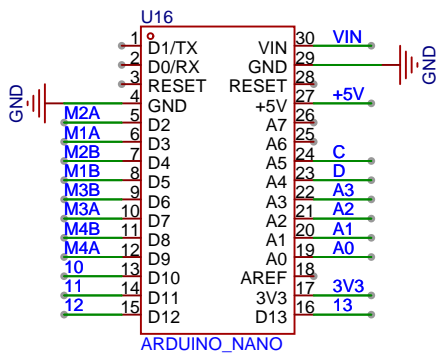


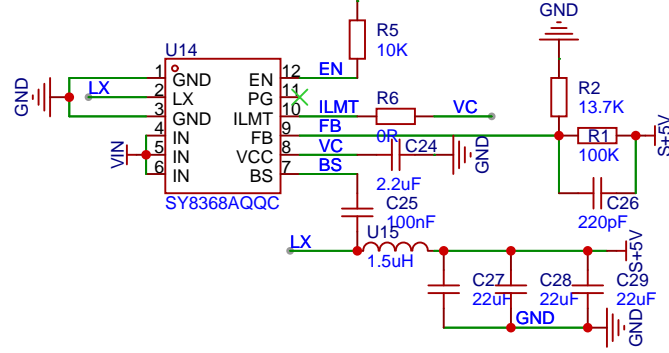
IIC驱动芯片



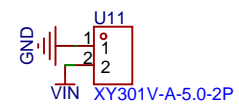
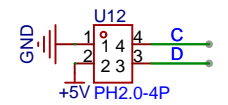
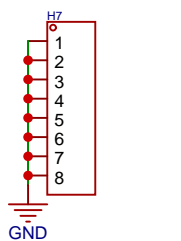
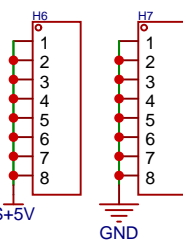
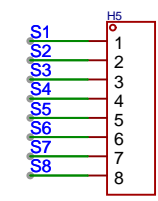
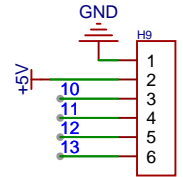
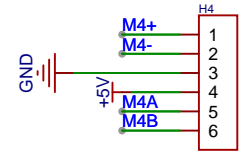
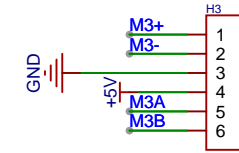
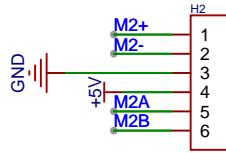
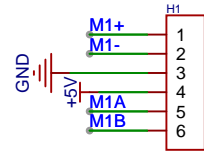
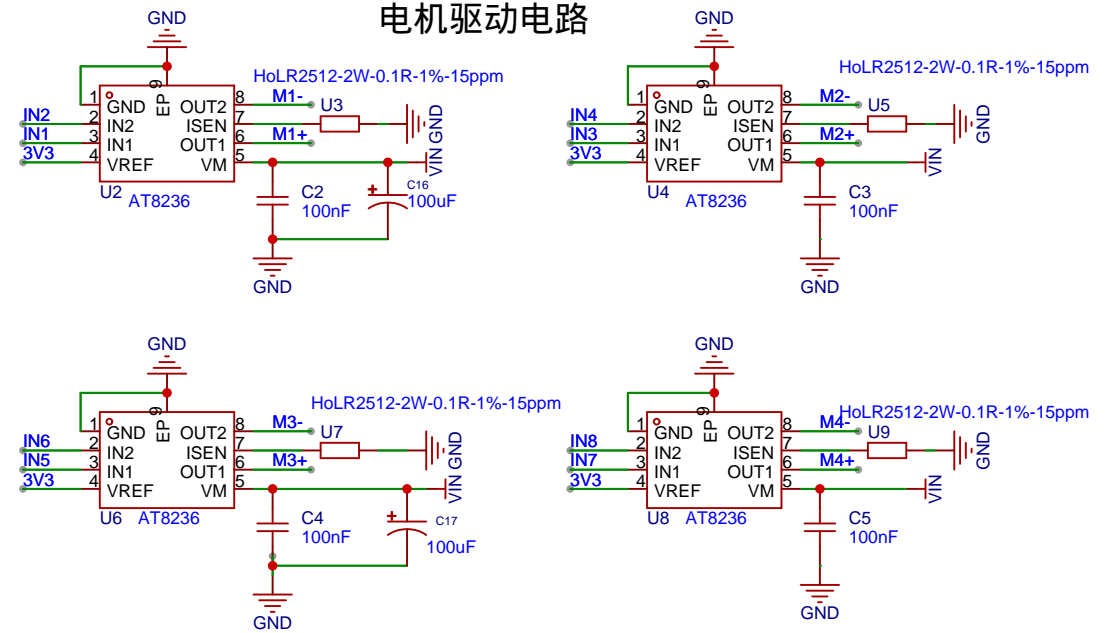
ARDUINO NANO



降压电路



电机驱动电路



原理图	Schematic1			更新日期	2024-03-27
图页	P1			创建日期	2024-03-27
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审阅					
		版本	尺寸	页	1 共 1
		V1.0	A4	嘉立创EDA	