Connector Standard

Upper Right Connector: Communication

Connector 1x 10 pins Male Part number: 2211R-10G-LP-F1

Spec files : For ATMEGA32 and ATMEGA8

Port	E_port Name	ATMEGA32(TQFP)	ATMEG8(TQFP)	
1	VCC	VCC (5,38,17)	VCC (4,6)	
2	GND	GND(6,18,39)	GND(3,5)	
3	SCL/ E_PORTD0	PC0(19)	PC5(28)* JP1	
4	SDA/ E_PORTD1	PC1(20)	PC4(27)* JP2	
5	RX/ E_PORTD2	PD0(9)** JP3	PD0(30) *	
6	TX/ E_PORTD3	PD1(10)* JP4	PD1(31) *	
7	INT0/ E_PORTD4	PD2(11)	PD2(32)* JP3	
8	ICP/ E_PORTD5	PD6(15)* JP1	PB0(12)* JP4	
9	TIMER1/ E_PORTD6	PD5(14)/OC1A	PB1(13)* JP5	
10	TIMER2/ E_PORTD7	PD4(13)/OC1B	PB2(14)* JP6	

Lower Right connector : Anaglog Input

Connector 1x 10 pins Male Part number: 2211R-10G-LP-F1

Spec files

Port	E_port Name	ATMEGA32(TQFP)	ATMEG8(TQFP)
1	AVCC	AVCC(27)	AVCC(18)
2	AREF	AREF(29)	AREF(20)
3	ADC_1/E_PORTC0	PA0(37)	PC0(23)
4	ADC_2/E_PORTC1	PA1(36)	PC1(24)
5	ADC_3/E_PORTC2	PA2(35)	PC2(25)
6	ADC_4/E_PORTC3	PA3(34)	PC3(26)
7	ADC_5/E_PORTC4	PA4(33)	PC4(27)*
8	ADC_6/E_PORTC5	PA5(32)	PC5(28)*
9	ADC_7/E_PORTC6	PA6(31)	ADC6(19)
10	ADC_8/E_PORTC7	PA7(30)	ADC7(22)

Upper Left connector: Digital O/P / SPI

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Connector 1x 10 pins Male Part number: 2211R-10G-LP-F1

Spec files : For ATMEGA32 and ATMEGA8

Port	E_port Name	ATMEGA32(TQFP)	ATMEG8(TQFP)
1	VCC	VCC(5,17,38)	VCC (4,6)
2	GND	GND(6,18,39)	GND(3,5)
3	E_PORTA0	PB0(40)	PD4(2)
4	E_PORTA1	PB1(41)	PD5(9)
5	E_PORTA2	PB2(42)	PD6(10)
6	E_PORTA3	PB3 (43)	PD7(11)
7	E_PORTA4 /SS	PB4(44)	PD2(32)*
8	E_PORTA5 / MOSI	PB5(1)	PD3(1)*
9	E_PORTA6/ MISO	PB6(2)	
10	E_PORTA7/ SCK	PB7(3)	

Lower Left connector: Digital I/P Connector 1x 10 pins Male Part number: 2211R-10G-LP-F1

Spec files : For ATMEGA32 and ATMEGA8

Port	E_port Name	ATMEGA32(TQFP)		ATMEG8(TQFP)	
1	TIMER0	PD7(16)	JP2	PB3(15)*	JP7
2	INT1	PD3(12)		PD3(1)*	JP8
3	E_PORTB0	PC2(21)		PB0(12)*	
4	E_PORTB1	PC3(22)		PB1(13)*	
5	E_PORTB2	PC4(23)		PB2(14)*	
6	E_PORTB3	PC5(24)		PB3(15)*	
7	E_PORTB4	PC6(25)		PB4(44)	
	E_PORTB5	PC7(26)		PB5(1)	
8				(use for mode sw	ritch)
9	E_PORTB6	PD7(16)	JP2		•
10	E_PORTB7/ICP	PD6(15) *	JP1		

^{*} Selectable by Jumprt right left

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^{**} Jumper select for using TX/RX or PortD