

DATA SHEET

Order code	Manufacturer code	Description		
58-0320	n/a	L-7113F3BT IR LED 5MM BLUE LENS (RC)		

	Page 1 of 6
The enclosed information is believed to be correct, Information may change ±without noticeqdue to product improvement. Users should ensure that the product is suitable for their use. E. & O. E.	Revision A 20/02/2007

Sales: 01206 751166 Sales@rapidelec.co.uk Technical: 01206 835555 Tech@rapidelec.co.uk Fax: 01206 751188 www.rapidonline.com



T-1 3/4 (5mm) INFRARED EMITTING DIODE

Part Number: L-7113F3BT

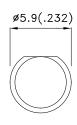
Features

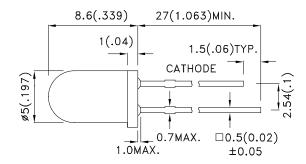
- MECHANICALLY AND SPECTRALLY MATCHED TO THE PHOTOTRANSISTOR.
- BLUE TRANSPARENT LENS
- RoHS COMPLIANT.

Description

F3 Made with Gallium Arsenide Infrared Emitting diodes.

Package Dimensions





- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is $\pm 0.25(0.01")$ unless otherwise noted.
- Lead spacing is measured where the leads emerge from the package.
 Specifications are subject to change without notice.





SPEC NO: DSAE2820 **REV NO: V.10** DATE: SEP/04/2007 PAGE: 1 OF 5 APPROVED: WYNEC **CHECKED: Allen Liu** DRAWN: Y.L.LI ERP: 1101004991

Selection Guide

Part No.	Dice	Lens Type	Po (mW/sr) [2] @ 20mA *50mA		Viewing Angle [1]
			Min. Typ.		201/2
L-7113F3BT	F3 (GaAs)	BLUE TRANSPARENT	4	20	- 20°
L-7 1 131 3D1	BLUE TRANSPARENT		*7	*30	20

Notes:

- 1. 01/2 is the angle from optical centerline where the luminous intensity is 1/2 the optical centerline value. 2. * Luminous intensity with asterisk is measured at 50mA;Radiant Intensity/ luminous flux: +/-15%.

Electrical / Optical Characteristics at TA=25°C

· · · · · · · · · · · · · · · · · · ·						
Parameter	P/N	Symbol	Тур.	Max.	Units	Test Conditions
Forward Voltage [1]	F3	VF	1.2	1.6	V	IF=20mA
Reverse Current	F3	lr		10	uA	VR = 5V
Capacitance	F3	С	90		pF	VF=0V;f=1MHz
Peak Spectral Wavelength	F3	λP	940		nm	IF=20mA
Spectral Bandwidth	F3	Δλ1/2	50		nm	IF=20mA

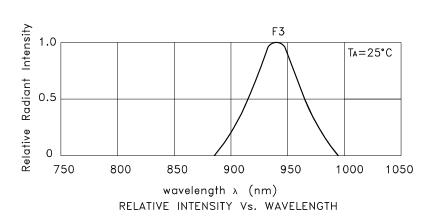
1. Forward Voltage: +/-0.1V.

Absolute Maximum Ratings at TA=25°C

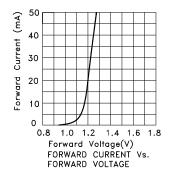
Parameter	Symbol	F3	Units	
Power dissipation	Рт	80	mW	
DC Forward Current	lF	50		
Peak Forward Current [1]	iFS	1.2	А	
Reverse Voltage	VR	5	V	
Operating Temperature	ТА	-40 To +85	°C	
Storage Temperature	Тѕтс	-40 To +85	°C	
Lead Solder Temperature [2]	260°C For 3 Seconds			
Lead Solder Temperature [3]	260°C For 5 Seconds			

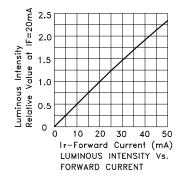
- 1. 1/100 Duty Cycle, 10µs Pulse Width.
- 2. 2mm below package base.
- 3. 5mm below package base.

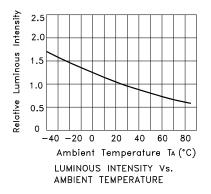
SPEC NO: DSAE2820 **REV NO: V.10** DATE: SEP/04/2007 PAGE: 2 OF 5 APPROVED: WYNEC **CHECKED: Allen Liu** DRAWN: Y.L.LI ERP: 1101004991

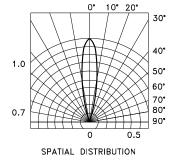


L-7113F3BT







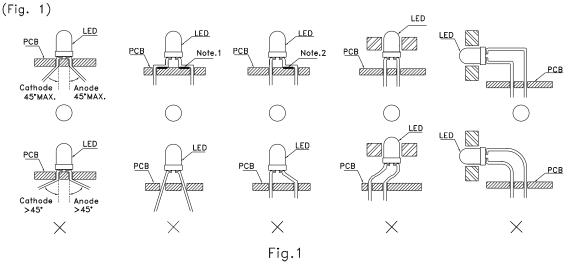


 SPEC NO: DSAE2820
 REV NO: V.10
 DATE: SEP/04/2007
 PAGE: 3 OF 5

 APPROVED: WYNEC
 CHECKED: Allen Liu
 DRAWN: Y.L.LI
 ERP: 1101004991

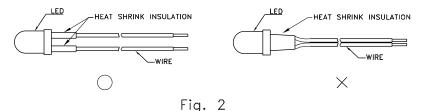
LED MOUNTING METHOD

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead—forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures.

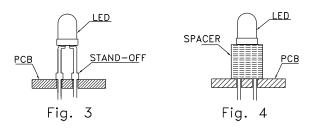


" \bigcirc " Correct mounting method " \times " Incorrect mounting method Note 1-2: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

2. When soldering wire to the LED, use individual heat—shrink tubing to insulate the exposed leads to prevent accidental contact short—circuit. (Fig. 2)



3. Use stand—offs (Fig. 3) or spacers (Fig. 4) to securely position the LED above the PCB.

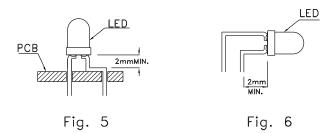


 SPEC NO: DSAE2820
 REV NO: V.10
 DATE: SEP/04/2007
 PAGE: 4 OF 5

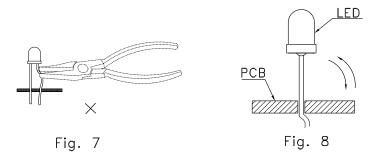
 APPROVED: WYNEC
 CHECKED: Allen Liu
 DRAWN: Y.L.LI
 ERP: 1101004991

LEAD FORMING PROCEDURES

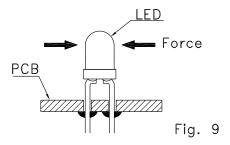
1. Maintain a minimum of 2mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)



- 2. Lead forming or bending must be performed before soldering, never during or after Soldering.
- 3. Do not stress the LED lens during lead—forming in order to fractures in the lens epoxy and damage the internal structures.
- 4. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)
- 5. Do not bend the leads more than twice. (Fig. 8)



6. After soldering or other high—temperature assembly, allow the LED to cool down to 50°C before applying outside force (Fig. 9). In general, avoid placing excess force on the LED to avoid damage. For any questions please consult with Kingbright representative for proper handling procedures.



 SPEC NO: DSAE2820
 REV NO: V.10
 DATE: SEP/04/2007
 PAGE: 5 OF 5

 APPROVED: WYNEC
 CHECKED: Allen Liu
 DRAWN: Y.L.LI
 ERP: 1101004991