


USB_Simulation_Board A2.0

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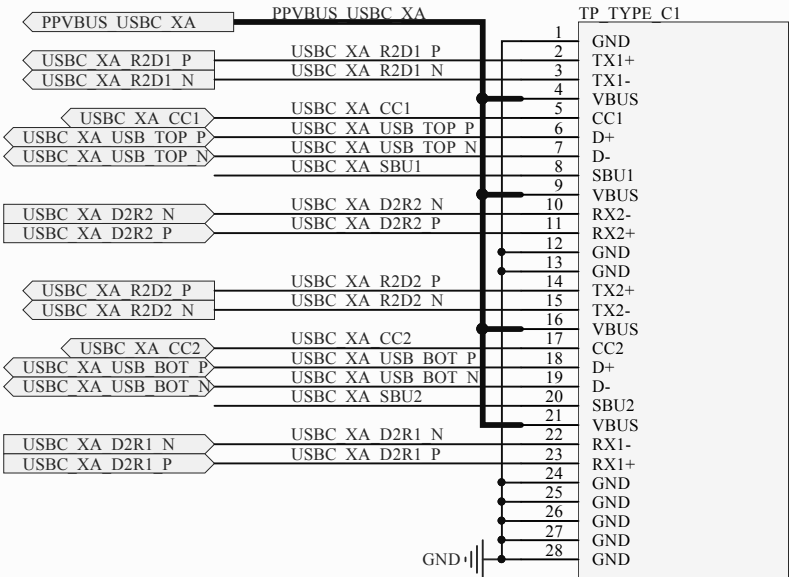
31	DP_RECEIVER4_2	Peiwu.Ren	2016/11/9
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Change Logs

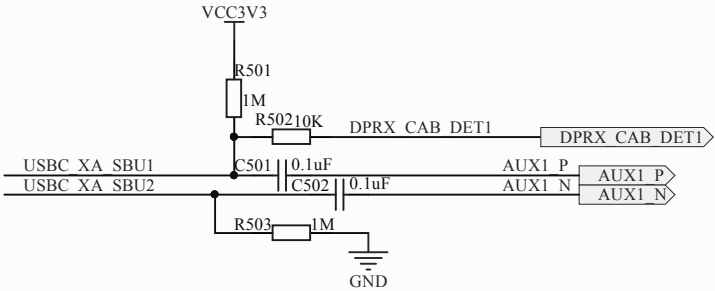
VER A1.0 Create	2016/11/9
VER A1.1	
Add potassium circuit	2016/11/12
VER A2.0	
1.modify the core board's PCB package	
2. Add power supply module to each channel	
3.modify DAC5V to VCC3V3	
4.Modify the configuration mode of the redriver	
5.Add a USB_A test circuit	
6.Add io expanders	2016/12/8

		Project	TYPE_C MODULE A1.0.PriPcb
		Title	01_Description.SchDoc
Customer	Intelligent	Drawn by	Peiwu.Ren
Version	A1.0	Check by	*
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UUT1



TP_TYPE_C

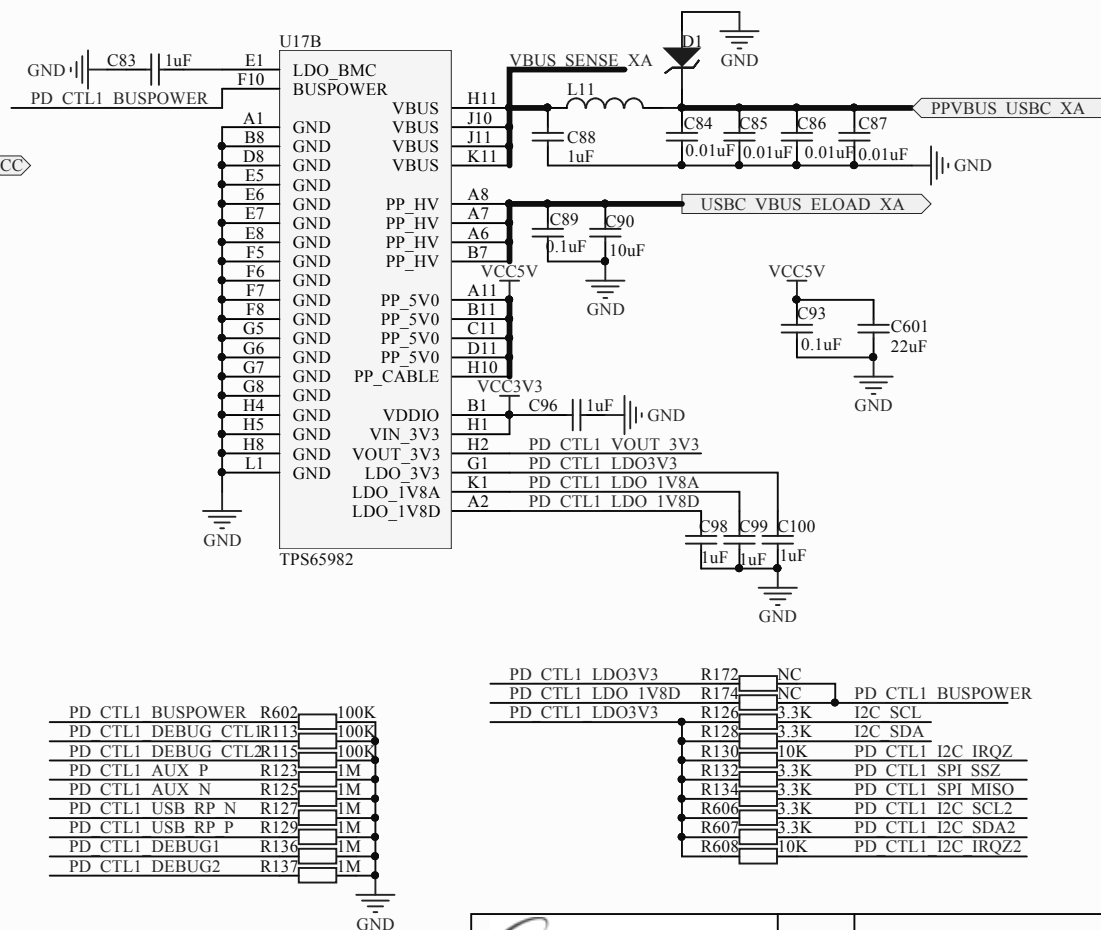
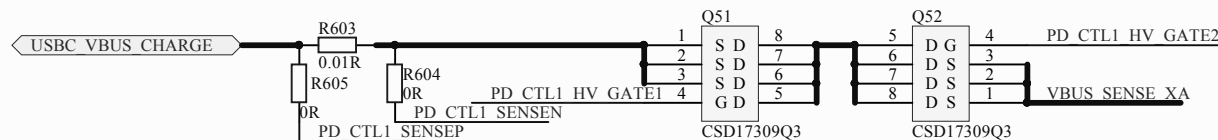
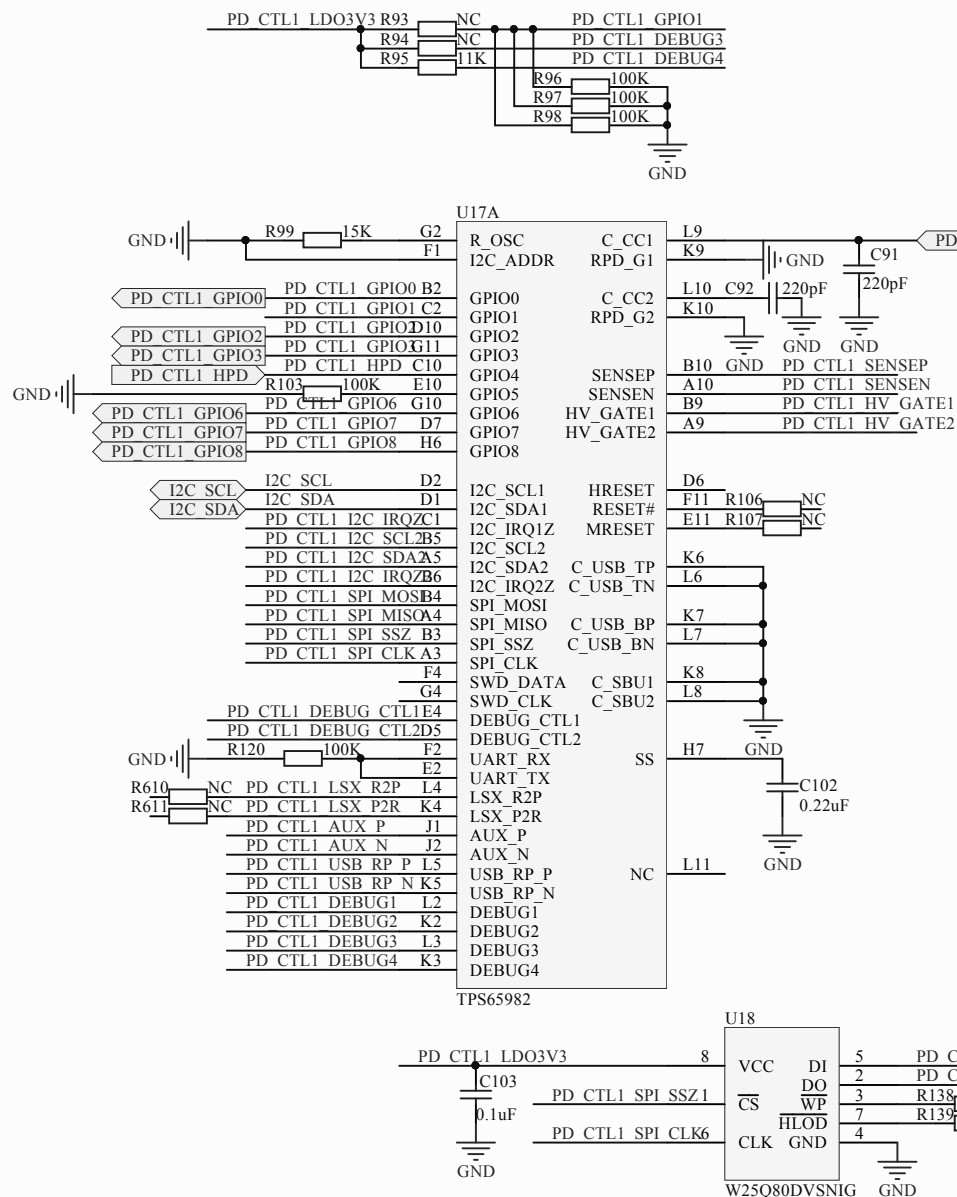



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Date	17/2/9	DocNumber	

A

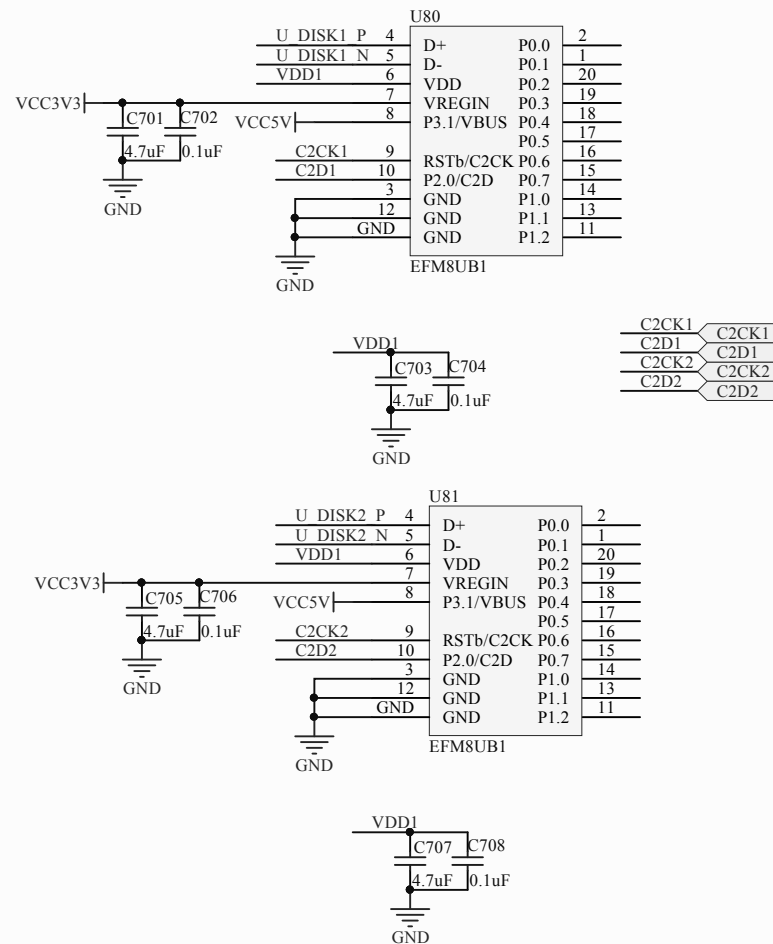
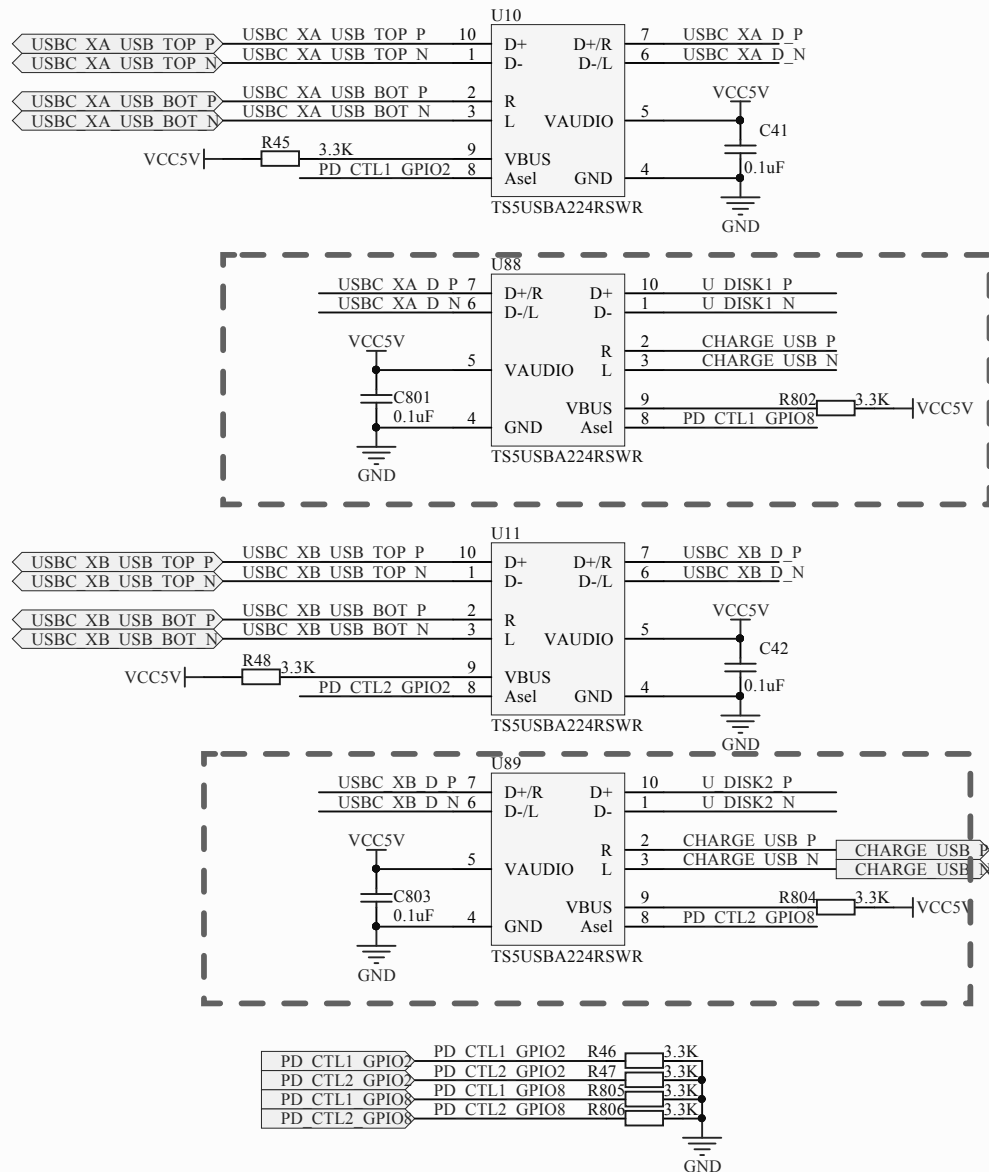
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PD CONTROLLER 1



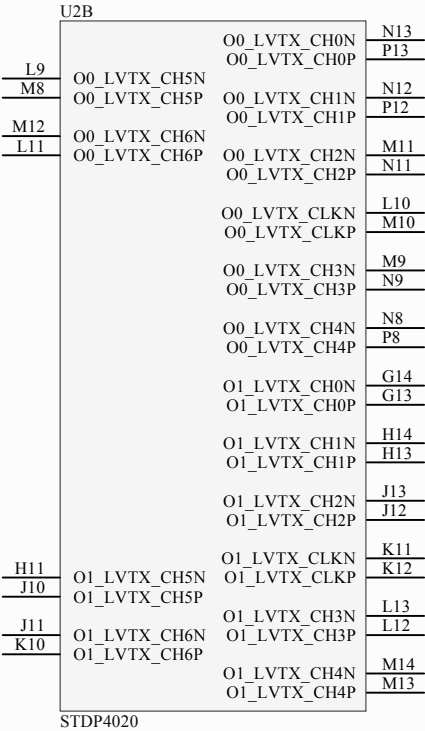
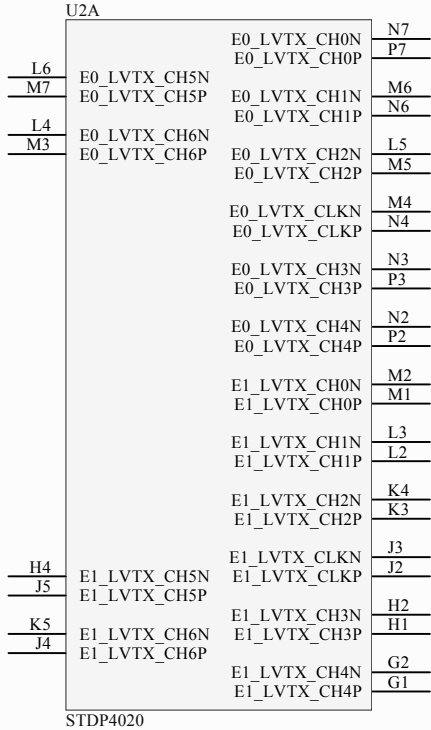
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USB2.0



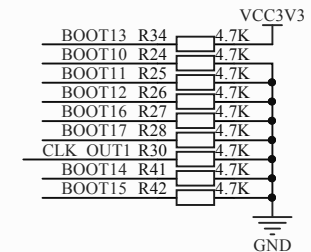
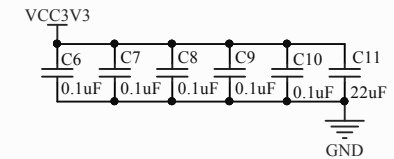
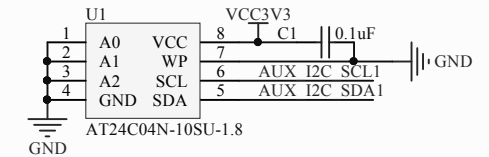
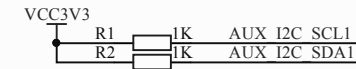
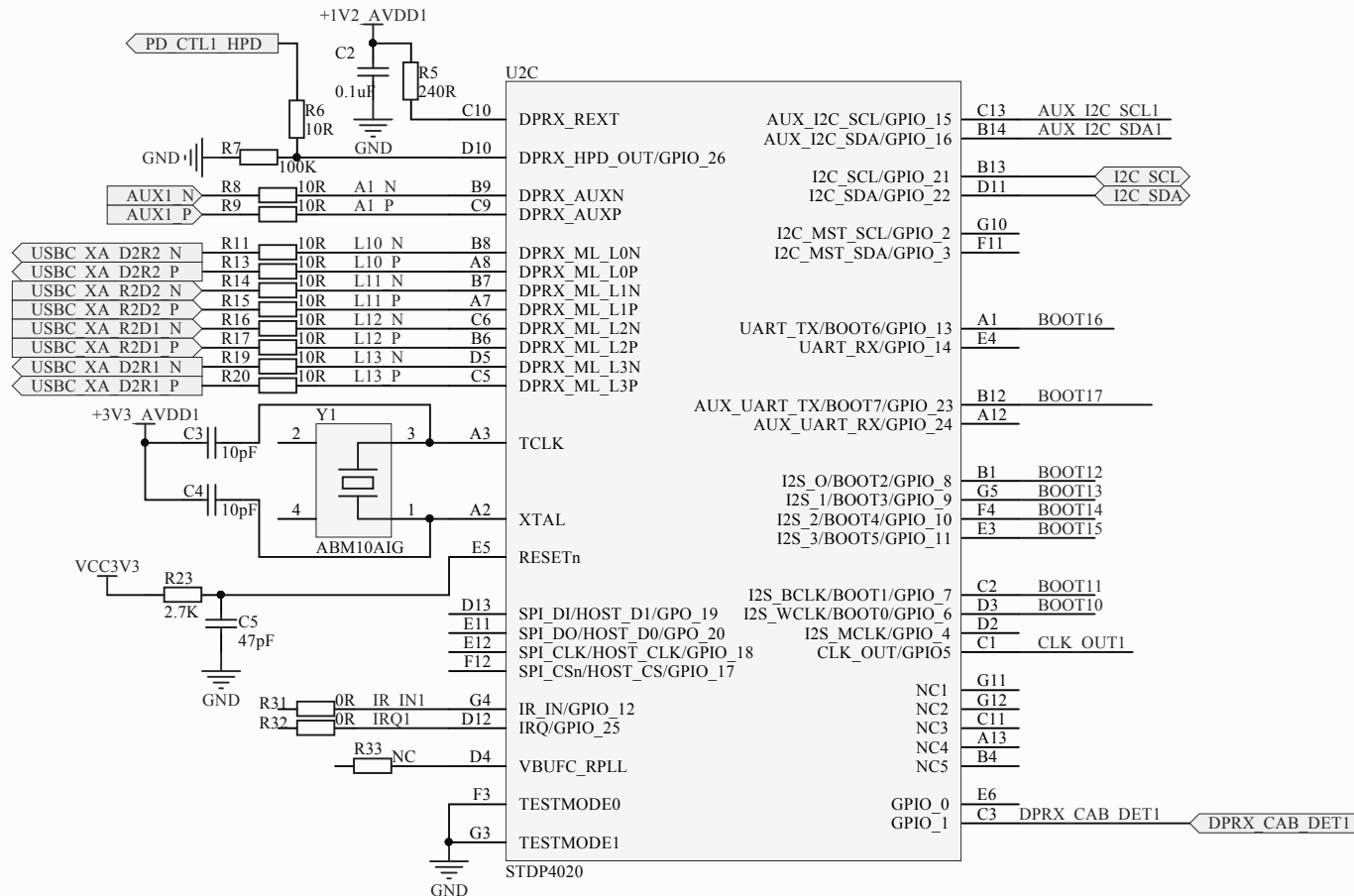
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DP_RECEIVER1_1



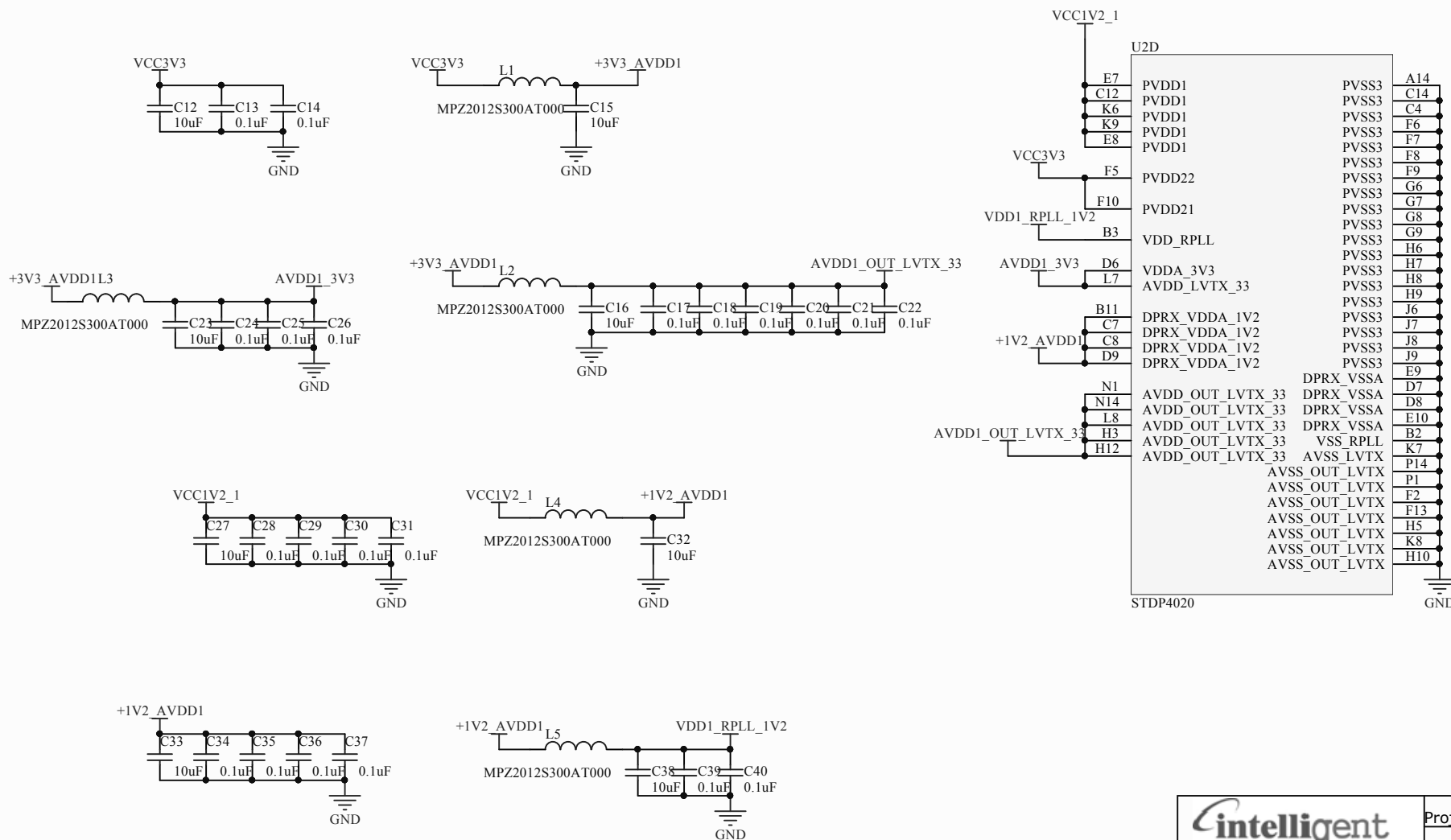
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DP_RECEIVER1_2



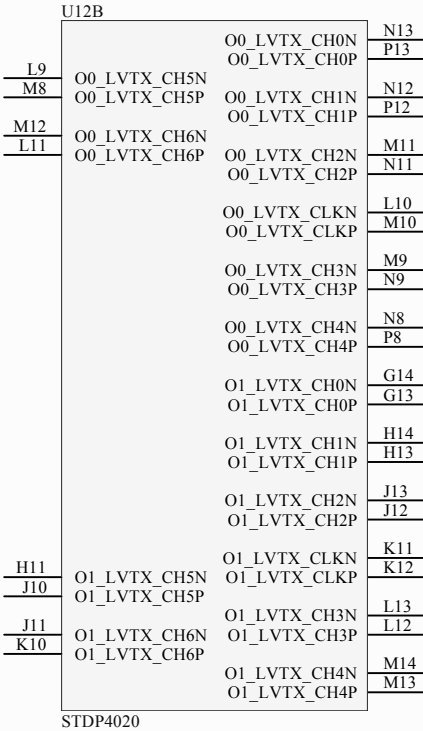
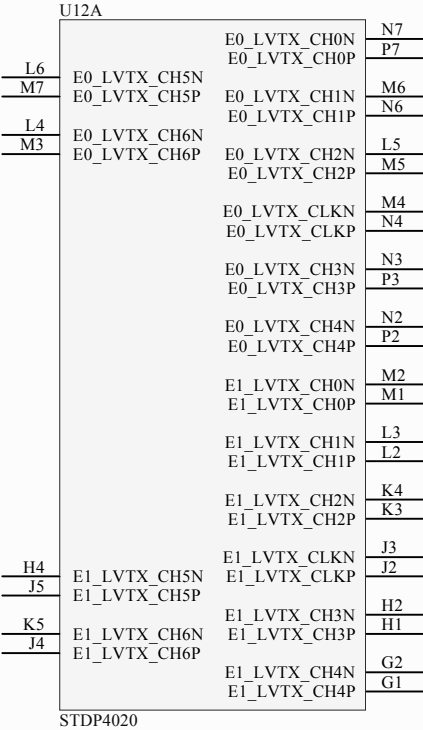
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DP_RECEIVER1_3



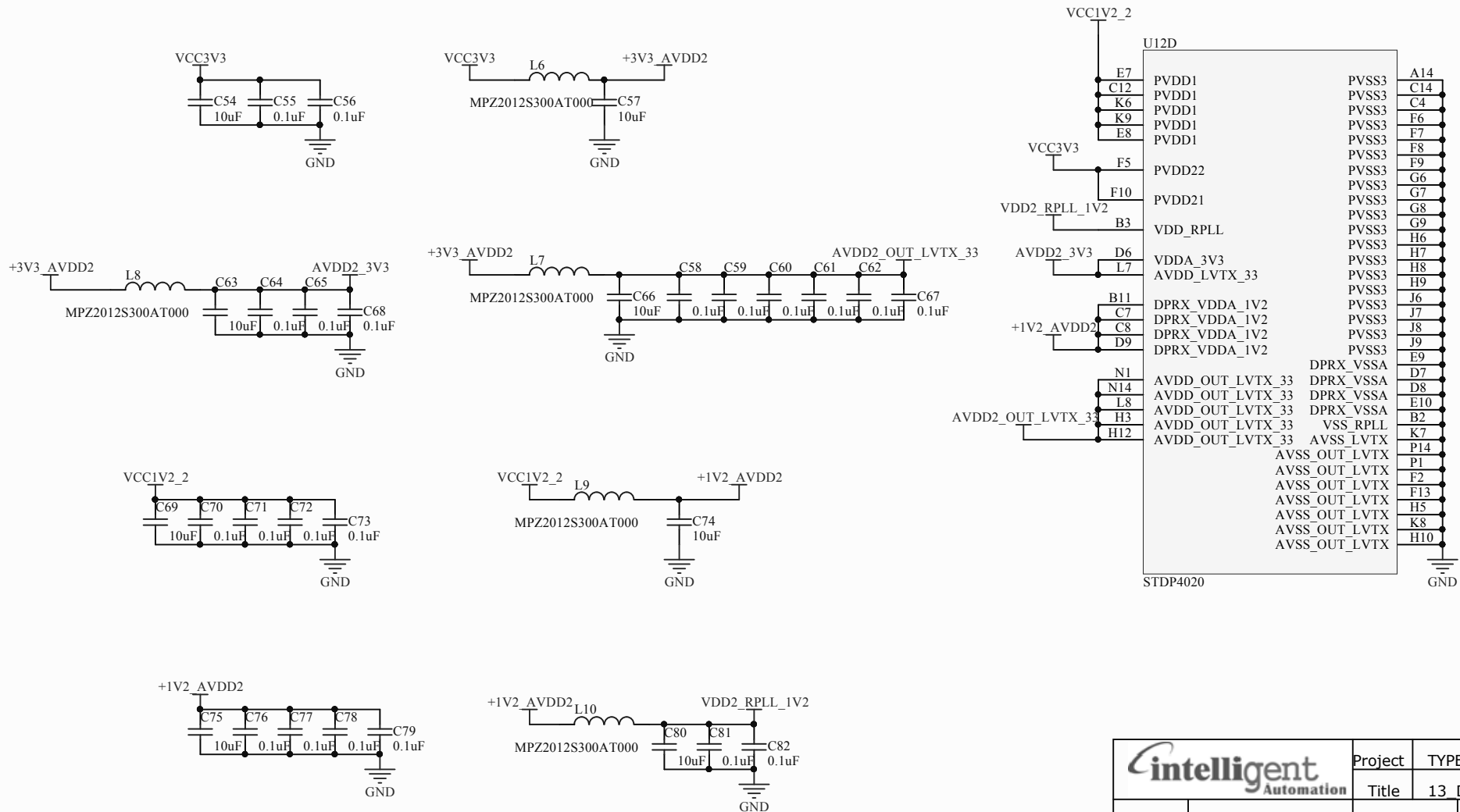
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DP_RECEIVER2_1



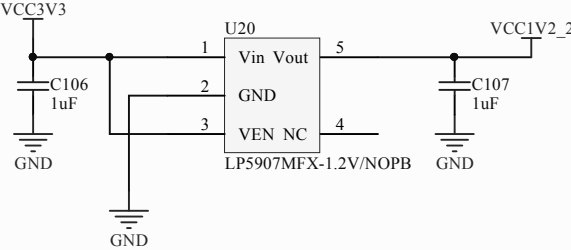
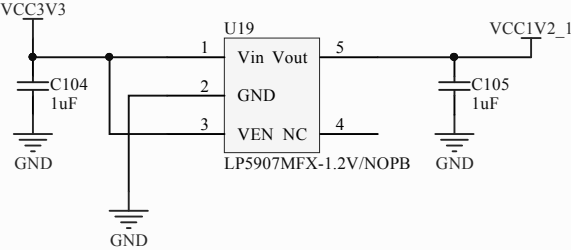
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DP_RECEIVER2_3



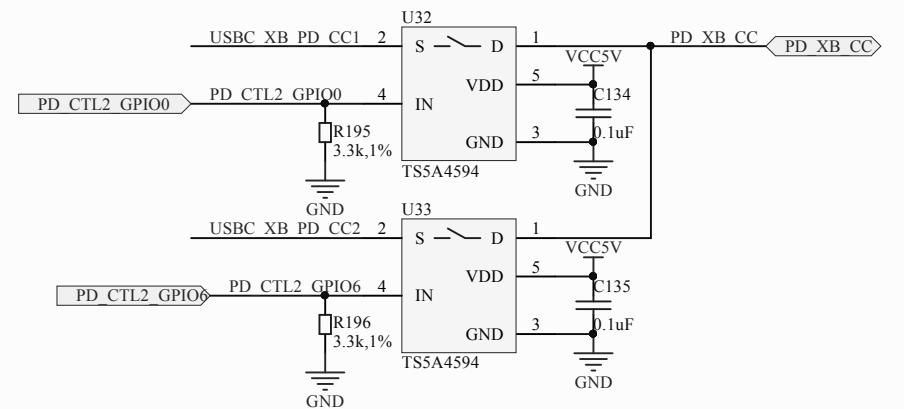
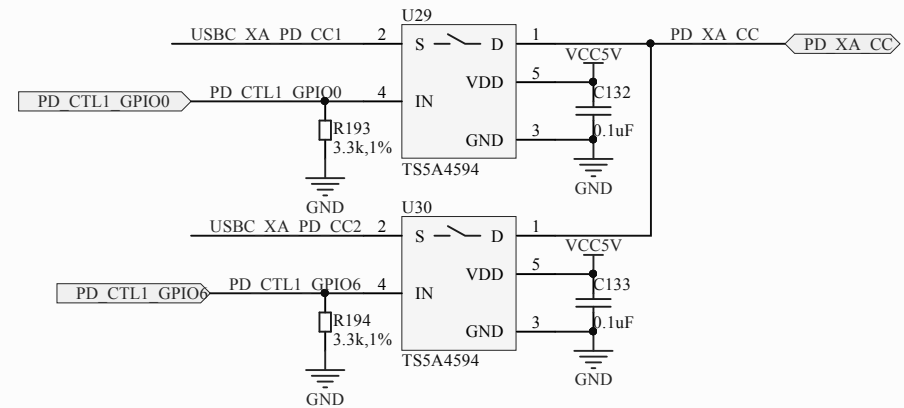
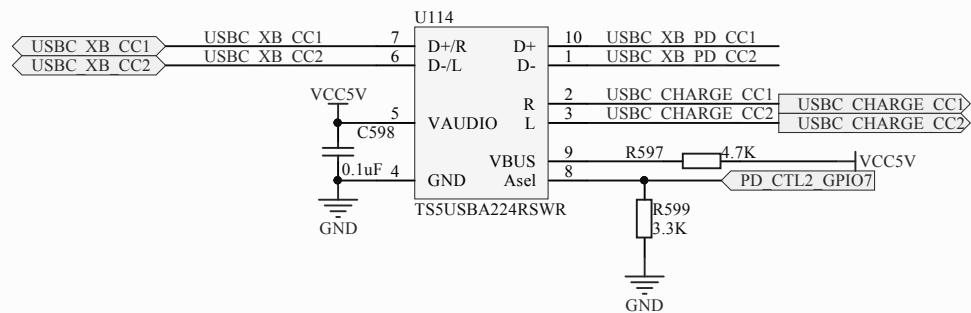
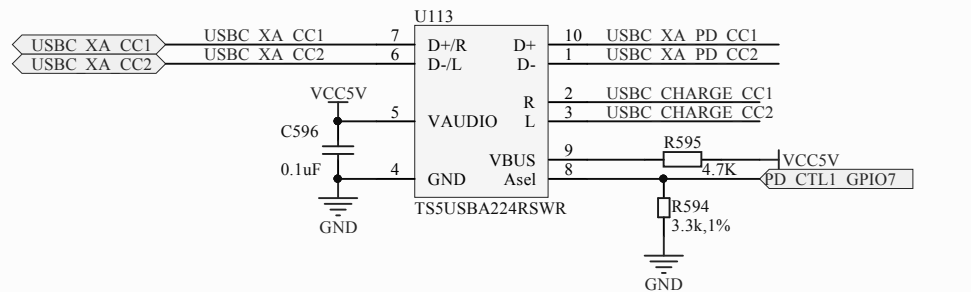
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POWER SUPPLY



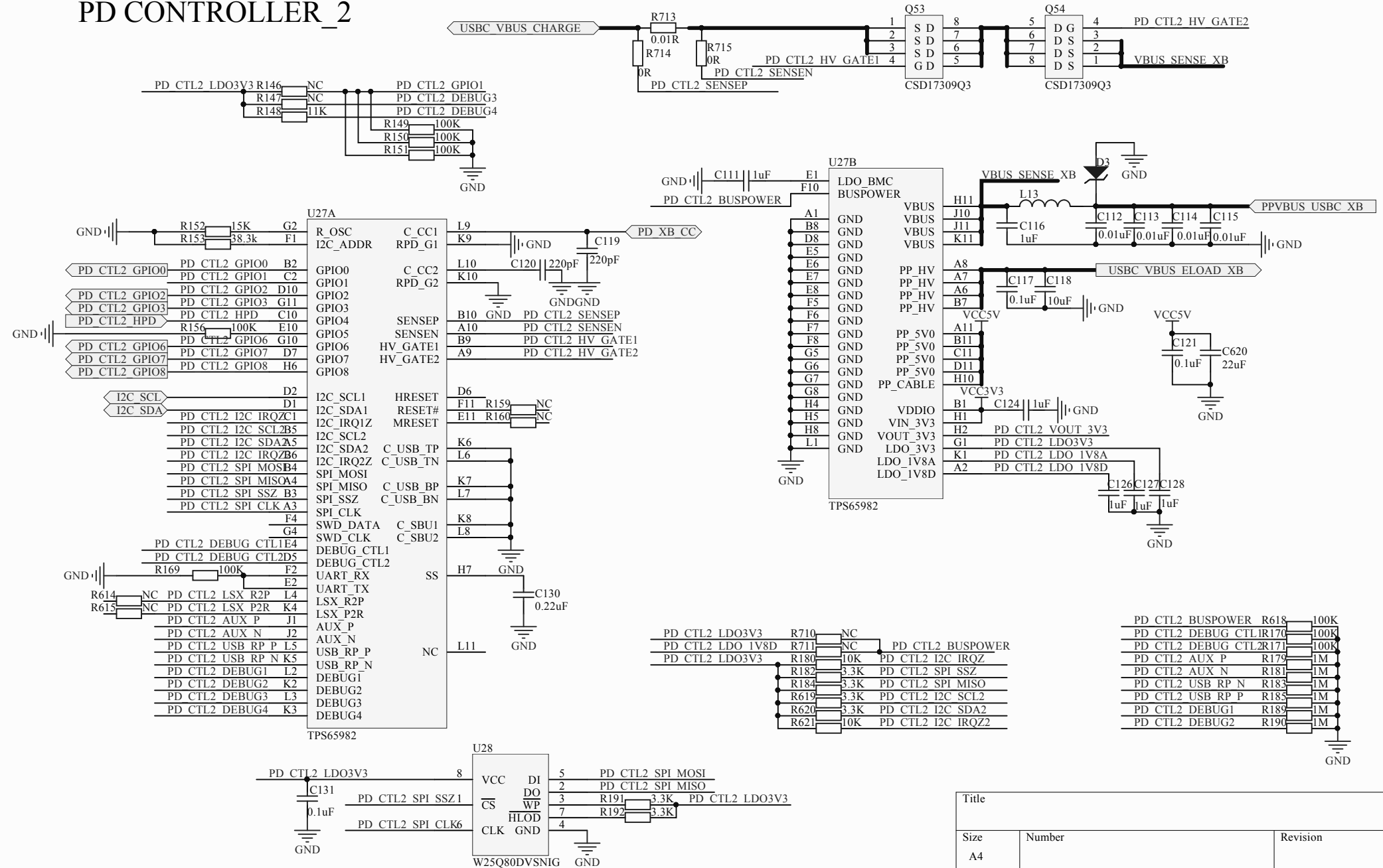
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CC_RELAY



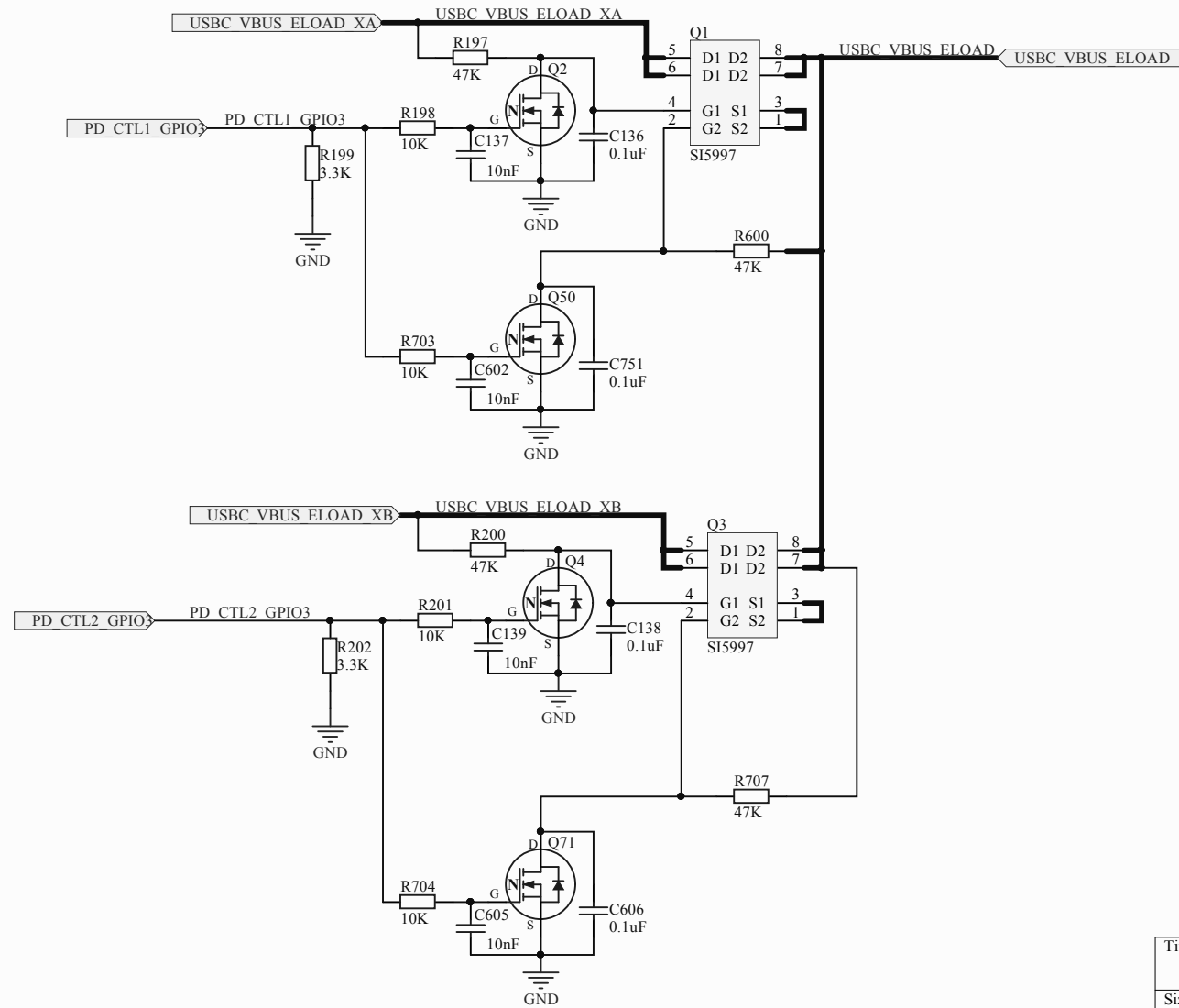
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PD CONTROLLER 2



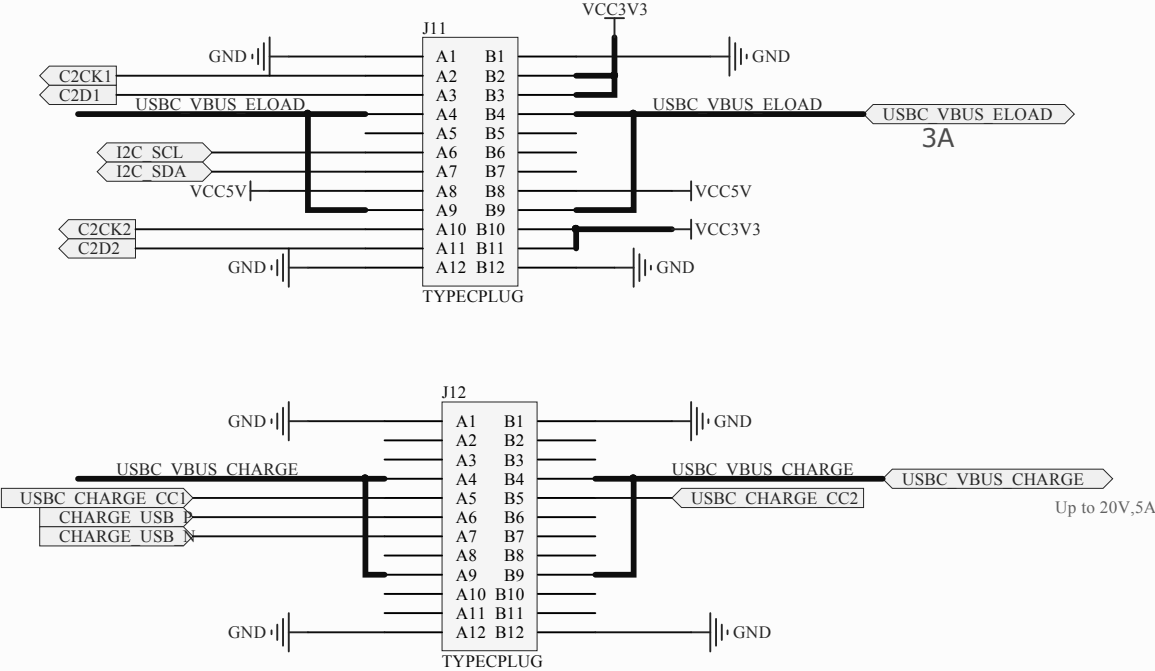
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E-LOAD RELAY



Title		
Size	Number	Revision
A4		
Date:	17/2/9	Sheet of
File:	\\.\14 E-LOAD RELAY.SchDoc	Drawn By:

MODULE INTERFACE



Title		
Size	Number	Revision
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