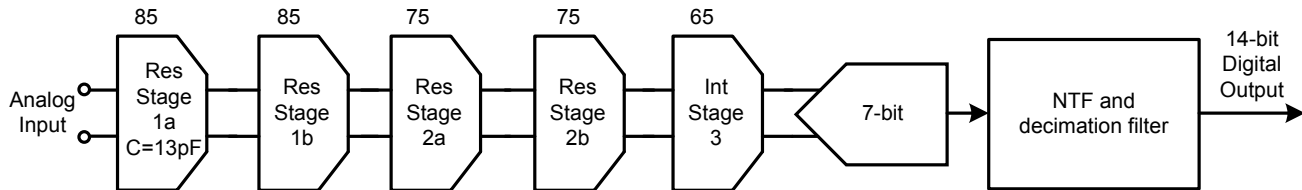


14-bit, OSR = 4, OLSDM/Pipelined Hybrid



14-bit, 1.5-bit stage, pipelined ADC

