



Calibration registers and serial interface

This diagram shows a microchip die with various functional blocks highlighted by white boxes. The blocks are arranged in a horizontal row across the middle of the die. From left to right, they are: a Clock generator, a Flash memory block, and eight stages labeled Stage 8 through Stage 1. Above this row is a large block for Calibration registers and serial interface. The die itself has a grid-like pattern of circuitry and is surrounded by a border of larger, square-shaped pads.

Clock generator

Flash

Stage 8

Stage 7

Stage 6

Stage 5

Stage 4

Stage 3

Stage 2

Stage 1