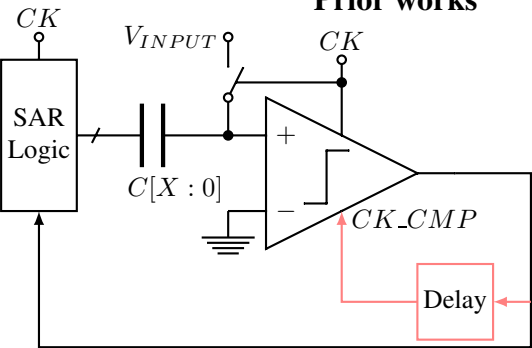
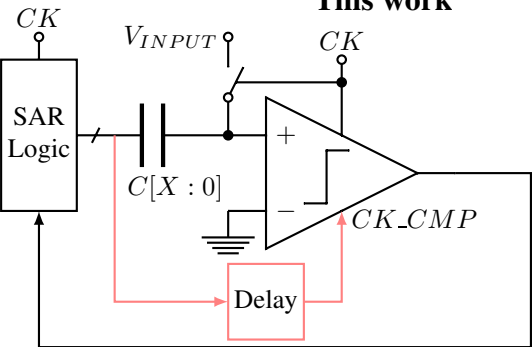


## Prior works



(a) Clock generation separate from CDAC

## This work



(b) Clock generation including CDAC