

WUQIAN TANG

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RESEARCH INTERESTS

Logic Synthesis Optimization; Placement and Routing Optimization; Various Algorithms in Electronic Design Automation (EDA).

WORK EXPERIENCE

The Chinese University of Hong Kong Shenzhen Research Institute
Assistant Researcher in Design Automation Laboratory
PI: Prof. Bei Yu

Shenzhen, Mainland China
Jan. 2024 – Mar. 2024

EDUCATION

National Tsing Hua University
Ph.D. Student in Computer Science
Supervisor: Prof. Chun-Yao Wang

Hsinchu, Taiwan
Sep. 2023 – Present

National Tsing Hua University
M.S. in Computer Science
Supervisor: Prof. Chun-Yao Wang

Hsinchu, Taiwan
Sep. 2021 – Jul. 2023

Thesis: Model Reduction Using a Hybrid Approach of Genetic Algorithm and Rule-based Method

Ming Chuan University
B.S. in Computer Science and Information Engineering
Honor: Outstanding Contribution Award (2/115)

Taoyuan, Taiwan
Sep. 2017 – Jun. 2021
Graduation Ceremony

SELECTED AWARDS AND HONORS

- “Qilin Cup” (top-1 from all 400+ teams) in Integrated Circuit EDA Elite Challenge 2023
- Honorable Mention (4th Place) in 2023 CAD Contest @ ICCAD 2023
- Third Place in 2023 EDATHON 2023
- First Place Award in 2022 CAD Contest @ ICCAD 2022
- National Tsing Hua University Mainland China Student Scholarship 2021 – 2024
- Silver Award in International Collegiate Programming Contest (ICPC), Asia Taipei-Hsinchu Site 2020

PUBLICATIONS

Conference Papers

[C₃] Chuan-Shun Huang, **Wuqian Tang**, Yung-Chih Chen, Yi-Ting Li, Shih-Chieh Chang, Chun-Yao Wang, “An Efficient Approach to Iterative Network Pruning”, IEEE International VLSI Symposium on Technology, Systems and Applications (VLSI-TSA), Hsinchu, Taiwan, Apr. 22–25, 2024.

[C₂] **Wuqian Tang**, Yi-Ting Li, Kai-Po Hsu, Kuan-Ling Chou, You-Cheng Lin, Chia-Feng Chien, Tzu-Li Hsu, Yung-Chih Chen, Ting-Chi Wang, Shih-Chieh Chang, TingTing Hwang, and Chun-Yao Wang, “A Hybrid Approach to Reverse Engineering on Combinational Circuits”, IEEE/ACM Proceedings Design, Automation and Test in Europe (DATE), Valencia, Spain, Mar. 25–27, 2024.

[C₁] **Wuqian Tang**, Chuan-Shun Huang, Yung-Chih Chen, Yi-Ting Li, Shih-Chieh Chang, Chun-Yao Wang, “Model Reduction Using a Hybrid Approach of Genetic Algorithm and Rule-based Method”, Workshop on Synthesis And System Integration of Mixed Information technologies (SASIMI), Taipei, Taiwan, Mar. 11–12, 2024.

SKILLS AND OTHERS

Languages: Verilog, C/C++, Java, Python, SQL, and MATLAB.

Certification: Expert Level in Collegiate Programming Examination (CPE), certified by ACM-ICPC Contest Council for Taiwan.

Hobbies: Badminton, Traditional Chinese and oracle bone script.