# **WUQIAN TANG**

+886 900222211 \(\phi\) wqtang.tsinghua@gmail.com

### **RESEARCH INTERESTS**

Logic Synthesis Optimization; Placement and Routing Optimization; Various Algorithms in Electronic Design Automation (EDA).

# **WORK EXPERIENCE**

# The Chinese University of Hong Kong Shenzhen Research Institute

Shenzhen, Mainland China Jan. 2024 – Mar. 2024

Assistant Researcher in Design Automation Laboratory

Supervisor: Prof. Bei Yu

**EDUCATION** 

### **National Tsing Hua University**

Hsinchu, Taiwan

Ph.D. Student in Computer Science Supervisor: Prof. Chun-Yao Wang Sep. 2023 – Present

**National Tsing Hua University** 

Hsinchu, Taiwan

M.S. in Computer Science

Sep. 2021 – Jul. 2023

Supervisor: Prof. Chun-Yao Wang

Thesis: Model Reduction Using a Hybrid Approach of Genetic Algorithm and Rule-based Method

# Ming Chuan University

Taoyuan, Taiwan

B.S. in Computer Science and Information Engineering

Sep. 2017 - Jun. 2021

Honor: Outstanding Contribution Award (2/115)

Graduation Ceremony

### SELECTED AWARDS AND HONORS

| • "Qilin Cup" (top-1 from all 400+ team | ns) in Integrated Circuit EDA Elite Challen | nge 2023 |
|---|---|----------|
|   |   |          |

• Honorable Mention (4th Place) in 2023 CAD Contest @ ICCAD

2023

• Third Place in 2023 EDAthon

2023

• First Place Award in 2022 CAD Contest @ ICCAD

2022

• National Tsing Hua University Mainland China Student Scholarship

2021 – 2024

• Silver Award in International Collegiate Programming Contest (ICPC), Asia Taipei-Hsinchu Site

2020

### **PUBLICATIONS**

# **Conference Papers**

- [C<sub>3</sub>] **Wuqian Tang**, Yi-Ting Li, Kai-Po Hsu, Kuan-Ling Chou, You-Cheng Lin, Chia-Feng Chien, Tzu-Li Hsu, Yung-Chih Chen, Ting-Chi Wang, Shih-Chieh Chang, TingTing Hwang, and Chun-Yao Wang, "A Hybrid Approach to Reverse Engineering on Combinational Circuits", IEEE/ACM Proceedings Design, Automation and Test in Europe (**DATE**), Valencia, Spain, Mar. 25–27, 2024.
- [C<sub>2</sub>] Chuan-Shun Huang, **Wuqian Tang**, Yung-Chih Chen, Yi-Ting Li, Shih-Chieh Chang, Chun-Yao Wang, "An Efficient Approach to Iterative Network Pruning", Workshop on Synthesis And System Integration of Mixed Information technologies (**SASIMI**), Taipei, Taiwan, Mar.11–12, 2024.
- [C<sub>1</sub>] **Wuqian Tang**, Chuan-Shun Huang, Yung-Chih Chen, Yi-Ting Li, Shih-Chieh Chang, Chun-Yao Wang, "Model Reduction Using a Hybrid Approach of Genetic Algorithm and Rule-based Method", Workshop on Synthesis And System Integration of Mixed Information technologies (**SASIMI**), Taipei, Taiwan, Mar.11–12, 2024.

# **SKILLS AND OTHERS**

Languages: Verilog, C/C++, Java, Python, SQL, and MATLAB.

Certification: Expert Level in Collegiate Programming Examination (CPE), certified by ACM-ICPC Contest Council for Taiwan.

Hobbies: Badminton, Traditional Chinese and oracle bone script.