**WUQIAN TANG**

+886 900222211 ◇ wqtang@cs.nthu.edu.tw

**RESEARCH INTERESTS**

Logic Synthesis Optimization; Placement and Routing Optimization; Various Algorithms in Electronic Design Automation (EDA).

**WORK EXPERIENCE**

**The Chinese University of Hong Kong Shenzhen Research Institute** *Shenzhen, Mainland China*

Assistant Researcher in Design Automation Laboratory *Jan. 2024 – Mar. 2024*

PI: Prof. Bei Yu

**EDUCATION**

**National Tsing Hua University** *Hsinchu, Taiwan*

Ph.D. Student in Computer Science  *Sep. 2023 – Present*

Supervisor: Prof. Chun-Yao Wang

**National Tsing Hua University** *Hsinchu, Taiwan*

M.S. in Computer Science  *Sep. 2021 – Jul. 2023*

Supervisor: Prof. Chun-Yao Wang

Thesis: Model Reduction Using a Hybrid Approach of Genetic Algorithm and Rule-based Method

**Ming Chuan University** *Taoyuan, Taiwan*

B.S. in Computer Science and Information Engineering  *Sep. 2017 – Jun. 2021*

Honor: Outstanding Contribution Award (2/115) *Graduation Ceremony*

**SELECTED AWARDS AND HONORS**

* “Qilin Cup” (top-1 from all 400+ teams) in [Integrated Circuit EDA Elite Challenge](https://eda.icisc.cn/) *2023*
* Honorable Mention (4th Place) in [2023 CAD Contest @ ICCAD](https://iccad-contest.org/2023/)   *2023*
* Third Place in [2023 EDAthon](https://sites.google.com/view/ceda-hk/edathon-2023) -*2023*
* First Place Award in [2022 CAD Contest @ ICCAD](https://iccad-contest.org/2022/) *2022*
* National Tsing Hua University Mainland China Student Scholarship *2021 – 2024*
* Silver Award in International Collegiate Programming Contest (ICPC), Asia Taipei-Hsinchu Site *2020*

**PUBLICATIONS**

**Conference Papers**

[C3] Chuan-Shun Huang, **Wuqian Tang**, Yung-Chih Chen, Yi-Ting Li, Shih-Chieh Chang, Chun-Yao Wang, “An Efficient Approach to Iterative Network Pruning”, IEEE International VLSI Symposium on Technology, Systems and Applications (**VLSI-TSA**), Hsinchu, Taiwan, Apr. 22–25, 2024.

[C2] **Wuqian Tang**, Yi-Ting Li, Kai-Po Hsu, Kuan-Ling Chou, You-Cheng Lin, Chia-Feng Chien, Tzu-Li Hsu, Yung-Chih Chen, Ting-Chi Wang, Shih-Chieh Chang, TingTing Hwang, and Chun-Yao Wang, “A Hybrid Approach to Reverse Engineering on Combinational Circuits”, IEEE/ACM Proceedings Design, Automation and Test in Europe (**DATE**), Valencia, Spain, Mar. 25–27, 2024.

[C1] **Wuqian Tang**, Chuan-Shun Huang, Yung-Chih Chen, Yi-Ting Li, Shih-Chieh Chang, Chun-Yao Wang, “Model Reduction Using a Hybrid Approach of Genetic Algorithm and Rule-based Method”, Workshop on Synthesis And System Integration of Mixed Information technologies (**SASIMI**), Taipei, Taiwan, Mar.11–12, 2024.

**SKILLS AND OTHERS**

**Languages:** Verilog, C/C++, Java, Python, SQL, and MATLAB.

**Certification:** Expert Level in [Collegiate Programming Examination (CPE)](https://cpe.cse.nsysu.edu.tw/), certified by ACM-ICPC Contest Council for Taiwan.

**Hobbies:** Badminton, Traditional Chinese and oracle bone script.