
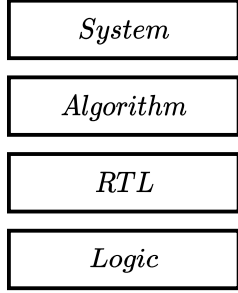



*Level of Abstraction*



*Level of Detail*



*Verilog*



*VHDL*



*SystemC / C++*



*Matlab / Simulink*