

Simple **SignalTapII Starter Guide**

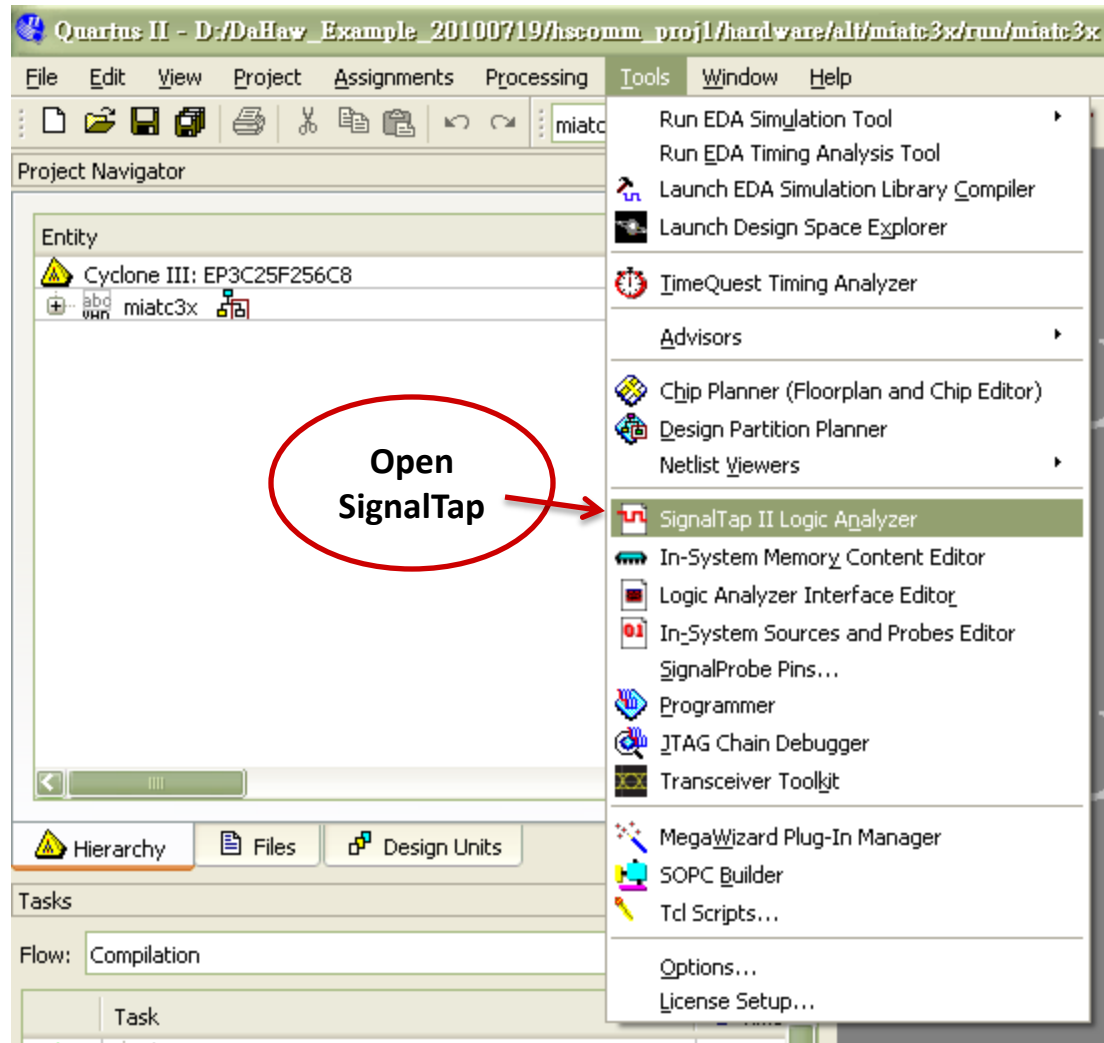
MIAT-C3X FPGA Development Board

浯陽科技有限公司



CSIE MIAT Lab.
National Central University

Open SignalTapII



SignalTap II Logic Analyzer - D:/DaHaw_Example_20100719/hscomm_proj1/hardware/alt/miatc3x/run/miatc3x - miatc3x - [./stp/stp1.s...

File Edit View Project Processing Tools Window

Instance Manager: Ready to acquire

Instance	Status	LEs: 881	Memory: 139264	M512,MLAB:
auto_sig...	Not running	881 cells	139264 bits	0 blocks

JTAG Chain Configuration: JTAG ready

Hardware: USB-Blaster [USB-0]

Device: @1: EP3C25/EP4CE22 (0x020F30DD)

SOF Manager: hardware/alt/miatc3x/output/miatc3x.sof

trigger: 2010/07/20 22:16:58 #0 Lock mode: Allow all changes

Type	Alias	Name	Data Enable	Trigger Enable	Trigger Conditions
		+c3x_gpio_b1[33..22]	34	34	1 Basic
		+c3x_gpio_b1[21..6]	34	34	xxxxh
		c3x_gpio_b1[5]	34	34	x
		c3x_gpio_b1[4]	34	34	x
		c3x_gpio_b1[3]	34	34	x
		c3x_gpio_b1[2]	34	34	x
		c3x_gpio_b1[1]	34	34	F
		c3x_gpio_b1[0]	34	34	x

Signal Configuration:

Clock: pll100:pll100_0|c0

Sample depth: 4 K RAM type: Auto

Segmented: 2 2 K sample size

Storage qualifier:

Type: Continuous

external_storage_qualifier

external_storage_qualifier

Hierarchy Display:

Data Log: auto_signaltap_0

auto_signaltap_0

100% 00:00:00



Scan Chain

Fetch Clock

Fetch Depth

Trigger Condition

Add Target Signals

SignalTap II Logic Analyzer - D:\DaHaw_Example_20100719\hscmm_proj1\hardware\alt\miatc3x\run\miatc3x - miatc3x - [../stp/stp1.s...

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		c3x_gpio_b1[3]
		c3x_gpio_b1[2]
		c3x_gpio_b1[1]
		c3x_gpio_b1[0]

Node Finder

Named: * Filter: Pins: all Customize... List OK

Look in: |miatc3x| Include subentities Cancel

Nodes Found:

Name	Assignments	Type
------	-------------	------

Selected Nodes:

Name	Assignments	Type
------	-------------	------

Double Click

Note
