AT91RM9200 Pins

Pin	Active	Direction	Connection	Use
VDDCORE	N/A	IN	1.65-1.95 V	Supply power to core logic
VDDIOM	N/A	IN	3.0-3.6 V	Supply power to Memory I/O Lines
VDDIOP	N/A	IN	3.0-3.6 V	Supply power to peripheral I/O lines
VDDOSC	N/A	IN	1.65-1.95 V	Supply power to oscillator
VDDPLL	N/A	IN	1.65-1.95 V	Supply power to oscillator and PLL
GND	N/A	IN	Ground	Ground
GNDOSC	N/A	IN	Ground	Ground
GNDPLL	N/A	IN	Ground	Ground
RESET				
XIN32	N/A	IN	32 kHz oscillator	Monitoring 32 kHz oscillator
XOUT32	N/A	OUT	32 kHz oscillator circuit	Driving 32 kHz oscillator
XIN	N/A	IN	Main oscillator	Monitoring main oscillator
XOUT	N/A	OUT	Main oscillator	Driving main oscillator
PLLRCA	N/A	IN	2 nd order filter for PLL	PLL A Filter
PLLRCB	N/A	IN	2 nd order filter for PLL	PLL B Filter
NRST	LOW	IN	Reset circuitry	Power on reset and system reset
NTRST	LOW	IN	Reset circuitry	Power on reset and debugger interface resets (ICE/JTAG)
TST0	N/A	IN	Pull-down GND	Manufacturer tests, don't use
TST1	N/A	IN	Pull-down GND	Manufacturer tests, don't use

Pin	Active	Direction	Connection	Use
A0/NBS0	N/A	ОИТ	Address Bus (through buffer)	Used for sending address to chip selected by chip select
A1/NBS2/ NWR2	N/A	OUT	Address Bus (through buffer)	Used for sending address to chip selected by chip select
A2-A15	N/A	OUT	Address Bus (through buffer)	Used for sending address to chip selected by chip select
A16/BA0	N/A	ОИТ	Address Bus (through buffer)	Used for sending address to chip selected by chip select
A17/BA1	N/A	ОИТ	Address Bus (through buffer)	Used for sending address to chip selected by chip select
A18-A22	N/A	ОИТ	Address Bus (through buffer)	Used for sending address to chip selected by chip select
D0-D15	N/A	Bidirectional	Data bus (through buffer)	Sending and receiving data from data bus
NCS0/BFCS	LOW	OUT	None	None
NCS1/SDCS	LOW	OUT	SRAM chip select	Select SRAM Chip when accessing SRAM
NCS2	LOW	OUT	DRAM chip select	Select DRAM Chip when accessing DRAM
NCS3/SMCS	LOW	OUT	LCD Display chip select	Selecting LCD Display.
NRD/NOE/ CFOE	LOW	OUT	Chips on data bus	Enable output to data bus on selected chip
NWR0/NEW/ CFWE	LOW	OUT	Chips on data bus	Enable input from data bus on selected chip
NWR1/NBS1/ CFIOR	LOW	OUT	Chips on data bus	Enable input from data bus on selected chip
NWR3/NBS3/ CFIOW	LOW	OUT	Chips on data bus	Enable input from data bus on selected chip
RAS	LOW	ОИТ	SDRAM	Row Select Signal
CAS	LOW	ОИТ	SDRAM	Column Select Signal
SDCK	N/A	ОИТ	SDRAM clock	SDRAM Clock
SDCKE	HIGH	OUT	SDRAM clock enable	SDRAM Clock enable
SDWE	LOW	ОИТ	SDRAM	SDRAM Write Enable
SDA10	N/A	ОИТ	SDRAM	SDRAM address 10 Line

Pin	Active	Direction	Connection	Use
HDMA	N/A	N/A	Not Used	USB (not used)
HDPA	N/A	N/A	Not Used	USB (not used)
DDM	N/A	N/A	Not Used	USB (not used)
DDP	N/A	N/A	Not Used	USB (not used)
PA0/MISO/ PCK3	N/A	IN	Audio DAC/ADC	Audio DAC/ADC SPI
PA1/MOSI/ PCK0	N/A	OUT	Audio DAC/ADC	Audio DAC/ADC SPI
PA2/SPCK/ IRQ4	N/A	OUT	Audio DAC/ADC	Audio DAC/ADC SPI Clock
PA3/NPCS0/ IRQ5	LOW	ОИТ	Audio DAC	Select audio DAC chip
PA4/NPCS1/ PCK1	LOW	OUT	Audio ADC	Select audio ADC chip
PA5/NPCS2/ TXD3	LOW	OUT	None	None
PA6/NPCS3/ RXD3	LOW	OUT	None	None
PA7/ETXCK/ EREFCK/PCK2	N/A	IN	Ethernet jack	Ethernet clock
PA8/ETXEN/ MCCDB	N/A	OUT	Ethernet jack	Ethernet transmit enable
PA9/ETX0/ MCDB0	N/A	OUT	Ethernet jack	Ethernet transmit data
PA10/ETX1/ MCDB1	N/A	OUT	Ethernet jack	Ethernet transmit data
PA11/ECRS/ ECRSDV/MCDB2	N/A	IN	Ethernet jack	Ethernet carrier sense
PA12/ERX0/ MCDB3	N/A	IN	Ethernet jack	Ethernet receive data
PA13/ERX1/ TCLK0	N/A	IN	Ethernet jack	Ethernet receive data
PA14/ERXER /TCLK1	N/A	IN	Ethernet jack	Ethernet receive data
PA15/EMDC/ TCLK2	N/A	IN	Ethernet jack	Ethernet management data clock
PA16/EMDIO /IRQ6	N/A	IN/OUT	Ethernet jack	Ethernet management data input/output
PA17/TXD0/ TIOA0	N/A	Not used	Not used	Not used
PA18/RXD0/ TIOB0	N/A	Not used	Not used	Not used

Pin	Active	Direction	Connection	Use
PA19/SCK0/	N/A	Not used	Not used	Not used
TIOA1				
PA20/CTS0/	N/A	Not used	Not used	Not used
TIOB1				
PA21/RTS0/	N/A	Not used	Not used	Not used
TIOA2				
PA22/RXD2/	N/A	Not used	Not used	Not used
TIOB2				
PA23/TXD2/	N/A	Not used	Not used	Not used
IRQ3	N1 / A	NI - I I	NI. I I	No.
PA24/SCK2/	N/A	Not used	Not used	Not used
PCK1	N1 / A	1/0	Dootloodon	I2C data line to be obtained as
PA25/TWD/	N/A	1/0	Bootloader	I2C data line to bootloader
IRQ2 PA26/TWCK/	NI/A	1/0	Bootloader	I2C clock line to bootloader
IRQ1	N/A	1/0	Bootioader	ize clock line to bootloader
PA27/MCCK/	N/A	Not used	Not used	Not used
TCLK3	IN/A	Not used	Not used	Not used
PA28/MCCDA	N/A	Not used	Not used	Not used
/TCLK4	IN/A	Not asca	Not useu	lvot useu
PA29/MCDA0/	N/A	Not used	Not used	Not used
TCLK5	, , .	. Tot useu	1101 4364	. Tot useu
PA30/DRXD/	N/A	IN	Debugger	Debug receive data
CTS2	,			
PA31/DTXD/	N/A	OUT	Debugger	Debug transmit data
RTS2/BMS				
PB0/TF0/	N/A	Not used	Not used	Not used
RTS3				
PB1/TK0/	N/A	Not used	Not used	Not used
CTS3				
PB2/TD0/	N/A	Not used	Not used	Not used
SCK3				
PB3/RD0/	N/A	Not used	Not used	Not used
MCDA1	_			
PB4/RK0/	N/A	Not used	Not used	Not used
MCDA2	21/2			<u></u>
PB5/RF0/	N/A	Not used	Not used	Not used
MCDA3	N1 / A	NI a to consider	Nint	Not word
PB6/TF1/	N/A	Not used	Not used	Not used
TIOA3 PB7/TK1/	NI/A	Not used	Notuced	Not used
TIOB3	N/A	Not used	Not used	Not used
PB8/TD1/	N/A	Not used	Not used	Not used
TIOA4	14/74	ואטנ שאפע	INOL USEU	INOU USEU
PB9/RD1/	N/A	Not used	Not used	Not used
TIOB4	11/7	INOL USEU	I VOL USEU	I VOC USEU
11004			<u> </u>	<u>l</u>

Pin	Active	Direction	Connection	Use
PB10/RK1/ TIOA5	N/A	Not used	Not used	Not used
PB11/RF1/ TIOB5	N/A	Not used	Not used	Not used
PB12/TF2/ ETX2	N/A	Not used	Not used	Not used
PB13/TK2/ ETX3	N/A	Not used	Not used	Not used
PB14/TD2/ ETXER	N/A	Not used	Not used	Not used
PB15/RD2/ ERX2	N/A	Not used	Not used	Not used
PB16/RK2/ ERX3	N/A	Not used	Not used	Not used
PB17/RF2/ ERXDV	N/A	IN	Ethernet jack	Ethernet receive data valid
PB18/RI1/ ECOL	N/A	Not used	Not used	Not used
PB19/DTR1/ ERXCK	N/A	IN	Ethernet jack	Ethernet receive clock
PB20/TXD1	N/A	Not used	Not used	Not used
PB21/RXD1	N/A	Not used	Not used	Not used
PB22/SCK1	N/A	Not used	Not used	Not used
PB23/DCD1	N/A	Not used	Not used	Not used
PB24/CTS1	N/A	Not used	Not used	Not used
PB25/DSR1/ EF100	N/A	Not used	Not used	Not used
PB26/RTS1	N/A	Not used	Not used	Not used
PB27/PCK0	N/A	Not used	Not used	Not used
PB28/FIQ	N/A	Not used	Not used	Not used
PB29/IRQ0	N/A	Not used	Not used	Not used
PC0/BFCK	N/A	IN	Buttons	PC0 Button input (1,1)
PC1/BFRDY/ SMOE	N/A	IN	Buttons	PC1 Button input (1,2)
PC2/BFAVD	N/A	IN	Buttons	PC2 Button input (1,3)

/A	IN	6	
		Buttons	PC3 Button input (1,4)
/A	IN	Buttons	PC4 Button input (2,1)
/A	IN	Buttons	PC5 Button input (2,2)
/A	IN	Buttons	PC6 Button input (2,3)
/A	IN	Buttons	PC7 Button input (2,4)
/A	IN	Buttons	PC8 Button input (3,1)
/A	IN	Buttons	PC9 Button input (3,2)
/A	IN	Buttons	PC10 Button input (3,3)
/A	IN	Buttons	PC11 Button input (3,4)
/A	IN	Buttons	PC12 Button input (4,1)
/A	IN	Buttons	PC13 Button input (4,2)
/A	IN	Buttons	PC14 Button input (4,3)
/A	IN	Buttons	PC15 Button input (4,4)
/A	IN	Buttons	PC16 Button input (Hangup button)
/A	Not used	Not used	Not used
/A	Not used	Not used	Not used
/A	Not used	Not used	Not used
/A	Not used	Not used	Not used
/A	Not used	Not used	Not used
/A	Not used	Not used	Not used
/A	Not used	Not used	Not used
/A	Not used	Not used	Not used
/A	Not used	Not used	Not used
	/A //A //A //A //A //A //A //A //A //A	/A IN	/A IN Buttons /A