```
@Done
(a
@
                               Boot.inc
9
                     Contains definitions used by boot.s
9
@ Revision History:
@ Name
             Comment
@ Will Werst
             Initial version
                                  Some lonely night around 6/10/17
@ Will Werst
            Comment
                                  October 2017
00000 SMC Definitions
   @ SRAM
   @ RWHOLD, RWSETUP
                   0x00----
                                   1/2 cycle setup and hold
                     0x--00----
                                   Standard chip select waveform
   @ ACSS
                    0x----30--
                                   connected to 16-bit device
   @ 1 Wait state
                    0x----81
                                   Enable 1 wait state
   .equ SMC CSR1 VAL, 0x00003081
   @ DRAM
   @ RWHOLD, RWSETUP 0x30-----
                                  3 RWHOLD
   @ ACSS
                    0x--00----
                                 Standard chip select waveform
                    0x----43--
                                 Connected to 8-bit device with 3 clock cycles data float
   @ 8 bit
   Enable 7 wait state
   .equ SMC CSR2 VAL, 0x30004387
   @ RWHOLD, RWSETUP
                     0x00----
                                 1/2 cycle setup and hold
                    0x--00----
   @ ACSS
                                 Standard chip select waveform
   (a
                    0x----40--
                                 Connected to 8-bit device
   @ 3 Wait state
                   0x----83
                                  Enable 3 wait state
   .equ SMC CSR7 VAL, 0x00004083
00000 PMC Definitions
   .equ PMC PLLAR VAL, 0x20153F05 @ MULA = 21 + 1, DIVA = 5, PLLACOUNT = 63, OUTA = 00
   .equ PMC_MCKR_VAL, 0x00000006 @0x0000000E for dram
   .equ PMC_PCK0 VAL, 0x0000006
   .equ PMC PCER VAL, 0x0000001C @Enable PIOA, PIOB
   .equ PMC PCK1 VAL, 0x00000019 @MCK source, Pre-scale by 64
   .equ PMC MCKR MDIV, 0
                               @Run cpu 1+1 times faster than MCK
00000 PIOA Definitions
   .equ PIOA BSR VAL, 0x01000000
   .equ PIOA OER VAL,
                    0x01000000
   .equ PIOA_PDR VAL,
                    0x01000000
00000 PIOB Definitions
   .equ PIOB ASR VAL, 0x08000000
   .equ PIOB OER VAL, 0x08000000
   .equ PIOB PDR VAL, 0x08000000
```