

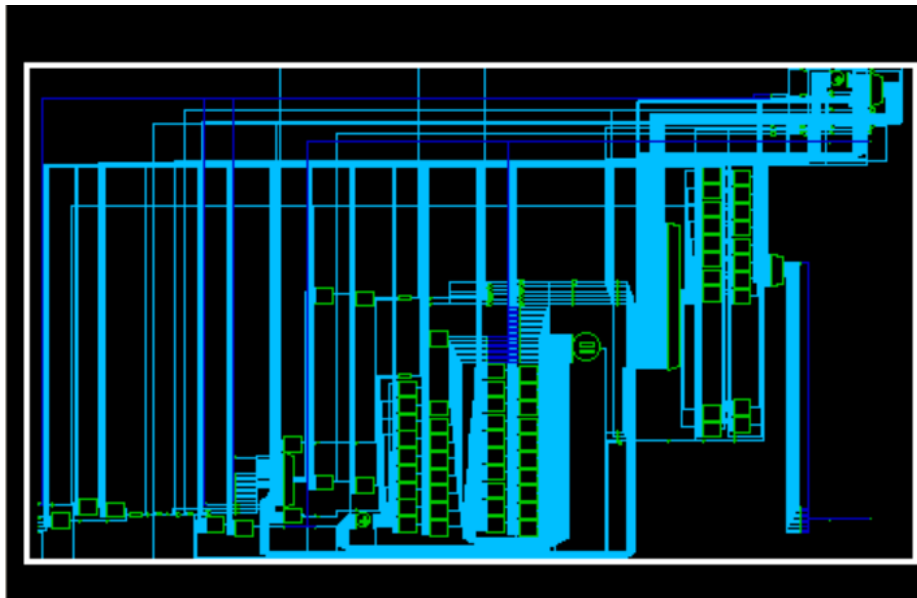
三、ncverilog 模擬結果 (sim + syn)

```
6.nthucad.cs.nthu.edu.tw
FSDB Dumper for IUS, Release Verdi3_J-2014.12-SP3, Linux, 07/05/2015
(C) 1996 - 2015 by Synopsys, Inc.
*Verdi3* FSDB WARNING: The FSDB file already exists. Overwriting the FSDB file may crash the pro
grams that are using this file.
*Verdi3* : Create FSDB file 'PTM.fsdb'
*Verdi3* : Begin traversing the scopes, layer (0).
*Verdi3* : End of traversing.

GET ! addr = 18 , your_flag = 1 , ans_flag = 1
GET ! addr = 32 , your_flag = 1 , ans_flag = 1
GET ! addr = 45 , your_flag = 1 , ans_flag = 1
GET ! addr = 58 , your_flag = 1 , ans_flag = 1
GET ! addr = 70 , your_flag = 1 , ans_flag = 1
GET ! addr = 86 , your_flag = 1 , ans_flag = 1
GET ! addr = 120 , your_flag = 1 , ans_flag = 1
GET ! addr = 159 , your_flag = 1 , ans_flag = 1
GET ! addr = 199 , your_flag = 1 , ans_flag = 1

Result = 9 , Answer = 9
!!!! ACCEPTED !!!!!

Simulation complete via $finish(1) at time 6120 NS + 0
./PTM_tb.v:97 $finish;
ncsim> exit
[dld0117@ic27 ~/lab3]$
```



四、nWave

