```
* the pte page can be thought of an array like this: pte_t[PTRS_PER_PTE]
* this macro returns the index of the entry in the pte page which would
* control the given virtual address
#define pte index(address) \
                (((address) >> PAGE SHIFT) & (PTRS PER PTE - 1))
#define pte offset kernel(dir, address) \
        ((pte t *) pmd page kernel(*(dir)) + pte index(address))
```