

```

1      lgdt boot_gdt_descr - __PAGE_OFFSET
2
3      ...
4      lgdt cpu_gdt_descr
5      ...
6
7  boot_gdt_descr:
8      .word __BOOT_DS+7                # limit (31, end of DS)
9      .long boot_gdt_table - __PAGE_OFFSET # base location
10
11  ENTRY(boot_gdt_table)
12      .fill GDT_ENTRY_BOOT_CS,8,0
13      .quad 0x00cf9a000000ffff # kernel 4GB code at 0x00000000
14      .quad 0x00cf92000000ffff # kernel 4GB data at 0x00000000
15
16  cpu_gdt_descr:
17      .word GDT_ENTRIES*8-1
18      .long cpu_gdt_table
19
20      .fill NR_CPUS-1,8,0            # space for the other GDT descriptors
21
22  ENTRY(cpu_gdt_table)
23      ...
24      # Entry 12-15
25      .quad 0x00cf9a000000ffff # 0x60 kernel 4GB code at 0x00000000
26      .quad 0x00cf92000000ffff # 0x68 kernel 4GB data at 0x00000000
27      .quad 0x00cffa000000ffff # 0x73 user 4GB code at 0x00000000
28      .quad 0x00cff2000000ffff # 0x7b user 4GB data at 0x00000000
29      ...

```