```
    lgdt boot_gdt_descr - __PAGE_OFFSET

    ...
    lgdt cpu_gdt_descr
    ...

boot_gdt_descr:
    .word __BOOT_DS+7                    # limit (31, end of DS)
    .long boot_gdt_table - __PAGE_OFFSET # base location

ENTRY(boot_gdt_table)
    .fill GDT_ENTRY_BOOT_CS,8,0
    .quad 0x00cf9a000000ffff  # kernel 4GB code at 0x00000000
    .quad 0x00cf92000000ffff  # kernel 4GB data at 0x00000000

cpu_gdt_descr:
    .word GDT_ENTRIES*8-1
    .long cpu_gdt_table

    .fill NR_CPUS-1,8,0       # space for the other GDT descriptors

ENTRY(cpu_gdt_table)
    ...
    # Entry 12-15
    .quad 0x00cf9a000000ffff  # 0x60 kernel 4GB code at 0x00000000
    .quad 0x00cf92000000ffff  # 0x68 kernel 4GB data at 0x00000000
    .quad 0x00cffa000000ffff  # 0x73 user 4GB code at 0x00000000
    .quad 0x00cff2000000ffff  # 0x7b user 4GB data at 0x00000000
    ...
```