

```
/*  
 * The Linux x86 paging architecture is 'compile-time dual-mode', it  
 * implements both the traditional 2-level x86 page tables and the  
 * newer 3-level PAE-mode page tables.  
 */
```

```
#ifdef CONFIG_X86_PAE  
# include <asm/pgtable-3level_types.h>  
# define PMD_SIZE          (1UL << PMD_SHIFT)  
# define PMD_MASK          (~ (PMD_SIZE - 1))  
#else  
# include <asm/pgtable-2level_types.h>  
#endif
```

```
#define PGDIR_SIZE          (1UL << PGDIR_SHIFT)  
#define PGDIR_MASK          (~ (PGDIR_SIZE - 1))
```