Memory Protection
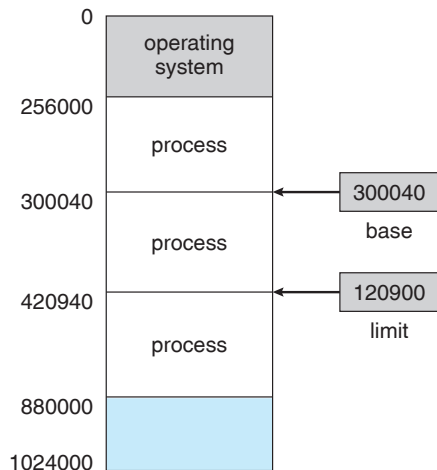Logical Address Space

Base register holds the smallest legal physical memory address
Limit register contains the size of the range

0	
	operating system
256000	
	process
300040	
	process
420940	
	process
880000	
1024000	

300040

base

120900

limit

A pair of `base` and `limit` registers define the logical address space

JMP 28

JMP 300068