

Quiz Week 4: Pipelining Problems

1. How many clock cycles to execute the following 5 instructions With Forwarding

Clock:	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
add x1 _w x2 _r x0 _r	IF	DEC	ALU	DM fw	WB										
sub x1 _w x2 _r x1 _r		IF	DEC	ALU	DM fw	WB									
lw x3 _w 0(x1 _r)			IF	DEC	ALU	DM	WB fw								
sw x4 _r 0(x3 _r)				IF	DEC	DEC stall	ALU	DM	WB						
add x1 _w x2 _r x2 _r					IF	IF	DEC	ALU	DM	WB					

1) The 5 instructions will take 10 clock cycles

2 How many clock cycles to execute the following instructions no forwarding

Clock:	1	2	3	4	5	6	7	8
beq x1 _r x2 _r label	IF	DEC	ALU	DM				
add x1 _w x2 _r x3 _r		IF	DEC	ALU	Flush	Flush		
sub x1 _w x2 _r x4 _r			IF	DEC	Flush	Flush		
add x5 _w x6 _r x7 _r								
label: exit				IF	DEC	ALU	DM	WB

2) The 5 instructions will take 8 clock cycles

3.a How many clock cycles to execute the following 6 instructions With Forwarding

Clock:	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
addi x1 _w x0 _r 3	IF	DEC	ALU	DM fw	WB fw										
addi x2 _w x1 _r 2		IF	DEC	ALU	DM	WB									
lw x3 _w 0(x1 _r)			IF	DEC	ALU	DM	WB fw								
add x2 _w x2 _r x3 _r				IF	DEC	DEC stall	ALU	DM	WB						
addi x3 _w x1 _r 1					IF	IF	DEC	ALU	DM	WB					
sw x0 _r 0(x7 _r)							IF	DEC	ALU	DM	WB				

3) The 6 instructions will take 11 clock cycles

3.b How many clock cycles to execute the loop without Forwarding, loop executes 100 times

ASSUMING BRANCH PREDICTOR IS PC+4, i.e. wrong 100 times

Clock:	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17		
label: lw x0 _w 0(x1 _r)	IF	DEC	ALU	DM	WB														
add x3 _w x2 _r x0 _r		IF	DEC stall	DEC stall	DEC	ALU	DM	WB											
sw x3 _r 0(x1 _r)			IF	IF	IF	DEC	DEC	DEC	ALU	DM	WB								
addi x1 _w x1 _r 4						IF	IF	IF	DEC	ALU	DM	WB							
addi x4 _w x4 _r -1									IF	DEC	ALU	DM	WB						
bnez x4 _r label										IF	DEC	DEC	DEC	ALU	DM	WB			
											IF	IF	IF	DEC	ALU	flush	flush		
														IF	DEC	flush	flush		
label:...															IF	DEC	ALU	DM	WB

Each loop executes every 14ccs. Last loop takes extra 2 to finish WB. = 14*100+2

4) Code will execute in 1402 clock cycles.