Wyatt Tack Bernardo Flores EE 329-01 F'24 Group F 2024-Oct-7

## **EE 329 A3**

This code is designed to validate the use of a simple LCD screen, in which the communication timing was critical. The code in the LCD module works through sending a nibble (half of a byte), with an extra RS bit, of data out on the communication lines, while the enable pin is pulsed to signal the LCD controller that data is being sent through. The timing is critical, as the LCD must be given enough time to read and implement the data before more is sent through. This calls for another module, the delay module, which uses one of the STM32L4 internal clocks to set microsecond timers as delays in the program. This exercise helped with timing-critical bus analyzation to ensure a working device.

#### **Link to YouTube Presentation:**

https://youtu.be/KA2iyTTzBKA

#### **Obtained Data:**

Figure A3(a): 3x4 Keypad and 16x2 LCD Wiring

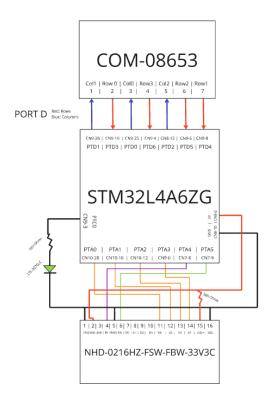
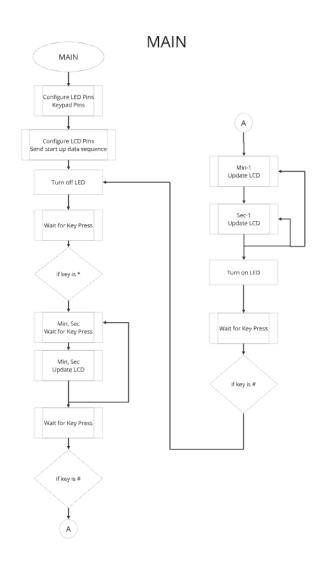


Table A3(a): Calibrated Delay Loop Timing

Entered In Time	Actual Time	Loop Delay Length	<b>Delay Loop Iterations</b>
00:30	00:30.30	930 uS	1000
01:30	1:30.21	930 uS	1000
03:00	2:59.18	930 uS	1000

# **Psuedocode Flow Chart:**



## **Formatted Source Code main.h:**

```
*********
           : main.h
: EE329 Lab A3
: Wyatt Tack (wwt) - wtack@calpoly.edu
: 10/5/2024
: ST-Link V1
 * @file
 * project
 * author
 * date
 * @attention : Copyright (c) 2024 STMicroelectronics. All rights
 main header for defines for C and stm32 headers/hal
 ***********************
#ifndef __MAIN_H
#define __MAIN H
#ifdef cplusplus
extern "C" {
#endif
#define STAR KEY (0x0A)
#define POUND KEY (0x0C)
/* Includes -----*/
#include "stm3214xx hal.h"
/* Exported functions prototypes -----*/
void SystemClock Config(void);
void Error Handler(void);
void Led Config(void);
#ifdef cplusplus
#endif
#endif
```

## **Formatted Source Code main.c:**

```
Timor device, uses Keypad to enter numbers for timor, and on pressing # will count down until timor reached, signaling IED until # is pressed again. 
Keypad defined at CPIGO COS PIGO-2, Now PTO-3- LID defined at CPIGO COS LID defined at CPIGO PTO COS LID defined at CPIGO PTO-0 (But the day of CPIGO PTO-0 
int main(void)
                                                                                            //Initialine clock and delay configs BAL Init();
BAL Init();
SystemClock Config();
SysTick Init();
//Initialine keypad and LCD
Keypad Config();
LcD init();
Led_Config();
while (1)
(
// reset LED and poll keypad until * pressed
currentKeyValue = 0;
                                                                                                                                                                                         LCD_set_cursor(clockMin10);
while(min10 < 0 || min10 > 5) {
    if (currentKeyValue == STAR_KEY) break;
        currentKeyValue = Exp_Roll();
        min10 = currentKeyValue)
                                                                                                                                                                                            )
LCD_write_char(minl0+ 0x30);
                                                                                                                                                                                         )
LCD_write_char(min1+ 0x30);
                                                                                                                                                                                         //wait for # start key
                                                                                                                                                                                           GPIOC->BSRR |= (GPIO PIN 0);
```

# Formatted Source Code main.c (CTD'):

```
in the NCC_OutbiltYppeRf structure.

ACC Destrictives. Destrictives beginning for the specified party.

ACC Destrictives. Destrictives by the COC Destrictives of the Coc Destrictives. Destrictives of the Coc Destrictives. Dest
```

## Formatted Source Code keypad.h:

```
*******************
 * @file : keypad.h
* project : EE329 Lab A3
 * author
           : Wyatt Tack (wwt) - wtack@calpoly.edu
 * date : 9/27/2024
 * firmware : ST-Link V1
 * @attention : Copyright (c) 2024 STMicroelectronics. All rights reserved.
 ********************
    main header for defines for keypad.h
 *************************
#ifndef INC KEYPAD H
#define INC KEYPAD H
#include "stm3214xx hal.h"
//----- KEYPAD Defines ------
#define COL PORT GPIOD
#define COL PINS (GPIO PIN 0 | GPIO PIN 1 | GPIO PIN 2)
#define ROW PORT GPIOD
#define ROW_PINS (GPIO_PIN_3 | GPIO_PIN_4 | GPIO_PIN_5 | GPIO_PIN_6)
#define SETTLE 1900 //defined as time for 20ms loop instruction (*5.5us)
#define BITO COL GPIO PIN 0 //defined as first bit for columns
#define BITO ROW GPIO PIN 3 //defined as first bit for rows
#define NUM COLS 3
#define NUM ROWS 4
#define NO KEYPRESS 0x0
#define KEY ZERO 11
#define CODE ZERO 0xF
//----- function prototypes -----
void Keypad Config(void);
int Keypad IsAnyKeyPressed(void);
int Keypad WhichKeyIsPressed(void);
uint8 t Key Poll(void);
#endif /* INC KEYPAD H */
```

### **Formatted Source Code keypad.c:**

```
* @file
* project
* author
* date
               : keypad.c
: EE329 Lab A3
: Wyatt Tack (wwt) - wtack@calpoly.edu
: 10/7/2024
: ST-Link V1
   Gate
firmware : ST-Link Vl

@attention : Copyright (c) 2024 STMicroelectronics. All rights reserved.
// ----- modified from excerpt from keypad.c ---
void Keypad_Config(void){//must be manually changed if separate GPIO port is used
                   else return( 0 );
                                         // nope.
//
int Keypad WhichKeyIsPressed(void) {
    // detect and encode a pressed key at {row,col}
    // assumes a previous call to Keypad jsAnyKeyPressed() returned TRUE
    // verifies the Keypad JsAnyKeyPressed() result (no debounce here),
    // determines which key is pressed and returns the encoded key ID
   int8_t iRow=0, iCol=0, iKey=0; // keypad row & col index, key ID result
int8 t bGotKey = 0; // bool for keypress, 0 = no press
   )
if ( bGotKey )
break;
                 encode {iRow,iCol} into LED word : row 1-3 : numeric, '1'-'9' row 4 : '*'=10, '0'=15, '\$'=12 no press: send NO_KEYPRESS
     currentKeyValue = (0xF);//error flag
     while(Keypad_IsAnyKeyPressed());
```

## **Formatted Source Code delay.h:**

```
* @file : delay.h

* project : EE329 Lab A3

* author : Wyatt Tack (wwt) - wtack@calpoly.edu

* date : 10/1/2024

* firmware : ST-Link V1

* @attention : Copyright (c) 2024 STMicroelectronics. All rights
reserved.
  ******************
    main header for defines for delay.h
#ifndef INC DELAY H
#define INC DELAY H
#include "stm3214xx hal.h"
void SysTick Init(void);
void delay_us(const uint32_t time_us);
#endif /* INC DELAY H */
```

#### **Formatted Source Code delay.c:**

```
********************
                 : delav.c
 * project
                  : EE329 Lab A3
 * author
                  : Wyatt Tack (wwt) - wtack@calpoly.edu
                  : 10/1/2024
 * date
                 : ST-Link V1
 * firmware
 * @attention
                 : Copyright (c) 2024 STMicroelectronics. All rights
         Functions for using SysTick clock for software delays. Provided
   on behalf of EE329 lab manual.
 ******************
 */
#include "delay.h"
// ----- delay.c w/o #includes ---
// configure SysTick timer for use with delay us().
// warning: breaks HAL delay() by disabling interrupts for shorter delay timing.
void SysTick Init(void) {
    SysTick->CTRL |= (SysTick_CTRL_ENABLE_Msk | // enable SysTick
Timer
                 SysTick CTRL CLKSOURCE Msk); // select CPU clock
    SysTick->CTRL &= ~(SysTick CTRL TICKINT Msk); // disable interrupt
// delay in microseconds using SysTick timer to count CPU clock cycles
// do not call with 0 : error, maximum delay.
// careful calling with small nums : results in longer delays than specified:
// e.g. @4MHz, delay us(\overline{1}) = 10=15 us delay.
void delay_us(const uint32 t time us) {
    // set the counts for the specified delay
    SysTick->LOAD = (uint32 t)((time us * (SystemCoreClock / 1000000)) - 1);
    SysTick->VAL = 0;
                                                // clear timer
count
    while (!(SysTick->CTRL & SysTick CTRL COUNTFLAG Msk)); // wait for flag
}
```

### **Formatted Source Code lcd.h:**

```
*********************
                  : lcd.h
 * project
                  : EE329 Lab A3
                  : Wyatt Tack (wwt) - wtack@calpoly.edu
 * author
                  : 10/1/2024
 * date
             : ST-Link V1 : Copyright (c) 2024 STMicroelectronics. All rights
 * firmware
 * @attention
         *****
   main header for defines for lcd.h
 ********************
#ifndef INC LCD H
#define INC LCD H
#include "delay.h"
#include "stm3214xx hal.h"
#define LCD MODER (0x03FFF)
#define LCD MODER 0 (0x01555)
#define LCD OTYPER (0x07F)
#define LCD PUPDR (0x03FFF)
#define LCD_OSPEEDR (0x03FFF)
#define LCD PORT GPIOA
#define LCD DATA BITS (GPIO PIN 0 | GPIO PIN 1 | GPIO PIN 2 | GPIO PIN 3)
                                //Pins 11-14
                                 //pull down R/W (set as only outputs)
void LCD init( void );
void LCD pulse ENA( void );
void LCD 4b command( uint8 t command );
void LCD command( uint8 t command );
void LCD write char( uint8 t letter );
void LCD set cursor( uint8 t position[2]);
void LCD write string( uint8 t writeData[] );
#endif /* INC LCD H */
```

#### **Formatted Source Code lcd.c:**

```
Functions for interfacing and communicating to LCD display through nibble mode. Provided on behalf of EE329 lab manual. Configured to be wired through GPIO FORT A Pin0-3 as D4-D7; Fin4 as RS; Fin5 as EN
LCD_PORT->OSPEEDR |= (LCD_OSPEEDR); delay_us(80000); //power-up wait 80 ms delay_us(80000); //clear_RS_bit for(int_data = 0, idx < 3; idx++) { // wake up 1,2,3: DATA = 0011 XXXXX command(0x30); // HI 4b of 8b cmd, low nibble = X (5000);
         LCD_4b_command( 0x
delay_us( 5000 );
    . void LCD_pulse_ENA( void ) {
    // ENAble line sends command on falling edge
    // set to restore default then clear to trigger
    LCD PORT->ORR |= (LCD EN );
    delay us( 25 );
    // TDDR > 320 ns
    LCD PORT->ORR |= (LCD_EN );
    // ENABLE = 107
    delay_us( 20 );
    // LOW values flakey, see A3:p.1
void LCD 4b command( uint8 t command ) {
// LCD command using high nibble only - used for 'wake-up' 0x30 commands
LCD PORT->ORE 6 - (LCD DATA BITS); // clear DATA bits
LCD PORT->ORR |= (command >> 4); // DATA = command
delay us (15);
LCD pulse ENA();
 void LCD_command( wint8 t command) (
// send command to LCD in 4-bit instruction mode
// HGHA nibble then LOW nibble, timing sensitive
LCD_PORT->ODR 6-~(LCD_DATA_BITS); // isolate_cmd bits
LCD_PORT->ODR (- ((command>>4) & LCD_DATA_BITS); // HIGH shifted low
delay_us(15); // LCD_DATA_BITS); // latch HIGH NIBBLE
                                              // latch HIGH NIBBLE
     calls LCD_command() w/char data; assumes all ctrl bits set LCD_FORT>->DR |= (LCD_RS); // RS = HI for data to delay us(15); LCD_command(letter); // character to print LCD_FORT->DDR &= -(LCD_RS); // RS = LO
```