

LIST OF CONTRIBUTORS

- Rajeevan Amirtharajah**, Department of Electrical and Computer Engineering, University of California–Davis, Davis, California (Chapter 24)
- Vaughn Betz**, Altera Corporation, San Jose, California (Chapter 14)
- Robert W. Brodersen**, Department of Electrical Engineering and Computer Science, University of California–Berkeley, Berkeley, California (Chapter 8)
- Timothy J. Callahan**, School of Computer Science, Carnegie Mellon University, Pittsburgh, Pennsylvania (Chapter 7)
- Eylon Caspi**, Tabula, Inc., Santa Clara, California (Chapter 9)
- Chen Chang**, Department of Mathematics and Department of Electrical Engineering and Computer Sciences, University of California–Berkeley, Berkeley, California (Chapter 8)
- Mark L. Chang**, Electrical and Computer Engineering, Franklin W. Olin College of Engineering, Needham, Massachusetts (Chapter 1)
- Wang Chen**, Department of Electrical and Computer Engineering, Northeastern University, Boston, Massachusetts (Chapter 32)
- Young H. Cho**, Open Acceleration Systems Research, Chatsworth, California (Chapter 28)
- Michael Chu**, DRC Computer, Sunnyvale, California (Chapter 9)
- Katherine Compton**, Department of Electrical and Computer Engineering, University of Wisconsin–Madison, Madison, Wisconsin (Chapters 4 and 11)
- Jason Cong**, Department of Computer Science, California NanoSystems Institute, University of California–Los Angeles, Los Angeles, California (Chapter 13)
- George A. Constantinides**, Department of Electrical and Electronic Engineering, Imperial College, London, United Kingdom (Chapter 23)
- André DeHon**, Department of Electrical and Systems Engineering, University of Pennsylvania, Philadelphia, Pennsylvania (Chapters 5, 6, 7, 9, 11, 36, 37, and 38)
- Chris Dick**, Advanced Systems Technology Group, DSP Division of Xilinx, Inc., San Jose, California (Chapter 25)
- Carl Ebeling**, Department of Computer Science and Engineering, University of Washington, Seattle, Washington (Chapter 17)
- Ken Eguro**, Department of Electrical Engineering, University of Washington, Seattle, Washington (Chapter 20)
- Diana Franklin**, Computer Science Department, California Polytechnic State University, San Luis Obispo, California (Chapter 35)

- Thomas W. Fry**, Samsung, Global Strategy Group, Seoul, South Korea
(Chapter 27)
- Maya B. Gokhale**, Lawrence Livermore National Laboratory, Livermore,
California (Chapter 10)
- Steven A. Guccione**, Cmpware, Inc., Austin, Texas (Chapters 3 and 19)
- Scott Hauck**, Department of Electrical Engineering, University of Washington,
Seattle, Washington (Chapters 20 and 27)
- K. Scott Hemmert**, Computation, Computers, Information and Mathematics
Center, Sandia National Laboratories, Albuquerque, New Mexico
(Chapter 31)
- Randy Huang**, Tabula, Inc., Santa Clara, California (Chapter 9)
- Brad L. Hutchings**, Department of Electrical and Computer Engineering,
Brigham Young University, Provo, Utah (Chapters 12 and 21)
- Nachiket Kapre**, Department of Computer Science, California Institute of
Technology, Pasadena, California (Chapter 6)
- Andreas Koch**, Department of Computer Science, Embedded Systems and
Applications Group, Technische Universität of Darmstadt, Darmstadt,
Germany (Chapter 15)
- Miriam Leeser**, Department of Electrical and Computer Engineering,
Northeastern University, Boston, Massachusetts (Chapter 32)
- John W. Lockwood**, Department of Computer Science and Engineering,
Washington University in St. Louis, St. Louis, Missouri; and Department
of Electrical Engineering, Stanford University, Stanford, California
(Chapter 34)
- Wayne Luk**, Department of Computing, Imperial College, London,
United Kingdom (Chapter 22)
- Sharad Malik**, Department of Electrical Engineering, Princeton University,
Princeton, New Jersey (Chapter 29)
- Yury Markovskiy**, Department of Electrical Engineering and Computer
Sciences, University of California–Berkeley, Berkeley, California (Chapter 9)
- Margaret Martonosi**, Department of Electrical Engineering, Princeton
University, Princeton, New Jersey (Chapter 29)
- Larry McMurchie**, Synplicity Corporation, Sunnyvale, California (Chapter 17)
- Brent E. Nelson**, Department of Electrical and Computer Engineering,
Brigham Young University, Provo, Utah (Chapters 12 and 21)
- Peichen Pan**, Magma Design Automation, Inc., San Jose, California
(Chapter 13)
- Oliver Pell**, Department of Computing, Imperial College, London, United
Kingdom (Chapter 22)
- Stylianios Perissakis**, Department of Electrical Engineering and Computer
Sciences, University of California–Berkeley, Berkeley, California (Chapter 9)

- Laura Pozzi**, Faculty of Informatics, University of Lugano, Lugano, Switzerland (Chapter 9)
- Brian C. Richards**, Department of Electrical Engineering and Computer Sciences, University of California–Berkeley, Berkeley, California (Chapter 8)
- Eduardo Sanchez**, School of Computer and Communication Sciences, Ecole Polytechnique Fédérale de Lausanne; and Reconfigurable and Embedded Digital Systems Institute, Haute Ecole d'Ingénierie et de Gestion du Canton de Vaud, Lausanne, Switzerland (Chapter 33)
- Lesley Shannon**, School of Engineering Science, Simon Fraser University, Burnaby, BC, Canada (Chapter 2)
- Satnam Singh**, Programming Principles and Tools Group, Microsoft Research, Cambridge, United Kingdom (Chapter 16)
- Greg Stitt**, Department of Computer Science and Engineering, University of California–Riverside, Riverside, California (Chapter 26)
- Russell Tessier**, Department of Computer and Electrical Engineering, University of Massachusetts, Amherst, Massachusetts (Chapter 30)
- Keith D. Underwood**, Computation, Computers, Information and Mathematics Center, Sandia National Laboratories, Albuquerque, New Mexico (Chapter 31)
- Andres Upegui**, Logic Systems Laboratory, School of Computer and Communication Sciences, École Polytechnique Fédérale de Lausanne, Lausanne, Switzerland (Chapter 33)
- Frank Vahid**, Department of Computer Science and Engineering, University of California–Riverside, Riverside, California (Chapter 26)
- John Wawrzynek**, Department of Electrical Engineering and Computer Sciences, University of California–Berkeley, Berkeley, California (Chapters 8 and 9)
- Nicholas Weaver**, International Computer Science Institute, Berkeley, California (Chapter 18)
- Joseph Yeh**, Lincoln Laboratory, Massachusetts Institute of Technology, Lexington, Massachusetts (Chapter 9)
- Peixin Zhong**, Department of Electrical and Computer Engineering, Michigan State University, East Lansing, Michigan (Chapter 29)