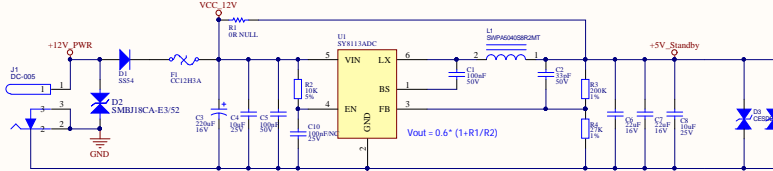


DC 12V Input

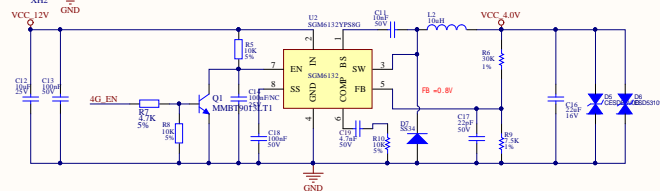
+5V Power



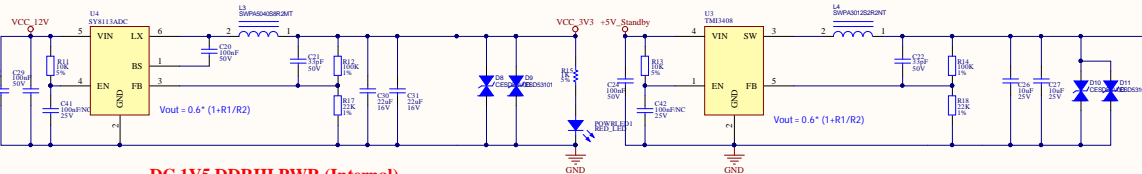
Power

- VDD
- AVDD_DDR
- AVDDIO_DRAM
- AVDDIO_DATA
- AVDDIO_NODIE
- AVDDIO_USB
- AVDDIO_ETH
- AVDDIO_XTAL
- AVDDIO_PLL
- VDDP_1
- VDDP_2
- VDDP_3
- VDDP_4
- VDDP_5
- VDDP_6
- VDDP_7
- VDDP_8
- VDDP_9
- VDDP_10
- VDDP_11
- VDDP_12
- VDDP_13
- VDDP_14
- VDDP_15
- VDDP_16
- VDDP_17
- VDDP_18
- VDDP_19
- VDDP_20
- VDDP_21
- VDDP_22
- VDDP_23
- VDDP_24
- VDDP_25
- VDDP_26
- VDDP_27
- VDDP_28
- VDDP_29
- VDDP_30
- VDDP_31
- VDDP_32
- VDDP_33
- VDDP_34
- VDDP_35
- VDDP_36
- VDDP_37
- VDDP_38
- VDDP_39
- VDDP_40
- VDDP_41
- VDDP_42
- VDDP_43
- VDDP_44
- VDDP_45
- VDDP_46
- VDDP_47
- VDDP_48
- VDDP_49
- VDDP_50
- VDDP_51
- VDDP_52
- VDDP_53
- VDDP_54
- VDDP_55
- VDDP_56
- VDDP_57
- VDDP_58
- VDDP_59
- VDDP_60
- VDDP_61
- VDDP_62
- VDDP_63
- VDDP_64
- VDDP_65
- VDDP_66
- VDDP_67
- VDDP_68
- VDDP_69
- VDDP_70
- VDDP_71
- VDDP_72
- VDDP_73
- VDDP_74
- VDDP_75
- VDDP_76
- VDDP_77
- VDDP_78
- VDDP_79
- VDDP_80
- VDDP_81
- VDDP_82
- VDDP_83
- VDDP_84
- VDDP_85
- VDDP_86
- VDDP_87
- VDDP_88
- VDDP_89
- VDDP_90
- VDDP_91
- VDDP_92
- VDDP_93
- VDDP_94
- VDDP_95
- VDDP_96
- VDDP_97
- VDDP_98
- VDDP_99
- VDDP_100

DC 4V0 PWR

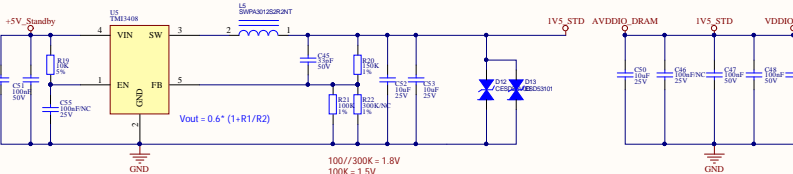


DC 3V3 PWR

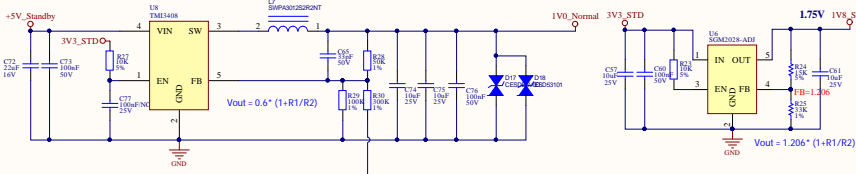


DC 1V5 DDRIII PWR (Internal)

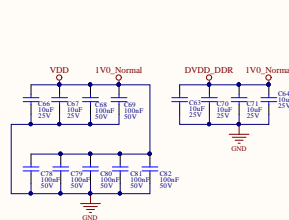
DC 1V8 DDRII PWR (Internal)



DC 1V0 Core Power

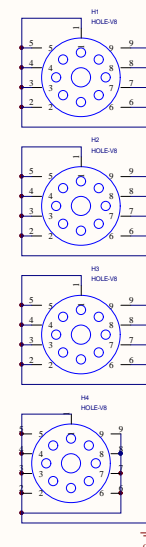


LDO 1V8 MIPI Panel

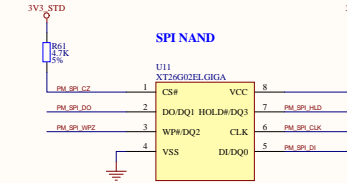
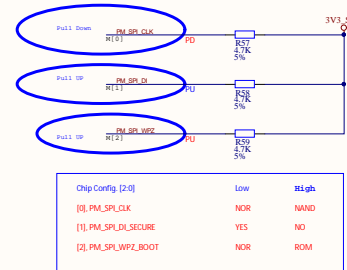
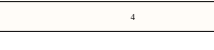
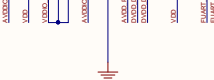
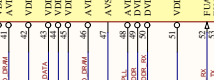
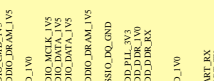
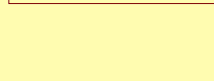
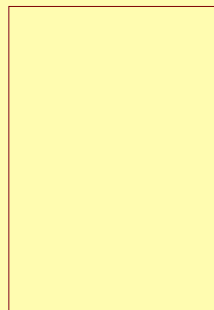
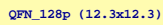
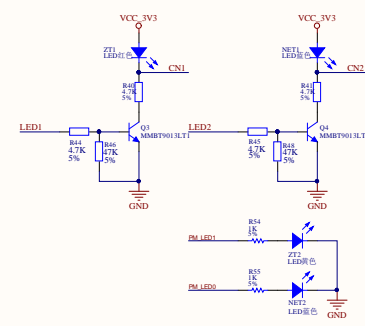
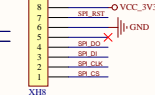
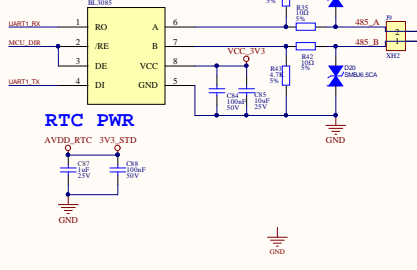
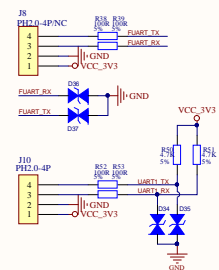
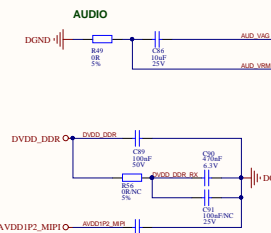
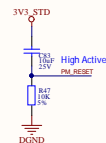


LCD_BL_PWM

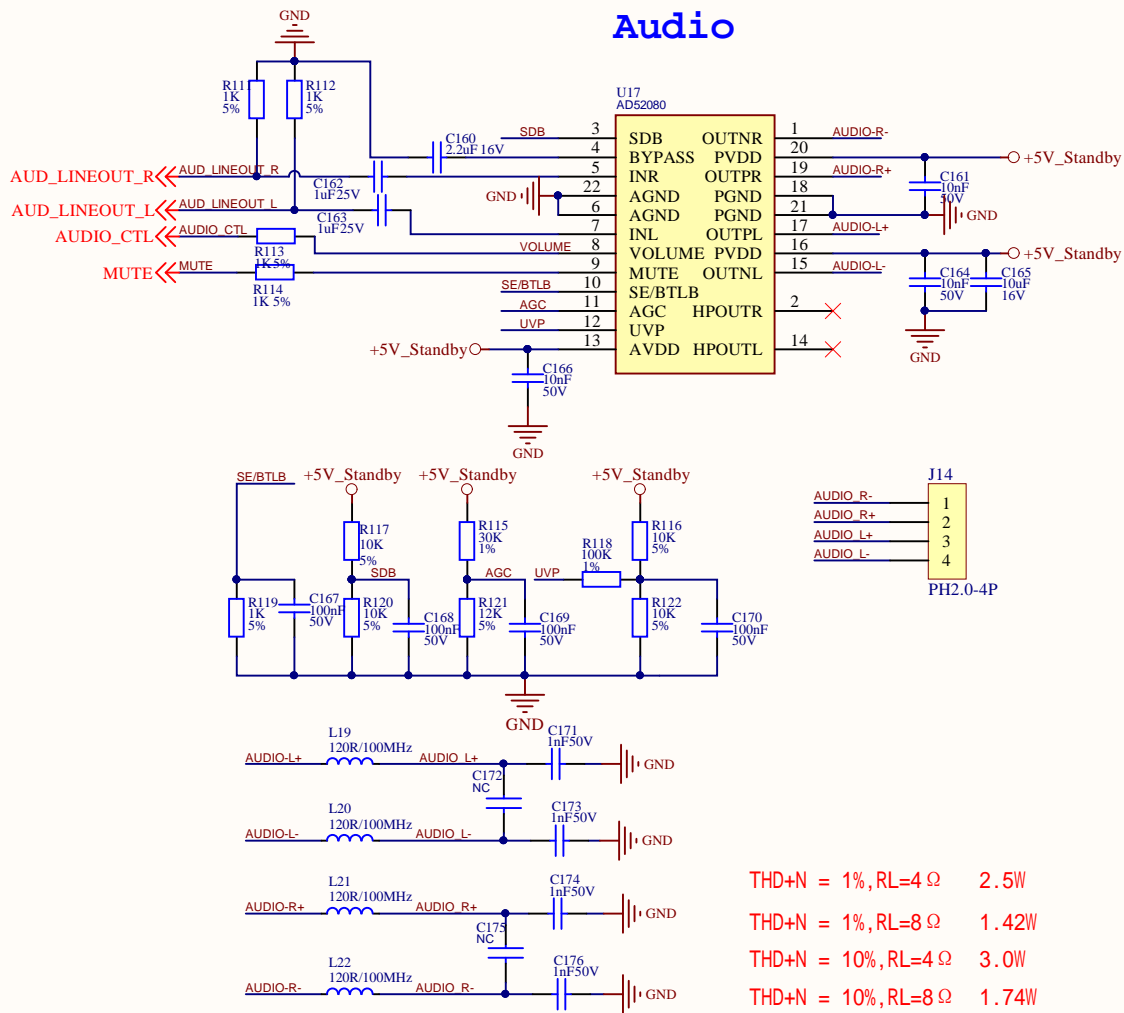
AG_EN



上海蜜连科技有限公司	
项目名称:	BN802TY_V1.0
文件名:	002Power
版本号:	V1.0
编辑者:	Angel
时间:	2021/03/03



项目名称:	BN802TY_V1.0
文件名:	003.SSD202D
版本号:	V1.0
编辑者:	Angel
时间:	2021/03/03



上海蜜连科技有限公司

项目名称:	BN802TY_V1.0
文件名:	005.Audio
版本号:	V1.0
编辑者:	Angel
时间:	2021/03/03