## **Dual General Purpose Transistors**

## **NPN Duals**

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-363/SC-88 which is designed for low power surface mount applications.

#### **Features**

- S and NSV Prefixes for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant\*

### **MAXIMUM RATINGS**

Rating	Symbol	BC846	BC847	BC848	Unit
Collector - Emitter Voltage	V <sub>CEO</sub>	65	45	30	V
Collector - Base Voltage	V <sub>CBO</sub>	80	50	30	V
Emitter - Base Voltage	V <sub>EBO</sub>	6.0	6.0	5.0	V
Collector Current – Continuous	I <sub>C</sub>	100	100	100	mAdc

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation Per Device FR-5 Board (Note 1) T <sub>A</sub> = 25°C Derate Above 25°C	P <sub>D</sub>	380 250 3.0	mW mW/°C mW/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	328	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

<sup>1.</sup>  $FR-5 = 1.0 \times 0.75 \times 0.062$  in

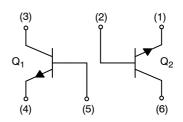


## ON Semiconductor®

http://onsemi.com



SOT-363 CASE 419B STYLE 1



### **MARKING DIAGRAM**



1x = Specific Device Code

x = B, F, G, LM = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = 10 mA) BC846, SBC846 Series BC847, SBC847 Series, NSVBC847 BC848 Series	V <sub>(BR)</sub> CEO	65 45 30	- - -	- - -	V
Collector – Emitter Breakdown Voltage ( $I_C = 10 \mu A$ , $V_{EB} = 0$ ) BC846, SBC846 Series BC847, SBC847 Series, NSVBC847 BC848 Series	V <sub>(BR)CES</sub>	80 50 30	- - -	- - -	V
Collector – Base Breakdown Voltage (I <sub>C</sub> = 10 μA) BC846, SBC846 Series BC847, SBC847 Series, NSVBC847 BC848 Series	V <sub>(BR)CBO</sub>	80 50 30	- - -	- - -	V
Emitter – Base Breakdown Voltage (I <sub>E</sub> = 1.0 μA) BC846, SBC846 Series BC847, SBC847 Series, NSVBC847 BC848 Series	V <sub>(BR)EBO</sub>	6.0 6.0 5.0	- - -	- - -	V
Collector Cutoff Current (V <sub>CB</sub> = 30 V) (V <sub>CB</sub> = 30 V, T <sub>A</sub> = 150°C)	I <sub>CBO</sub>	- -	- -	15 5.0	nA μA
ON CHARACTERISTICS					
DC Current Gain $ \begin{array}{l} (I_C=10~\mu\text{A},~V_{CE}=5.0~\text{V})\\ BC846B,~SBC846B,~BC847B,~SBC847B,~NSVBC847\\ BC847C,~SBC847C,~BC848C\\ (I_C=2.0~\text{mA},~V_{CE}=5.0~\text{V})\\ BC846B,~SBC846B,~BC847B,~SBC847B,~NSVBC847\\ BC847C,~SBC847C,~BC848C\\ \end{array} $	h <sub>FE</sub>	- - 200 420	150 270 290 520	- - 450 800	-
Collector – Emitter Saturation Voltage ( $I_C = 10 \text{ mA}$ , $I_B = 0.5 \text{ mA}$ ) ( $I_C = 100 \text{ mA}$ , $I_B = 5.0 \text{ mA}$ )	V <sub>CE(sat)</sub>	- -	_ _ _	0.25 0.6	V
Base – Emitter Saturation Voltage ( $I_C$ = 10 mA, $I_B$ = 0.5 mA) ( $I_C$ = 100 mA, $I_B$ = 5.0 mA)	V <sub>BE(sat)</sub>	- -	0.7 0.9	- -	V
Base – Emitter Voltage ( $I_C$ = 2.0 mA, $V_{CE}$ = 5.0 V) ( $I_C$ = 10 mA, $V_{CE}$ = 5.0 V)	V <sub>BE(on)</sub>	580 -	660 -	700 770	mV
SMALL-SIGNAL CHARACTERISTICS					
Current - Gain - Bandwidth Product (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 5.0 Vdc, f = 100 MHz)	f <sub>T</sub>	100	_	_	MHz
Output Capacitance (V <sub>CB</sub> = 10 V, f = 1.0 MHz)	C <sub>obo</sub>	-	-	4.5	pF
Noise Figure (I <sub>C</sub> = 0.2 mA, V <sub>CE</sub> = 5.0 Vdc, R <sub>S</sub> = 2.0 k $\Omega$ ,f = 1.0 kHz, BW = 200 Hz)	NF	-	-	10	dB

## TYPICAL CHARACTERISTICS - BC846BDW1T1G, SBC846BDW1T1G

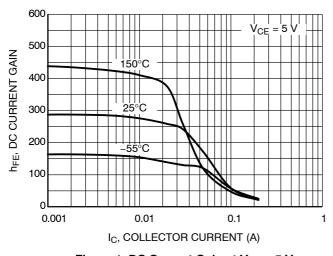


Figure 1. DC Current Gain at V<sub>CE</sub> = 5 V

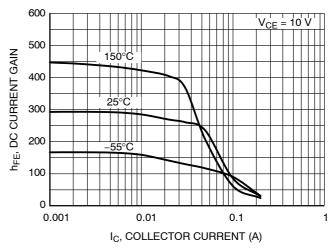


Figure 2. DC Current Gain at V<sub>CE</sub> = 10 V

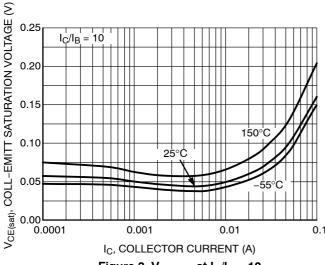


Figure 3.  $V_{CE(sat)}$  at  $I_C/I_B = 10$ 

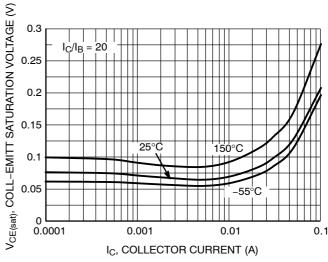
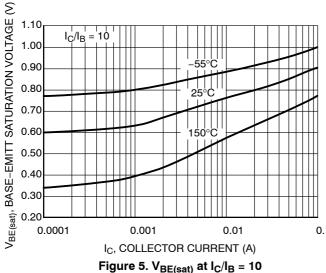


Figure 4.  $V_{CE(sat)}$  at  $I_C/I_B = 20$ 



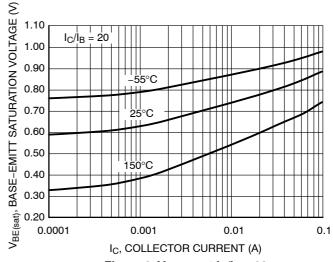


Figure 6.  $V_{BE(sat)}$  at  $I_C/I_B = 20$ 

## TYPICAL CHARACTERISTICS - BC846BDW1T1G, SBC846BDW1T1G

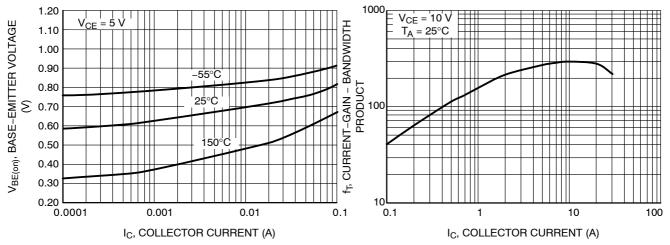


Figure 7. V<sub>BE(on)</sub> at V<sub>CE</sub> = 5 V

Figure 8. Current - Gain - Bandwidth Product

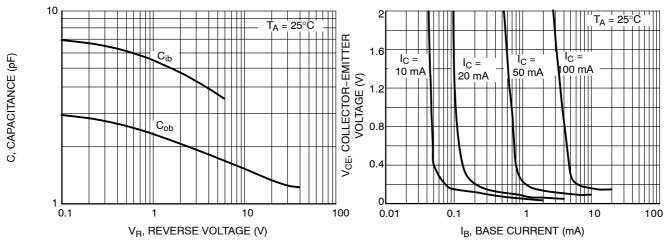


Figure 9. Capacitances

Figure 10. Collector Saturation Region

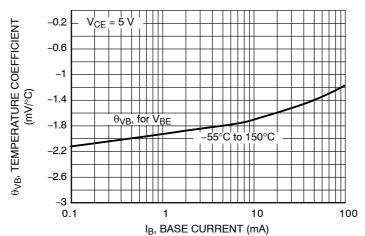
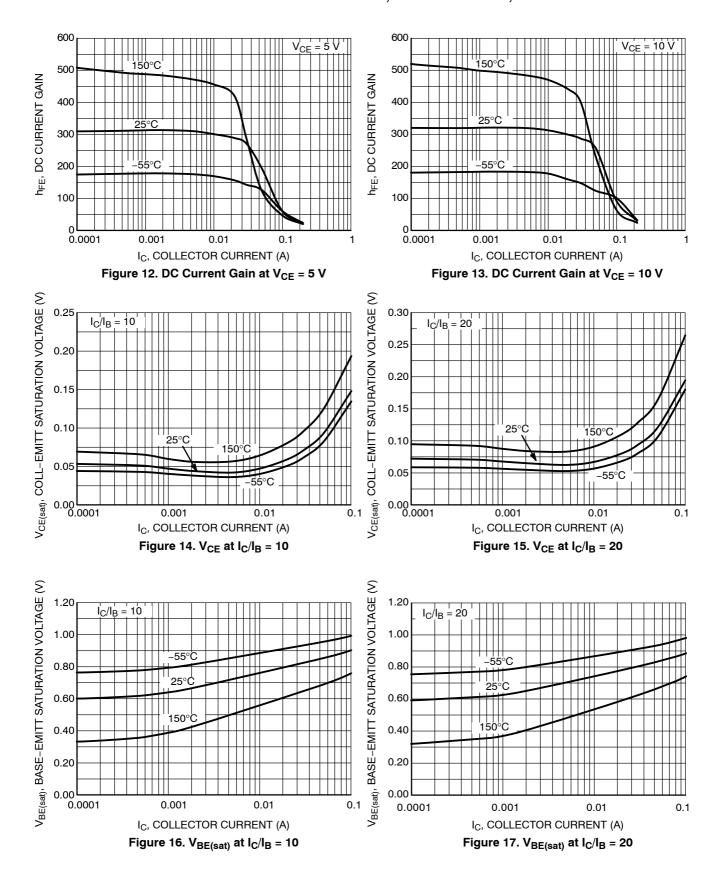
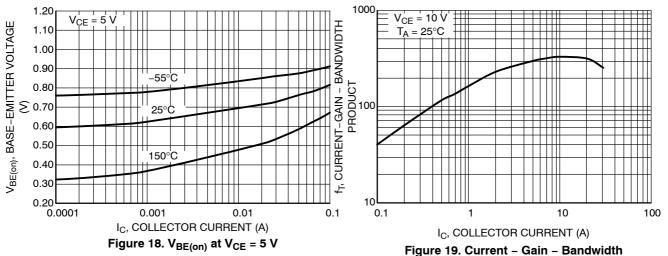


Figure 11. Base-Emitter Temperature Coefficient

## TYPICAL CHARACTERISTICS - BC847BDW1T1G, SBC847BDW1T1G, NSVBC847BDW1T2G



TYPICAL CHARACTERISTICS - BC847BDW1T1G, SBC847BDW1T1G, NSVBC847BDW1T2G



**Product** 

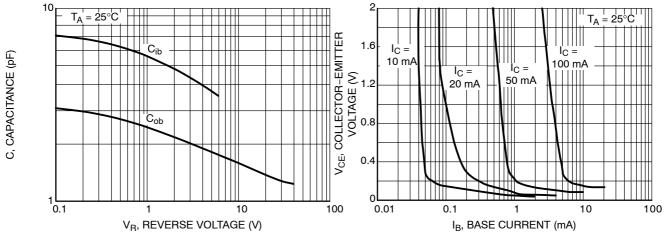


Figure 20. Capacitances Figure 21. Collector Saturation Region

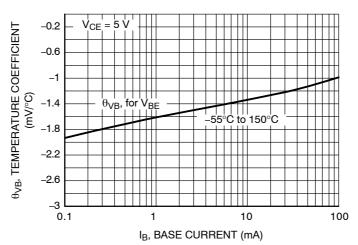


Figure 22. Base-Emitter Temperature Coefficient

## TYPICAL CHARACTERISTICS - BC848CDW1T1G, SBC848CDW1T1G

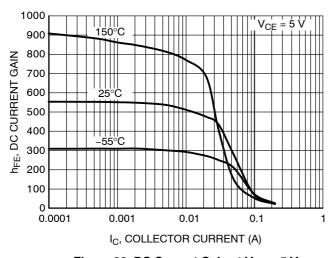


Figure 23. DC Current Gain at  $V_{CE} = 5 \text{ V}$ 

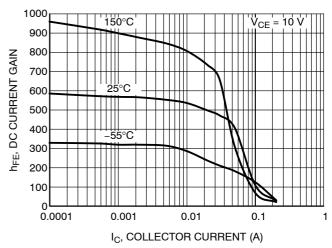
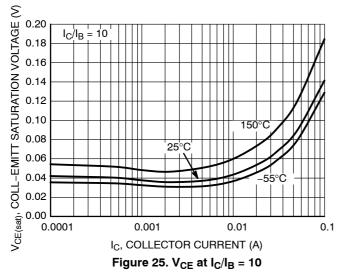


Figure 24. DC Current Gain at V<sub>CE</sub> = 10 V



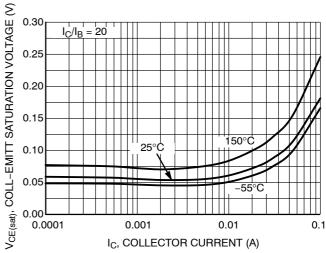


Figure 26.  $V_{CE}$  at  $I_C/I_B = 20$ 

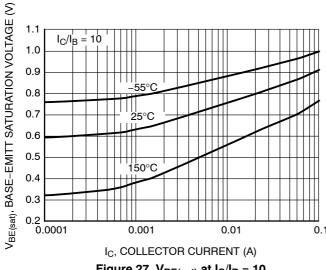


Figure 27.  $V_{BE(sat)}$  at  $I_C/I_B = 10$ 

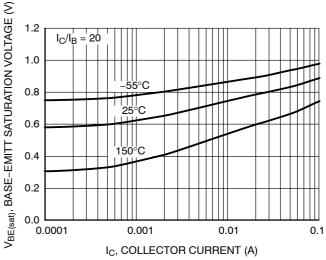


Figure 28.  $V_{BE(sat)}$  at  $I_C/I_B = 20$ 

## TYPICAL CHARACTERISTICS - BC848CDW1T1G, SBC848CDW1T1G

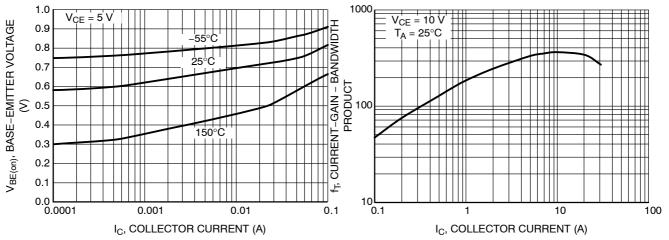


Figure 29.  $V_{BE(on)}$  at  $V_{CE} = 5 \text{ V}$ 

Figure 30. Current – Gain – Bandwidth Product

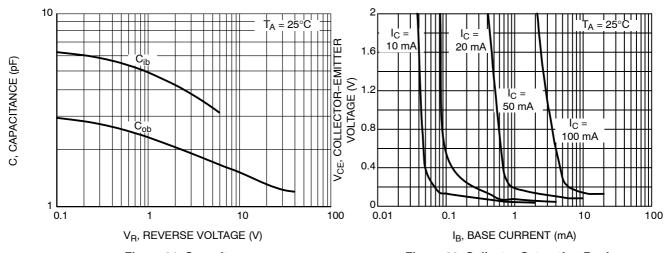


Figure 31. Capacitances

Figure 32. Collector Saturation Region

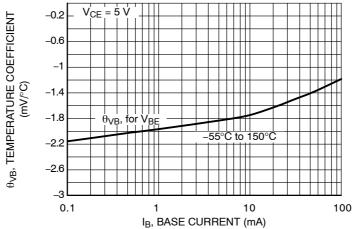


Figure 33. Base-Emitter Temperature Coefficient

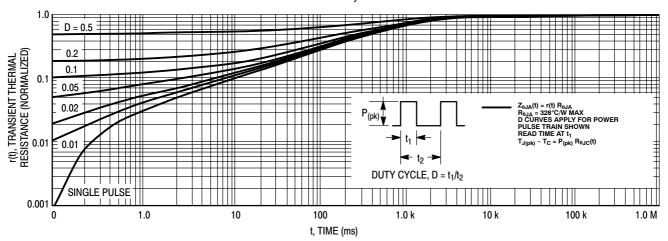


Figure 34. Thermal Response

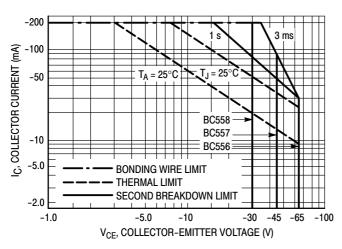


Figure 35. Active Region Safe Operating Area

The safe operating area curves indicate  $I_C$ – $V_{CE}$  limits of the transistor that must be observed for reliable operation. Collector load lines for specific circuits must fall below the limits indicated by the applicable curve.

The data of Figure 35 is based upon  $T_{J(pk)} = 150^{\circ}C$ ;  $T_{C}$  or  $T_{A}$  is variable depending upon conditions. Pulse curves are valid for duty cycles to 10% provided  $T_{J(pk)} \leq 150^{\circ}C$ .  $T_{J(pk)}$  may be calculated from the data in Figure 34. At high case or ambient temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by the secondary breakdown.

### **ORDERING INFORMATION**

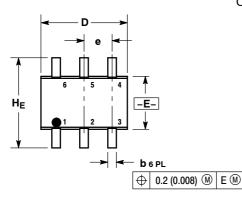
Device	Markings	Package	Shipping <sup>†</sup>	
BC846BDW1T1G	1B	SOT-363 (Pb-Free)	3,000 / Tape & Reel	
SBC846BDW1T1G*	1B	SOT-363 (Pb-Free)	3,000 / Tape & Reel	
BC847BDW1T1G	1F	SOT-363 (Pb-Free)	3,000 / Tape & Reel	
SBC847BDW1T1G*	1F	SOT-363 (Pb-Free)	3,000 / Tape & Reel	
BC847BDW1T3G	1F	SOT-363 (Pb-Free)	10,000 / Tape & Reel	
SBC847BDW1T3G*	1F	SOT-363 (Pb-Free)	10,000 / Tape & Reel	
NSVBC847BDW1T2G*	1F	SOT-363 (Pb-Free)	10,000 / Tape & Reel	
BC847CDW1T1G	1G	SOT-363 (Pb-Free)	3,000 / Tape & Reel	
SBC847CDW1T1G*	1G	SOT-363 (Pb-Free)	3,000 / Tape & Reel	
BC848CDW1T1G	1L	SOT-363 (Pb-Free)	3,000 / Tape & Reel	

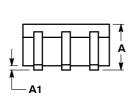
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

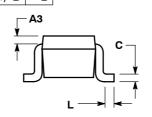
<sup>\*</sup>S and NSV Prefixes for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

#### PACKAGE DIMENSIONS

### SC-88 (SC70-6/SOT-363) CASE 419B-02 ISSUF W







#### NOTES:

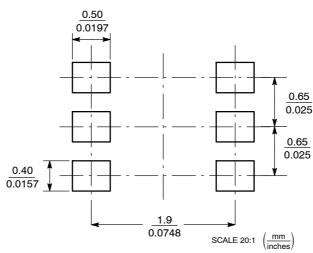
- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.95	1.10	0.031	0.037	0.043
A1	0.00	0.05	0.10	0.000	0.002	0.004
А3	0.20 REF			0.008 REF		
q	0.10	0.21	0.30	0.004	0.008	0.012
C	0.10	0.14	0.25	0.004	0.005	0.010
D	1.80	2.00	2.20	0.070	0.078	0.086
Е	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65 BSC			0.026 BSC		
Г	0.10	0.20	0.30	0.004	0.008	0.012
HE	2.00	2.10	2.20	0.078	0.082	0.086

STYLE 1: PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1

- 4. EMITTER 1
- 5 BASE 1
- 6. COLLECTOR 2

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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