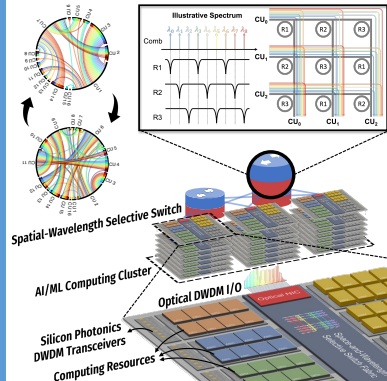
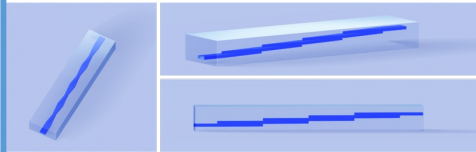


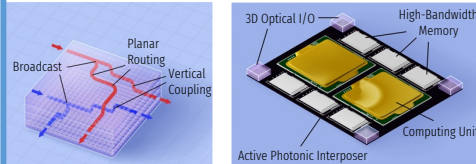
## (T1) Traffic-Adaptable Optical Interconnects



### ↓ (T2a) 3D Routing Concept w/ Low-Loss Adiabatic Tapers



### ↓ (T2b) 3D I/O Concept w/ Switching and Broadcasting



### (T2c) Extended Optical Connectivity for CU Clusters $\uparrow$

The left diagram illustrates a 3D IC architecture with multiple layers. It shows planar routing on each layer and vertical coupling between layers. A broadcast signal is also indicated. The right diagram illustrates a 3D IC architecture using an active photonic interposer. It shows 3D optical I/O, high-bandwidth memory, and computing units.

### (T2c) Extended Optical Connectivity for CU Clusters ↑