WEIZHE **GAN**

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EDUCATION

2014 SEP – 2018 JUN

BACHELOR, ZHEJIANG UNIVERSITY

Major: Electronic Engineering
GPA: 3.60/4 (major 3.85/4)
Thesis: *FPGA-based Digital Integrated Experimental Platform Design*

WORKING EXPERIENCE

2018 JUL – PRESENT

SOFTWARE ENGINEER (C++), Supcon Technology Co.

- Developed double-platform (Linux/Windows) I/O driver modules based on C++ and boost library, which could collect and parse data from remote devices.
- Developed test tools with visual interface based on QT, which enable the data collecting process to be observed.
- Tools: Clion, VS2010, QtCreator

PROJECT EXPERIENCE

2017 OCT – 2018 JAN

VOICE CHANGER: A DSP-BASED REAL-TIME AUDIO SIGNAL PROCESSOR

- Designed and implemented a Digital Signal Processing System that collects, stores, converts, and replays voice signals.
- Worked on CCS (Code Composer Studio) IDE with C language for the DSP chip programming and installation.

2017 JUL – 2017 AUG

SOKOBAN: A FPGA-BASED TRANSPORT PUZZLE GAME

- Worked as a team to implemented a 4 Levels 8X8 Sokoban game on FPGA.
- Programmed in Verilog for game control and all SVGA (Super Video Graphic Array) signal processing and output.

2016 MAY – 2017 MAY

SMART LAUNDRY: AUTO RETRACTABLE RACK ON INTEL MCS-51

- Designed a SMART retractable rack system, which could be controlled by: 1)SMS message, 2) Timer, or 3) Rain Sensor.
- Responsible for designing the hardware circuits and implementing on Printed Circuit Board.
- Programmed in C for main Control Logic and Communication among multi peripheral chips.

SKILLS

- Programming Languages: C, C++, Verilog, MATLAB
- Language: English (TOEFL, Proficiency Fluent)
- Operating System: Windows, Linux
- Knowledge: Data Structure, Algorithm