vector_irq

in arch/arm/kernel/entry-armv.S

1. vector table entry

```
2.
      * Interrupt dispatcher
 3.
 4.
             vector_stub irq, IRQ_MODE, 4
 5.
 6.
             .long __irq_usr
                                                   @ 0 (USR_26 / USR_32)
                   __irq_invalid
             .long
                                                   @ 1 (FIQ_26 / FIQ_32)
 8.
             .long __irq_invalid
                                                   @ 2 (IRQ_26 / IRQ_32)
9.
             .long __irq_svc
                                                   @ 3 (SVC_26 / SVC_32)
10.
             .long __irq_invalid
                                                   @ 4
                   __irq_invalid
11.
             .long
                                                   @ 5
                                                   @ 6
12.
             .long __irq_invalid
13.
             .long __irq_invalid
                                                   @ 7
                   __irq_invalid
14.
             .long
                                                   @ 8
15.
             .long __irq_invalid
                                                   @ 9
16.
             .long __irq_invalid
                                                   @ a
             .long __irq_invalid
17.
                                                   @ b
                   __irq_invalid
18.
             .long
                                                   @ c
19.
             .long __irq_invalid
                                                   @ d
20.
                   __irq_invalid
             .long
                                                   @ e
                                                   @ f
21.
                     __irq_invalid
             .long
```

```
__irq_usr() --> Application运行时发生interrupt
```

__irq_svc() --> kernel运行时发生interrupt

2. __irq_usr()

```
.align 5
      __irq_usr:
3.
              usr_entry
4.
              kuser_cmpxchg_check
5.
              irq_handler
6.
              get_thread_info tsk
              mov why, #0
8.
                      ret_to_user_from_irq
9.
       UNWIND(.fnend
10.
      ENDPROC(__irq_usr)
```

3. irq_handler macro

```
* Interrupt handling.
         */
 4.
                   .macro irq_handler
       #ifdef CONFIG_MULTI_IRQ_HANDLER
 6.
                   ldr r1, =handle_arch_irq
                   \begin{array}{lll} \text{mov} & & \text{r0, sp} \\ \text{adr} & & \text{lr, BSYM}(9997\text{f}) \end{array}
 8.
                   ldr pc, [r1]
 9.
10.
       #else
11.
                  arch_irq_handler_default
12.
        #endif
13.
        9997:
14.
                   .endm
```

4. 在G2 LSP中CONFIG_MULTI_IRQ_HANDLER=y

所以就是运行如下code

```
    ldr r1, =handle_arch_irq
    mov r0, sp
    adr lr, BSYM(9997f)
    ldr pc, [r1]
```

5. call handle_arch_irq()

in arch/arm/kernel/entry-armv.S

```
#ifdef CONFIG_MULTI_IRQ_HANDLER
globl handle_arch_irq
handle_arch_irq:
space 4
#endif
```

在static vmlinux中handle_arch_irq指示function pointer, 而且还未初始化。还function pointer一般在各个interrupt controller driver中被初始化。即最终直接跳转到与特定 interrupt controller相关的handler中。

2.1 __irq_svc()

在处理interrupt上,与__irq_usr()几乎相同。区别是在从hardware interrupt handler返回后的不同处理。

```
_irq_svc:
2.
            svc_entry
3.
            irq_handler
4.
5.
    #ifdef CONFIG_PREEMPT
6.
            get_thread_info tsk
             ldr r8, [tsk, #TI_PREEMPT] @ get preempt count
8.
             ldr
                   r0, [tsk, #TI_FLAGS]
                                                @ get flags
9.
                                                 @ if preempt count != 0
             teq r8, #0
10.
             movne r0, #0
                                                 @ force flags to 0
11.
             tst r0, #_TIF_NEED_RESCHED
            blne svc_preempt
12.
13.
     #endif
14.
15.
             svc_exit r5, irq = 1
                                                 @ return from exception
      UNWIND(.fnend
16.
17.
     ENDPROC(__irq_svc)
```