in arch/arm/include/asm/global_data.h

```
#ifdef CONFIG_ARM64
#define DECLARE_GLOBAL_DATA_PTR register volatile gd_t *gd asm ("x18")
#else
#define DECLARE_GLOBAL_DATA_PTR register volatile gd_t *gd asm ("r9")
#endif
#endif
```

在ARM(32)中, u-boot运行期间的的r9 register不做他用, 总是指向struct global_data。

如果在u-boot任何.c文件中要访问该structure,只要添加如下statment即可。

```
1. DECLARE_GLOBAL_DATA_PTR;
```

in include/asm-generic/global_data.h

```
1.
     typedef struct global_data {
2.
            bd_t *bd;
3.
            unsigned long flags;
4.
            unsigned int baudrate;
5.
            unsigned long cpu_clk; /* CPU clock in Hz!
                                                              */
6.
            unsigned long bus clk;
7.
            /* We cannot bracket this with CONFIG PCI due to mpc5xxx */
8.
            unsigned long pci_clk;
9.
            unsigned long mem_clk;
    #if defined(CONFIG_LCD) || defined(CONFIG_VIDEO) || 1
10.
11.
            unsigned long fb_base; /* Base address of framebuffer mem */
12.
     #endif
13.
     #if defined(CONFIG_POST) || defined(CONFIG_LOGBUFFER)
            unsigned long post_log_word; /* Record POST activities */
14.
15.
            unsigned long post_log_res; /* success of POST test */
16.
            unsigned long post_init_f_time; /* When post_init_f started */
17.
     #endif
18.
    #ifdef CONFIG_BOARD_TYPES
19.
            unsigned long board_type;
20.
     #endif
21.
            unsigned long have_console;  /* serial_init() was called */
22.
    #ifdef CONFIG_PRE_CONSOLE_BUFFER
23.
            unsigned long precon_buf_idx; /* Pre-Console buffer index */
24.
     #endif
25.
     #ifdef CONFIG_MODEM_SUPPORT
26.
            unsigned long do_mdm_init;
27.
            unsigned long be_quiet;
28.
     #endif
29.
            unsigned long env_addr; /* Address of Environment struct */
30.
            unsigned long env_valid; /* Checksum of Environment valid? */
31.
32.
            unsigned long ram_top; /* Top address of RAM used by U-Boot */
33.
34.
            35.
            phys_size_t ram_size; /* RAM size */
36.
            unsigned long mon_len; /* monitor len */
37.
            38.
            unsigned long start_addr_sp; /* start_addr_stackpointer */
39.
            unsigned long reloc_off;
40.
            struct global_data *new_gd; /* relocated global data */
41.
            const void *fdt_blob; /* Our device tree, NULL if none */
42.
            void *new fdt; /* Relocated FDT */
43.
            unsigned long fdt_size; /* Space reserved for relocated FDT */
                                 /* jump table */
44.
            void **jt;
            45.
46.
    #ifdef CONFIG_TRACE
47.
            void
                          *trace_buff; /* The trace buffer */
48.
     #endif
49.
     #if defined(CONFIG_SYS_I2C)
50.
            int
                           cur i2c bus; /* current used i2c bus */
51.
     #endif
52.
            unsigned long timebase_h;
53.
            unsigned long timebase_1;
```

```
54. struct arch_global_data arch; /* architecture-specific data */
55. } gd_t;
```

该global structure的初始分配(relocate code以前)是在arch/arm/lib/crt0.S中

```
#if defined(CONFIG_SPL_BUILD) && defined(CONFIG_SPL_STACK)
 2.
              ldr sp, =(CONFIG_SPL_STACK)
      #else
              ldr
                   sp, =(CONFIG_SYS_INIT_SP_ADDR)
5.
      #endif
6.
              bic
                       sp, sp, #7 /* 8-byte alignment for ABI compliance */
                      sp, sp, #GD_SIZE /* allocate one GD above SP */
              sub
                       sp, sp, #7  /* 8-byte alignment for ABI compliance */
r9, sp  /* GD is above SP */
              bic
9.
                       r9, sp
              mov
10.
              mov
                       r0, #0
11.
              bl
                       board_init_f
```

1

这里sp指向0x08000000 - sizof(gd_t) (取整)

```
#ifndef CONFIG_SYS_TEXT_BASE
#define CONFIG_SYS_TEXT_BASE 0x08000000 /* (CONFIG_SYS_BASE_ADDR + CONFIG_SYS_INI
    T_SP_SIZE + CONFIG_SYS_MALLOC_LEN) */
#endif

#define CONFIG_SYS_BASE_ADDR (CONFIG_SYS_TEXT_BASE - 0x800)
#define CONFIG_SYS_INIT_SP_ADDR (CONFIG_SYS_BASE_ADDR + CONFIG_SYS_INIT_SP_SIZE -
    GENERATED_GBL_DATA_SIZE)
```

2

r9 = 0x08000000 - sizof(gd t) - sizof(gd t)

我的感觉CONFIG_SYS_INIT_SP_ADDR 无需再减去GENERATED_GBL_DATA_SIZE。这里多减去GENERATED_GBL_DATA_SIZE也不会错,但好像没有必要。

对gd_t structure中很多成员的初始化,在board_init_f() function中 (in arch/arm/lib/board.c)