

1. vector_pabt in entry-armv.S

2. vector_stub pabt in entry-armv.S

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1.  /*
2.   * Prefetch abort dispatcher
3.   * Enter in ABT mode, spsr = USR CPSR, lr = USR PC
4.   */
5.   vector_stub    pabt, ABT_MODE, 4
6.
7.   .long __pabt_usr@ 0 (USR_26 / USR_32)
8.   .long __pabt_invalid @ 1 (FIQ_26 / FIQ_32)
9.   .long __pabt_invalid @ 2 (IRQ_26 / IRQ_32)
10.  .long __pabt_svc@ 3 (SVC_26 / SVC_32)
11.  .long __pabt_invalid @ 4
12.  .long __pabt_invalid @ 5
13.  .long __pabt_invalid @ 6
14.  .long __pabt_invalid @ 7
15.  .long __pabt_invalid @ 8
16.  .long __pabt_invalid @ 9
17.  .long __pabt_invalid @ a
18.  .long __pabt_invalid @ b
19.  .long __pabt_invalid @ c
20.  .long __pabt_invalid @ d
21.  .long __pabt_invalid @ e
22.  .long __pabt_invalid @ f
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3. __pabt_usr() in entry-armv.S

4. pabt_helper macro in entry-armv.S

5. b! CPU_PABORT_HANDLER (# define CPU_PABORT_HANDLER v7_pabort)

6. v7_pabort() in pabort-v7.S

7. do_PrefetchAbort() in arch/arm/mm/fault.c