1. 设置cdma, 主要是为cdma control register

```
1.
     int stmotor open(uint32 t motor num)
2.
     {
3.
              if (motor_num >= STEPPER_NUM_MOTORS)
4.
                       return -1;
5.
              smot step set blk addr irq(motor num, stmotors[motor num].stmotor cdma nu
     m);
6.
              stmotor_active |= 1 << motor_num;</pre>
7.
              return 0:
8.
9.
     }
```

```
1.
      static void smot_step_set_blk_addr_irq(stmotor_id_t motor_id, uint32_t cdma_num)
2.
 3.
              stmotors[motor id].tx dma slave.vtype = MV61 VDMA OWNED;
4.
              stmotors[motor_id].tx_dma_slave.wr_delay = 0;
5.
              stmotors[motor_id].tx_dma_slave.destendian = MV61_DMA_LITTLE_ENDIAN;
6.
              stmotors[motor_id].tx_dma_slave.srcendian = MV61_DMA_LITTLE_ENDIAN;
7.
              stmotors[motor_id].tx_dma_slave.flowctrl = MV61_DMA_MEMORY_TO_PERIPHERAL;
8.
              stmotors[motor_id].tx_dma_slave.dest_pid = cdma_num;
9.
              stmotors[motor_id].tx_dma_slave.dest_addr_inc = true;
              stmotors[motor_id].tx_dma_slave.src_addr_inc = true;
10.
11.
              stmotors[motor_id].tx_dma_slave.dest_width = MV61_DMA_XFER_WIDTH_32BIT;
12.
              stmotors[motor id].tx dma slave.src width = MV61 DMA XFER WIDTH 32BIT;
13.
              stmotors[motor_id].tx_dma_slave.data_unit_size = MV61_DMA_UNIT_SIZE_32BIT
14.
              stmotors[motor_id].tx_dma_slave.dest_burst = MV61_DMA_BURST1;
15.
              stmotors[motor_id].tx_dma_slave.src_burst = MV61_DMA_BURST1;
16.
              stmotors[motor_id].tx_dma_slave.dest_reg = (dma_addr_t)&(stmotors[motor_i
      d].phy stmotor regs->PWM T);
17.
              stmotors[motor_id].tx_dma_slave.timebase = MV61_TIMEBASE_1MS;
18.
              stmotors[motor_id].tx_dma_slave.timer = 0;
              stmotors[motor_id].tx_dma_slave.wrap = 24;
19.
20.
      }
21.
```

2. request cdma channel

```
stmotors[motor_id].stmotor_dma_chan = dma_request_channel(mask, filter, &(stmotor s[motor_id].tx_dma_slave));
```

由于cdma driver实现的原因,必须使用这种形式,即filter callback和struct mv61_dma_slave(已经被初始化完毕)

3. 把要传输的data放置到sg中,并且进行virtual to physical mapping

```
memcpy(list_entry->data_buffer, buf, count);
2.
              sg_alloc_table(list_entry->sg_table, 1, GFP_KERNEL);
 3.
              sgl = list_entry->sg_table->sgl;
4.
              sg_set_buf(sgl, list_entry->data_buffer, count);
6.
              sgl = sg_next(sgl);
7.
8.
              len = dma_map_sg(stmotors[motor_id].stmotor_dma_chan->device->dev,
9.
                                list_entry->sg_table->sgl,
10.
                                DMA_TO_DEVICE);
11.
```

4. 准备dma传输,获得transfer descriptor

其实可以用dmaengine_prep_slave_single(),而不是象这样直接访问函数指针。

5. 设置cdma传输完毕后的callback

6. 提交该transfer descriptor

7. 等待callback

1.

```
stmotors[motor_id].stmotor_dma_chan->device->device_issue_pending(stmotors[motor_id].stmotor_dma_chan);
```

invoke dma_async_issue_pending() AP可能更好。