## 1. before relocating u-boot

	(1G boundary)
I	I
I	I
I	I
	I
I	I
[	
I	
I	
u-boot	
I	
	< 0x0800,0000 (miniloader load u-boot to here)
GENERATED_GI	BL_DATA_SIZE
	< 0x07ff,ff50
Global data (0xa8	bytes)
 space	< 0x07ff,fea8 register volatile gd_t *gd asm ("r9"), gd_t *gd所指向的
l	
stack	1
I	
I	
I	 

Question:

这里GENERATED\_GBL\_DATA\_SIZE region好像完全没必要。它的大小就是sizeof(gd\_t) (取整后)。之所以有它,是因为

in include/configs/pegmatite.h

```
#define CONFIG_SYS_INIT_SP_ADDR (CONFIG_SYS_BASE_ADDR + CONFIG_SYS_INIT_SP_SIZE - GENERATED_GBL_DATA_SIZE)
```

我觉得好像这里减去GENERATED GBL DATA SIZE是没必要的,正确的应该应该是

```
#define CONFIG_SYS_INIT_SP_ADDR (CONFIG_SYS_BASE_ADDR + CONFIG_SYS_INIT_SP_SIZE)
```

因为gd\_t structure space在crt0.S中已经预留了.

```
#if defined(CONFIG_SPL_BUILD) && defined(CONFIG_SPL_STACK)
            ldr sp, =(CONFIG SPL STACK)
     #else
            ldr sp, =(CONFIG_SYS_INIT_SP_ADDR)
5.
     #endif
            bic sp, sp, #7 /* 8-byte alignment for ABI compliance */
7.
                  sp, sp, #GD_SIZE /* allocate one GD above SP */
            sub
                  sp, sp, #7 /* 8-byte alignment for ABI compliance */
            bic
9.
                  r9, sp
                                 /* GD is above SP */
            mov
10.
                   r0, #0
            mov
                    board init f
            bl
```

尝试一下修改code,看看u-boot运行是否出错。

2. after relocating	g u-boot
	(1G boundary)
I	1
48K (null)	I
	< 0x3fff,4000
16K TLB	I
	< 0x3fff,0000
I	I
1	I
LCD frame buff	er
(1200K)	1
1	I
1	I
	< 0x3fec,4000
u-boot data (ab	oout 4k)
II	256K
II	II
u-boot code (a	about 252k)
I	II
	< 0x3fe8,4000
I	
1	1

malloc heap	malloc()申请的空间从这里分配
I	I
1200K	I
I	I
	< 0x3f68,2000
Board Info (56 byt	tes)
	< 0x3f68,1fc8
Global Data (0xa8	3 bytes)
space	< 0x3f68,1f20, register volatile gd_t *gd asm ("r9"), gd_t *gd所指向的
16 bytes (gap)	I control of the second of the
	< 0x3f68,1f10
I	I
stack	I
I	I
I	I
I	I
I	I
I	I
I	I
I	I
ļ	I
I	
I	

I	
l aviational coloniat	
original u-boot	
I	
	< 0x0800,0000 (128M boundary)
I	
ı	
I	
ThreadX for R4 c	oro I
(4M)	I
I	
1	
1	

3. u-boot load kernel and device tree blob (dtb)			
I	I		
I	I		
1			
u-boot	1		
I	I		
I	I		
1			
I	I		
[	I		
I	· I		
' 1	' I		
	< 0x0640,0000(100M boundary)		
i I			
ThreadX forR4 co	ore		
	< 0x0600,0000(96M boundary)		
1	I		
I	I		
ļ	I		
1	I		
I			

I	I	
device tree blob		I
I		
ļ		< 0x00f0,0000 (15M boundary)
I	1	
<u> </u>	I	
I	·	
' I	ı	
1	I	ı
ľ		0x00f0,0000 - 0x8000 > uncompressed kernel
I		目前kernel在解压后远小于15M,所以预留的空间是足够的
ulmage		I
I		
I		
1		
		< 0x0000,8000 (32K boundary)
İ	ı	
	ı	
ĺ	 	
  32K	1	
  32K 	   	