build drivers/pci/host/pci-pegmatite.c时报如下warning

```
1.
      | WARNING: drivers/built-in.o(.text+0x29b80): Section mismatch in reference
      from the function pegmatite_pcie_enable() to the function .init.text:pci_ass
      ign unassigned resources()
2.
      | The function pegmatite_pcie_enable() references
      | the function __init pci_assign_unassigned_resources().
3.
4.
      | This is often because pegmatite_pcie_enable lacks a __init
        annotation or the annotation of pci_assign_unassigned_resources is wrong.
5.
6.
      | WARNING: drivers/built-in.o(.data+0x1314): Section mismatch in reference f
7.
      rom the variable pegmatite_pcie_driver to the function .init.text:pegmatite_
      pcie probe()
      The variable pegmatite pcie driver references
8.
9.
      the function __init pegmatite_pcie_probe()
      | If the reference is valid then annotate the
10.
      | variable with __init* or __refdata (see linux/init.h) or name the variable
11.
       *_template, *_timer, *_sht, *_ops, *_probe, *_probe_one, *_console
12.
```

root cause 如下

```
1.
      static int __init pegmatite_pcie_probe(struct platform_device *pdev)
 2.
 3.
          struct device_node *np = pdev->dev.of_node;
          int i = 0, ret;
 4.
 5.
          struct phy *phy;
 6.
          struct device *dev = &pdev->dev;
          struct device node *child;
 7.
 8.
 9.
10.
          return 0;
11.
      }
12.
13.
      static int pegmatite_pcie_remove(struct platform_device *pdev) {
14.
          int i;
15.
          pegmatite pcie top t *pcie = platform get drvdata(pdev);
16.
          pegmatite pcie controller t *controller;
          struct pci_bus *bus_itr;
17.
18.
19.
          pcie_free_intx_irq_domain();
20.
21.
          bus_itr = pci_find_next_bus(NULL);
22.
          while (bus_itr) {
23.
              pci stop root bus(bus itr);
24.
              pci_remove_root_bus(bus_itr);
25.
              bus_itr = pci_find_next_bus(NULL);
26.
          }
27.
28.
          for (i=0; i<pcie->nr controllers; ++i) {
29.
               controller = pcie->controllers[i];
30.
31.
               pci remove proc(controller);
32.
33.
               release_resource(&controller->mem);
34.
               irq_dispose_mapping(controller->irq);
35.
               iounmap(controller->pcie_reset_control);
36.
               iounmap(controller->pcie app base);
37.
               iounmap(controller->pcie trgcfg base);
38.
          }
39.
40.
          return 0;
41.
42.
43.
      static const struct of_device_id of_pcie_table[] = {
44.
          {.compatible = "marvell,pegmatite-pcie"},
45.
          {},
46.
      };
47.
      MODULE_DEVICE_TABLE(of, of_pcie_table);
48.
49.
      static struct platform_driver pegmatite_pcie_driver = {
50.
          .probe = pegmatite pcie probe,
51.
          .remove = pegmatite_pcie_remove,
           .driver = {
52.
53.
               .owner = THIS_MODULE,
```

原因就是static int **__init** pegmatite_pcie_probe(struct platform_device *pdev) in include/linux/init.h

```
1. #define __init __section(.init.text) __cold notrace
```

即pegmatite_pcie_probe()的code会被放入".init.text" section,即在kernel初始化结束后会丢 弃该section中的code,但

static struct platform_driver pegmatite_pcie_driver 是在普通的data section中的,不会被丢弃。

在不会被丢弃的section中引用到初始化后会被丢弃的function,所以link报warning了。

```
1.
      CONFIG_DEBUG_SECTION_MISMATCH Kconfig symbol:
 2.
      The section mismatch analysis checks if there are illegal
3.
      references from one section to another section.
4.
5.
      Linux will during link or during runtime drop some sections
6.
      and any use of code/data previously in these sections will
7.
      most likely result in an oops.
8.
      In the code functions and variables are annotated with
9.
      init, devinit etc. (see full list in include/linux/init.h)
      which results in the code/data being placed in specific sections.
10.
11.
      The section mismatch analysis is always done after a full
12.
      kernel build but enabling this option will in addition
13.
      do the following:
14.
15.
      Add the option -fno-inline-functions-called-once to gcc
      When inlining a function annotated init in a non-init
16.
      function we would lose the section information and thus
17.
18.
      the analysis would not catch the illegal reference.
19.
      This option tells gcc to inline less but will also
20.
      result in a larger kernel.
      Run the section mismatch analysis for each module/built-in.o
21.
22.
      When we run the section mismatch analysis on vmlinux.o we
23.
      lose valueble information about where the mismatch was
24.
      introduced.
25.
      Running the analysis for each module/built-in.o file
26.
      will tell where the mismatch happens much closer to the
27.
      source. The drawback is that we will report the same
      mismatch at least twice.
28.
29.
      Enable verbose reporting from modpost to help solving
30.
      the section mismatches reported.
31.
```