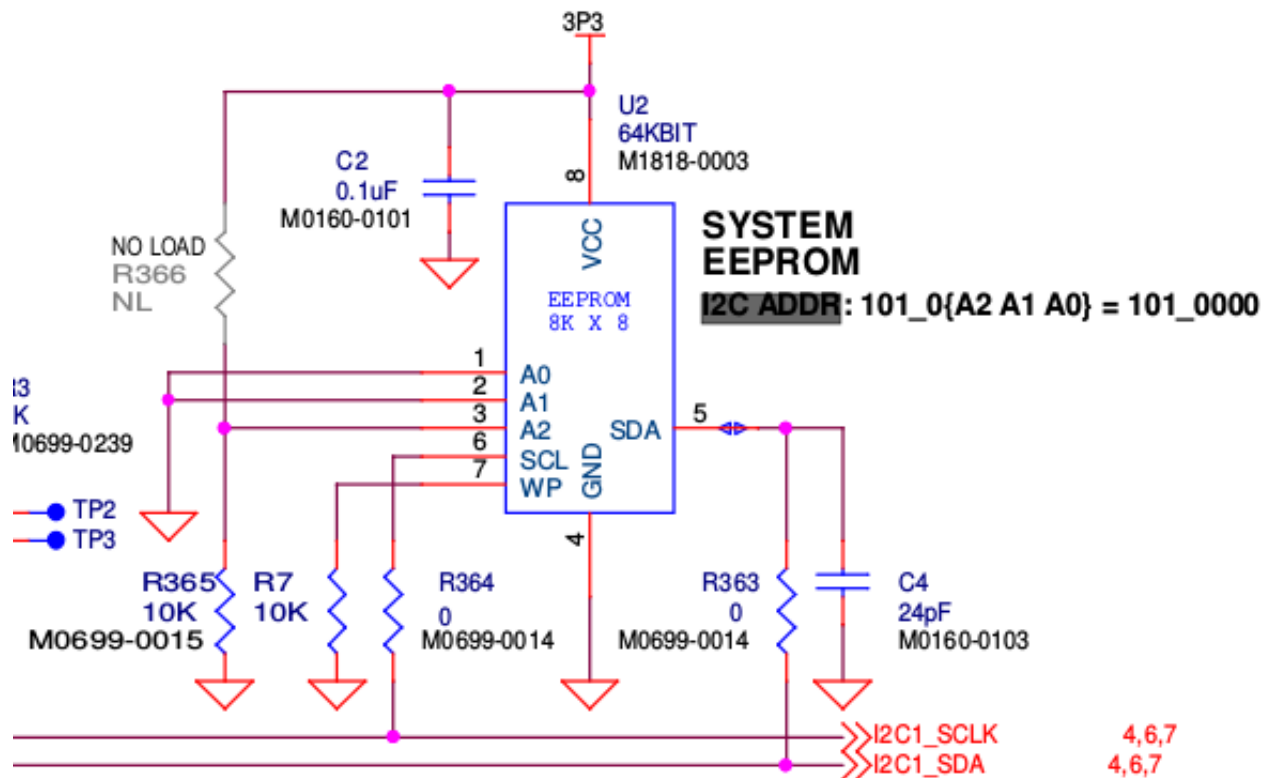


Gemstone2 TOC board的第一个I2C bus上attach了两个eeprom。

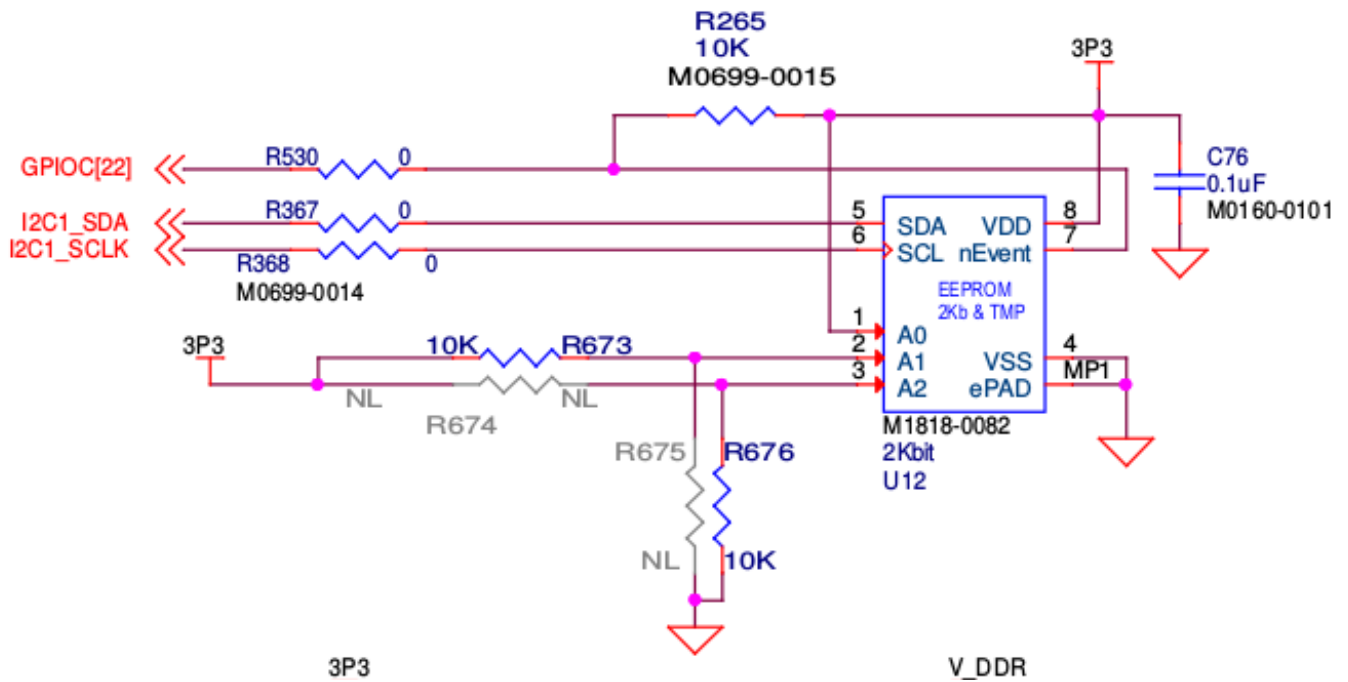
1. Notes:
- 2.
3. 对Firmware而言，第一个I2C是I2C0;而对原理图上而言，是I2C1。



该eeprom 是 $8\text{ K} * 8\text{ bits} = 8\text{K bytes}$

i2c address is $0\text{B}1010000 = 0\text{x}50$

I2C ADDR: 001_1{A2 A1 A0} = 001_1011 (Thermal Sensor)
 101_0{A2 A1 A0} = 101_0011 (EEPROM)
 011_0{A2 A1 A0} = 011_0011 (Software WP settings)



该eeprom是2Kb bit = 256 bytes

i2c address is 0B1010011 = 0x53

pcsgit/tools/test_automation/tests/eeprom

pcsgit/tools/SPD_EEPROM_init

这两个目录下的tool访问的都是该eeprom device (0x53)

该eeprom被hardware engineer成为DDR eeprom，说是用来存放对DDR的setting的。

The attachment is the schematic of Gemstone2 TOC board.