u-boot code是SMP-aware吗？

应该不是，但 ???

in arch/arm/cpu/armv7/pegmatite/smp_init.S

```
1.    .ltorg
2.
3.    .globl lowlevel_init
4.          .type   lowlevel_init, %function
5.    lowlevel_init:
6.          @ Save link register in r3
7.          mov     r3, lr
8.
9.          mrrc    p15, 1, r0, r1, c15     @ Read CPUECTRL register
10.         orr     r0, r0, #(1<<6)         @ Turn on SMP
11.         mcrr    p15, 1, r0, r1, c15     @ Write CPUECTRL register
12.
13.         mov     pc, r3                  @ return
```

读出CP15 coprocessor的c15 register，对第6位置位。从comment看是"Turn on SMP"。

in "arm_architecture_reference_v7A_v7R_manual"，

G.7.22   c15, IMPLEMENTATION DEFINED

As in ARMv7, CP15 c15 is reserved for IMPLEMENTATION DEFINED use. Typically, it is used for

processor-specific runtime and test features.

在Marvell自己的6270 / 6220 Programmer Guide上没有找到任何关于CP15 c15 register的描述。本
来这应该是Marvell的责任。