```
i2c4clk {
        compatible = "marvell,pegmatite-clkgen";
        #clock-cells = <0x0>;
        reg = <0x0 0xd0630440 0x0 0x8>;
        clocks = <0x5e>;
        clock-source = <0x0>;
        max-divide = <0x1e>;
        clock-frequency = <0x18cba80>;
        linux,phandle = <0x7b>;
        phandle = <0x7b>;
};

i2c4clkgate {
        compatible = "marvell,pegmatite-clkgate";
        #clock-cells = <0x0>;
        reg = <0x0 0xd0630440 0x0 0x8>;
        clocks = <0x7b>;
        always-used;
        linux,phandle = <0x29>;
        phandle = <0x29>;
};
```

这2个device node的register是相同的，都是<0x0 0xd0630440 0x0 0x8>，但driver是不同的

一个为"marvell,pegmatite-clkgen"，另一个为"marvell,pegmatite-clkgate"。

该clock register集成了gate clock and divide clock frequence的functions，所以用两个不同的clock driver来区分这两个逻辑上不同的功能 。比起用一个clock driver来实现更干净。It's a very beautiful design.