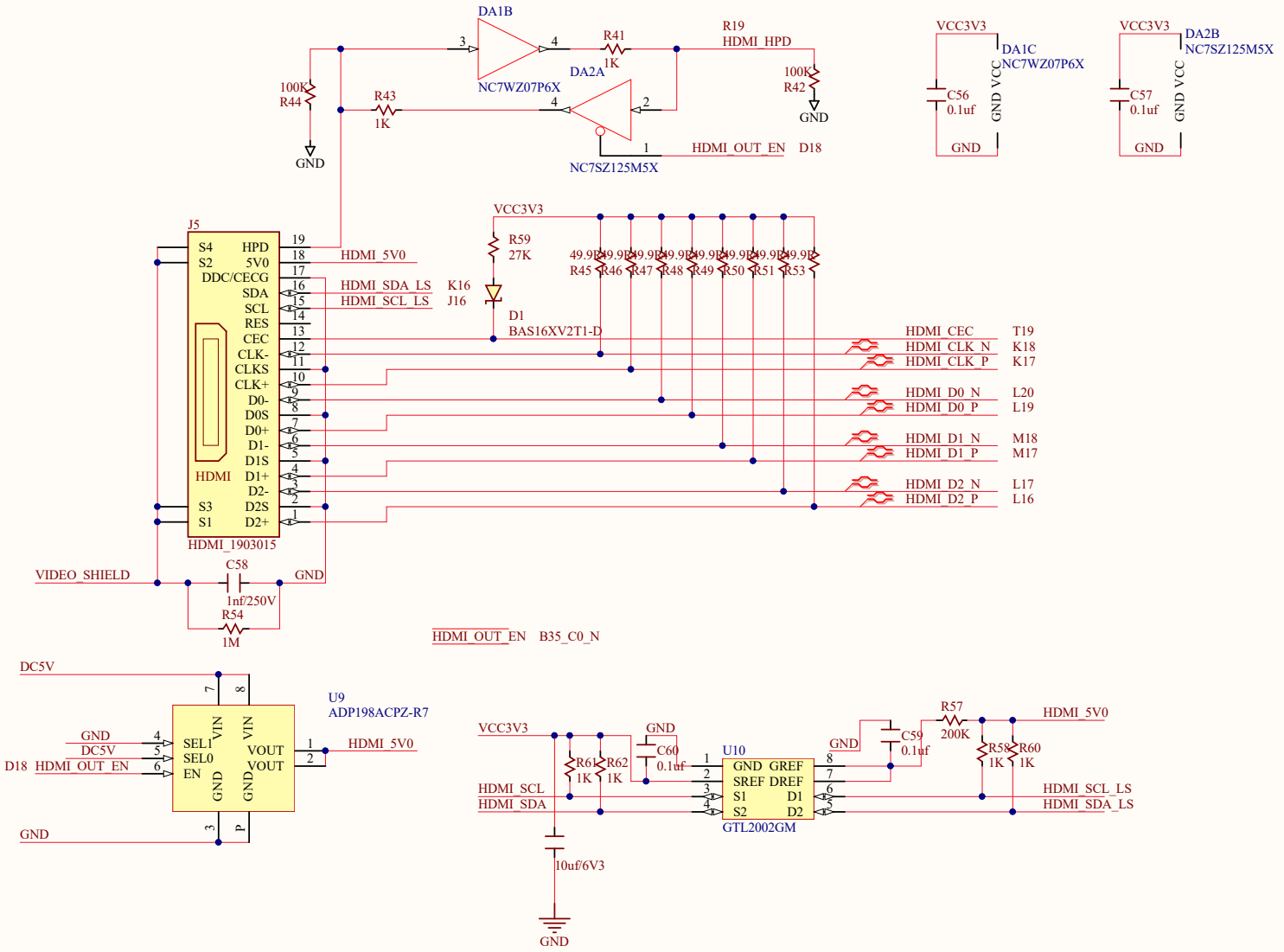

Title	MiZ702-Funtion
Rev	V1.1
Copyright	
Circuit	
Doc#	500-248
Engineer	EG
Author	GMA
Date	3/4/2015
Sheet#	4 out of 7

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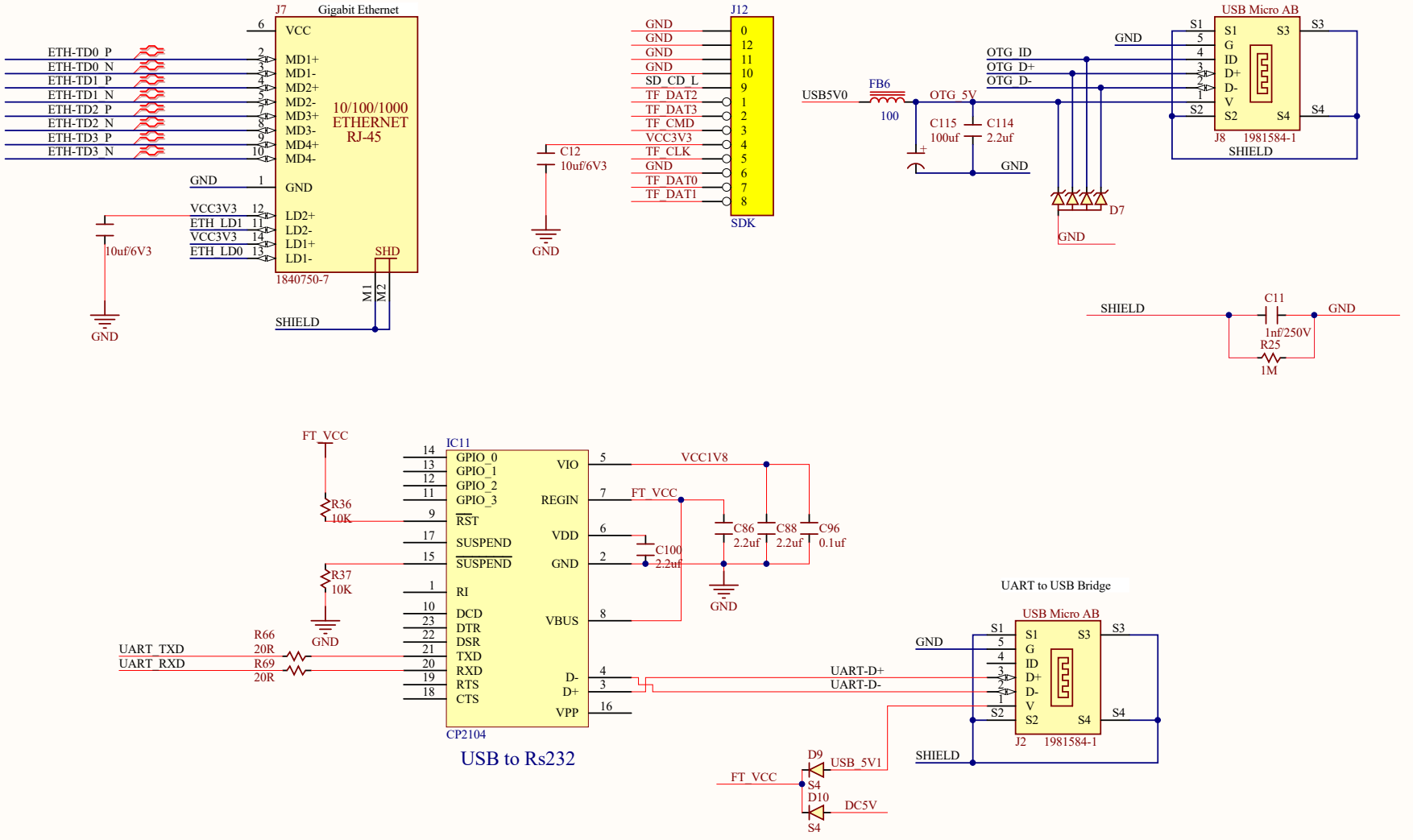
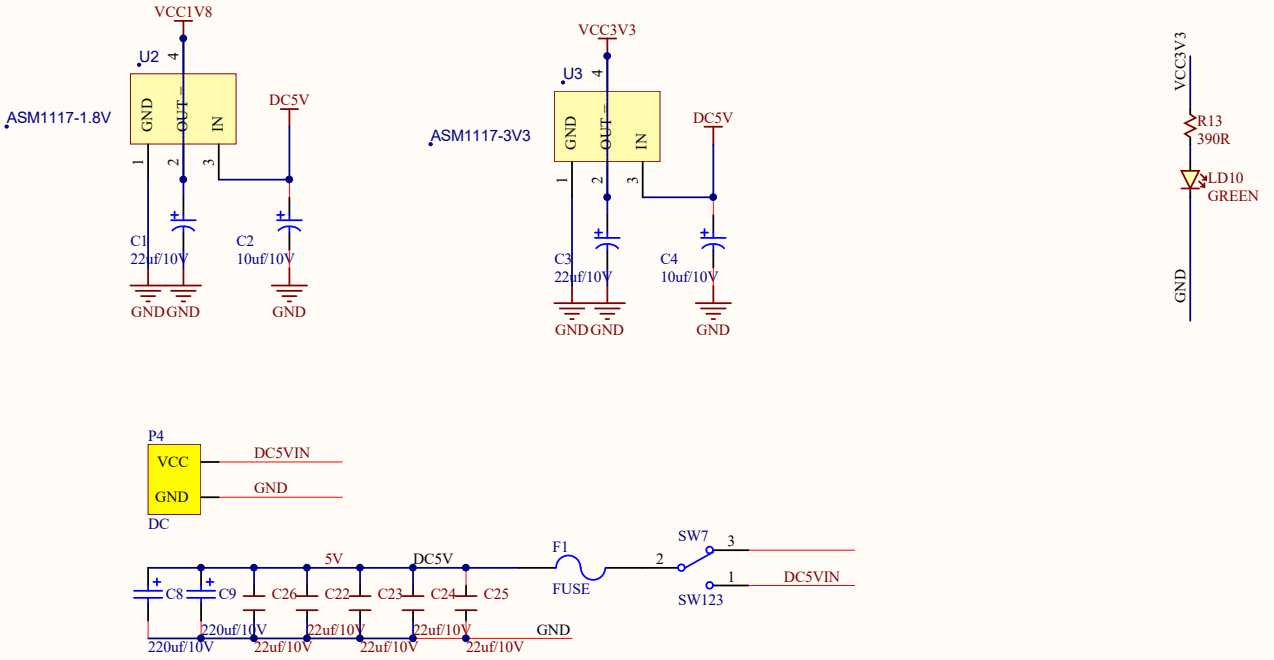
C:\Libraries\Digilent\Templates\DigilentLogo2009-1000.jpg



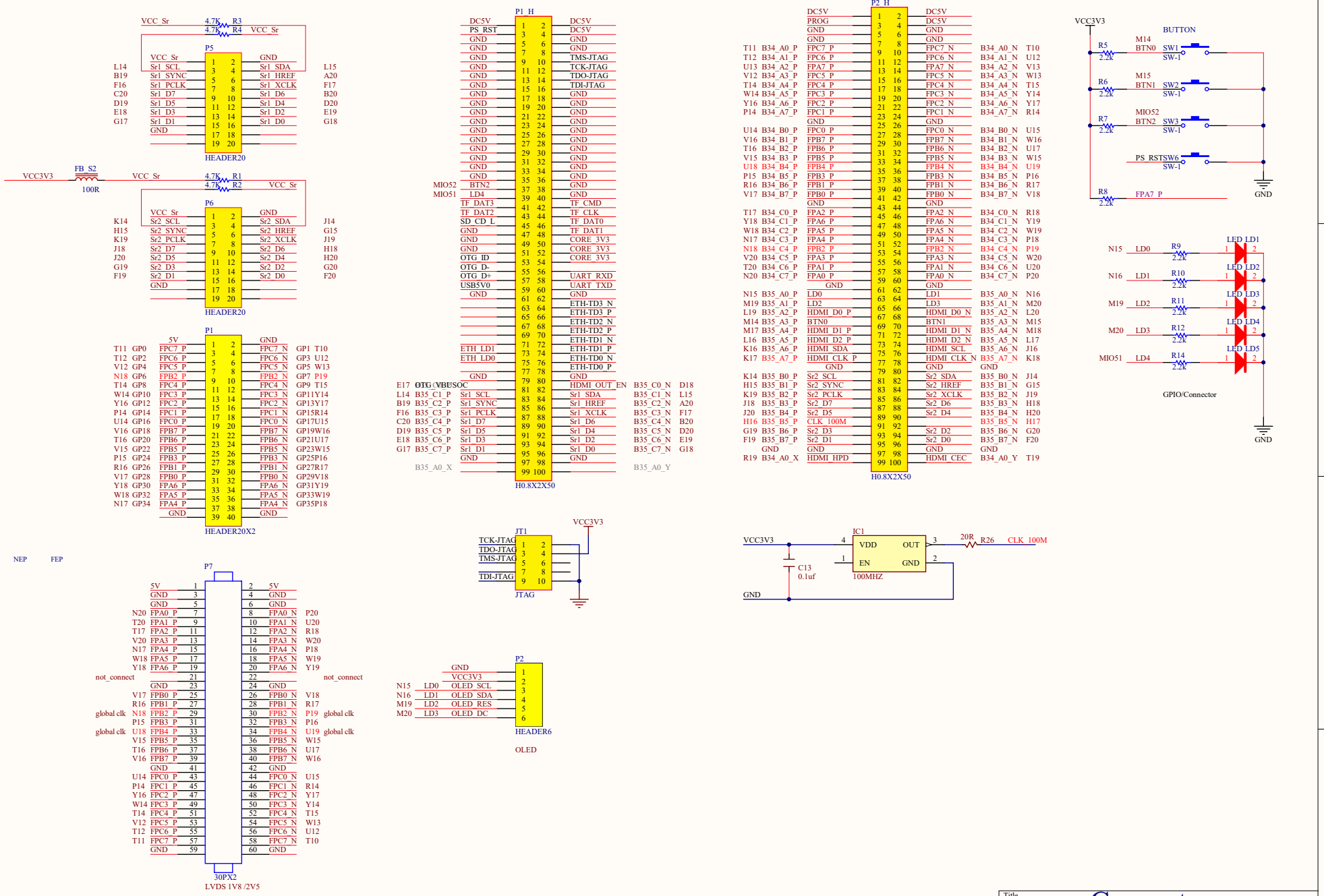
Title	MiZ702-Function
Rev	V1.1
Copyright	2013
Circuit	Ethernet,-RJ45
Doc#	500-248
Engineer	EG
Author	GMA
Date	3/4/2015
Sheet#	5 out of 7

MiCore Platform



Title	MiZ702-Funtion
Rev	v1.1
Copyright	
Circuit	Power Regulation
Doc#	
Engineer	EG
Author	jinry
Date	03/12/2015
Sheet#	15 out of 17

MiCore Platform



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Revision	
Date:	2017/2/10
Sheet	of
File:	E:\PCB_DS\..\04_connector.SchDoc
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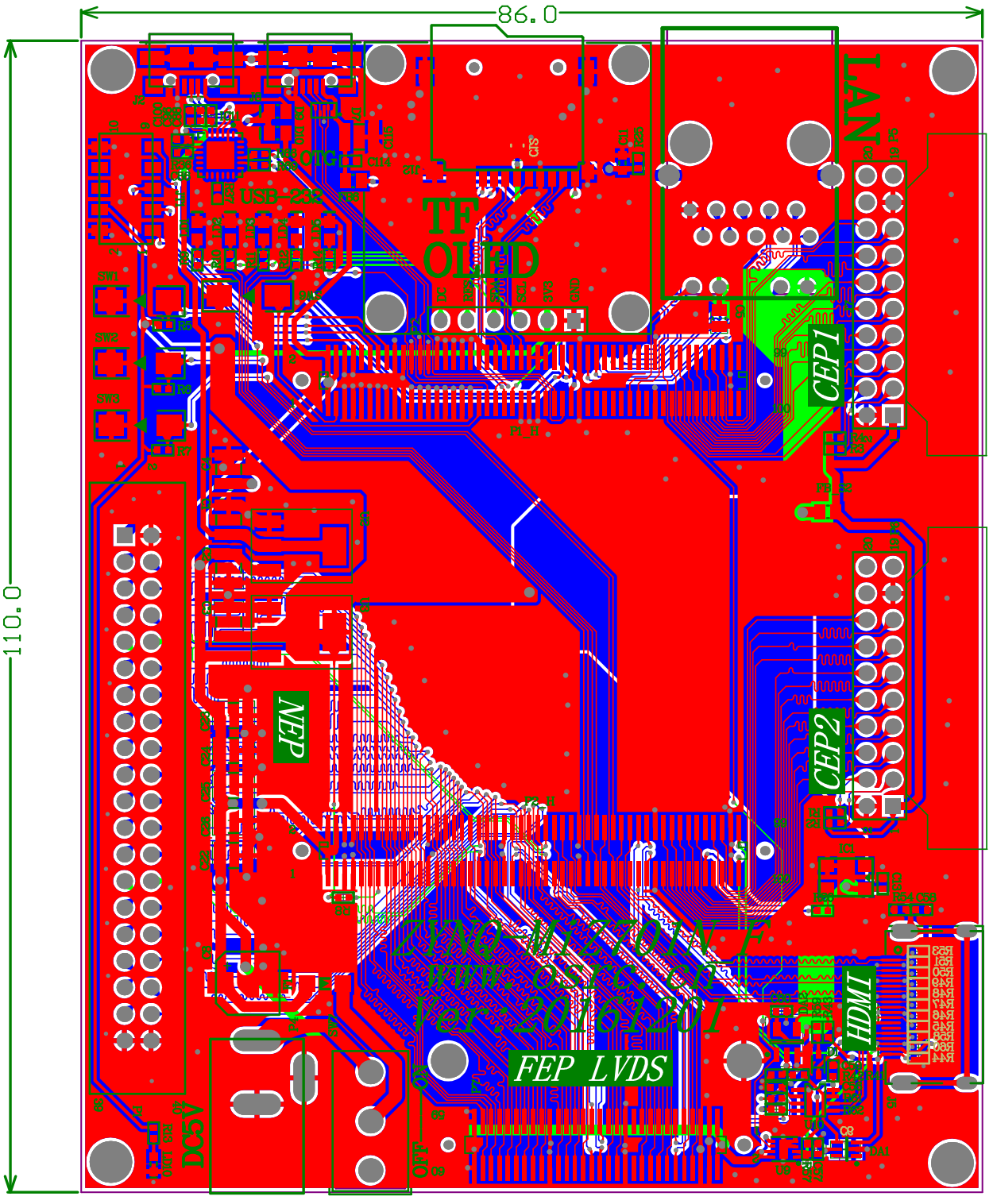
86.0
110.0
LAN
TF
OLED
CEP1
CEP2
NEP
HDMI
FEP LVDS
DC5V

Comment	Designator	Footprint	Quantity
22uf/10V	C1, C3, C22, C23, C24, C25, C26	CAP1210	7
10uf/10V	C2, C4	CAP1210	2
10uf/6V3	C5, C6, C12	CAP0805	3
220uf/10V	C8, C9	CD8	2
1nf/250V	C11	CAP0603	1
0.1uf	C13, C56, C57, C59, C60, C96	CAP0402	6
1nf/250V	C58	CAP0402	1
2.2uf	C86, C88, C100, C114	CAP0402	4
100uf	C115	CAP1210	1
BAS16XV2T1-D	D1	SOD-523	1
SESD0802Q4UG-0020-090	D7	MINI_DIO	1
S4	D9, D10	0805D	2
NC7WZ07P6X	DA1	SC70	1
NC7SZ125M5X	DA2	SOT23-5	1
FUSE	F1	1206	1
100	FB6	FB0805	1
100R	FB_S2	CAP0805	1
100MHZ	IC1	CY5.2X3.4	1
CP2104	IC11	QFN24	1
1981584-1	J2, J8	MicroUsb	2
HDMI_1903015	J5	HDMI_1903015	1
1840750-7	J7	TE1840808	1
SDK	J12	TF_K	1
JTAG	JT1	HEADER5X2X2.0T	1
LED	LD1, LD2, LD3, LD4, LD5	0603D	5
LED	LD10	D0603	1
HEADER20X2	P1	HEADER20X2X2.5	1
H0.8X2X50	P1_H, P2_H	H0.8X2X50_B	2
HEADER6	P2	HEADER1X6	1
DC	P4	DC5.0	1
HEADER20	P5, P6	HEADER10X2X2.5	2
30PX2	P7	H0.8X2X30_B	1
4.7K	R1, R2, R3, R4	CAP0402	4
2.2k	R5, R6, R7, R8, R9, R10, R11, R12, R14	RES0402	9
390R	R13	RES0402	1
1M	R25, R54	RES0402	2
20R	R26, R66, R69	RES0402	3
10K	R36, R37	RES0402	2
1K	R41, R43, R58, R60, R61, R62	RES0402	6
100K	R42, R44	RES0402	2
49.9R	R45, R46, R47, R48, R49, R50, R51, R53	RES0402	8
200K	R57	RES0402	1
27K	R59	RES0402	1
SW-1	SW1, SW2, SW3, SW6	SW-3X9	4
SW123	SW7	SW1-2-3 -B	1
ASM1117-1.8V	U2	SOT223	1
ASM1117-3V3	U3	SOT223	1
ADP198ACPZ-R7	U9	CP-8-10	1
GTL2002GM	U10	TSSOP8	1