this is I-instruction

funciton is:sw

binary code:1010111111011111110000000000010100

MIPS assembly code:sw \$sp,\$ra 0x14

this is I-instruction

funciton is:sw

MIPS assembly code:sw \$sp,\$a0 0x20

this is I-instruction

funciton is:sw

binary code:101011111010010100000000000100100

MIPS assembly code:sw \$sp,\$a1 0x24

this is R instruction

Instruction: add

Function code (binary): 100001

binary code:000000110000111111100100000100001

MIPS assembly code: add \$t9,\$t8,\$t7

this is I-instruction

funciton is:sw

binary code:10101111101110010000000000011000

MIPS assembly code:sw \$sp,\$t9 0x18

this is I-instruction

funciton is:lw

binary code:10001111101001010000000000011000

MIPS assembly code: lw \$sp, \$a1 0x18

this is J-instruction

function is:jal

binary code:00001100000000000001001011001100

MIPS assembly code: jal 0x12cc

this is I-instruction

funciton is:addiu

binary code:0010010010000100000010000110000

MIPS assembly code:addiu \$a0,\$a0 0x430

this is I-instruction

funciton is: lw

binary code:1000111110111111100000000000010100

MIPS assembly code: lw \$sp, \$ra 0x14

this is I-instruction

funciton is:addiu

MIPS assembly code:addiu \$sp,\$sp 0x20

this is R instruction

Instruction: add

Function code (binary): 001000

MIPS assembly code: add \$zero, \$ra, \$zero

this is R instruction

Instruction: add

Function code (binary): 100100

binary code:00000001101011100101100000100100

MIPS assembly code: add \$t3,\$t5,\$t6

this is I-instruction

funciton is:lw

MIPS assembly code: lw \$t2,\$t1 0x8

this is J-instruction

function is:j

binary code:0000100000000010010001101000101

MIPS assembly code: j 0x12345

this is R instruction

Instruction: add

Function code (binary): 100010

binary code:0000001010101010101100000100010

MIPS assembly code: add \$t3,\$s5,\$t1

this is I-instruction

funciton is:ori

binary code:00110101111100001011111011111

MIPS assembly code:ori \$t7,\$s0 Oxbeef

this is R instruction

Instruction: add

Function code (binary): 100000

binary code:00000010110011010101000000100000

MIPS assembly code: add \$t2,\$s6,\$t5