

```
addi sp,sp,-32
sd s0,24(sp)
addi s0,sp,32
mv a5,a0
sw a5,-20(s0)
lw a5,-20(s0)
addiw a5,a5,1
sw a5,-20(s0)
lw a5,-20(s0)
mv a0,a5
ld s0,24(sp)
addi sp,sp,32
ret
```

test2