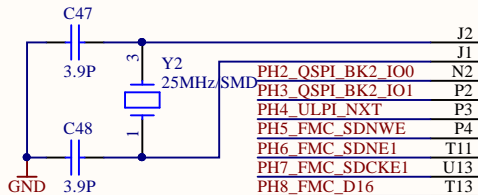


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A	<div>U_STM32H747XI_PartA STM32H747XI_PartA.sch</div> <div></div>	<div>U_IO IO.sch</div> <div></div>	A
B	<div>U_STM32H747XI_PartB STM32H747XI_PartB.sch</div> <div></div>	<div>U_Analog Analog.sch</div> <div></div>	B
C	<div>U_STM32H747XI_PartC STM32H747XI_PartC.sch</div> <div></div>		C
	<div>U_STM32H747XI_PartD STM32H747XI_PartD.sch</div> <div></div>		
	<div>U_Memory Memory.sch</div> <div></div>		
	<div>U_LCD_SDIO LCD_SDIO.sch</div> <div></div>		
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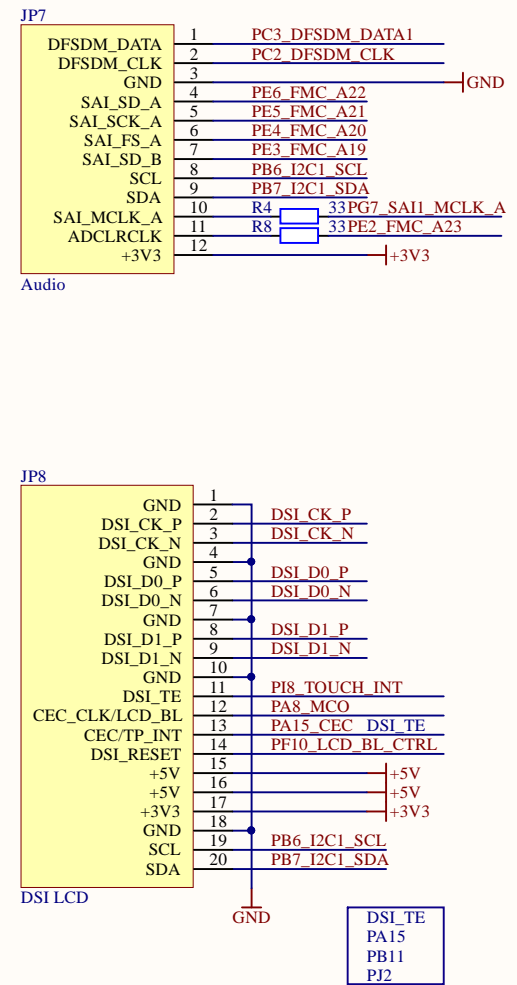
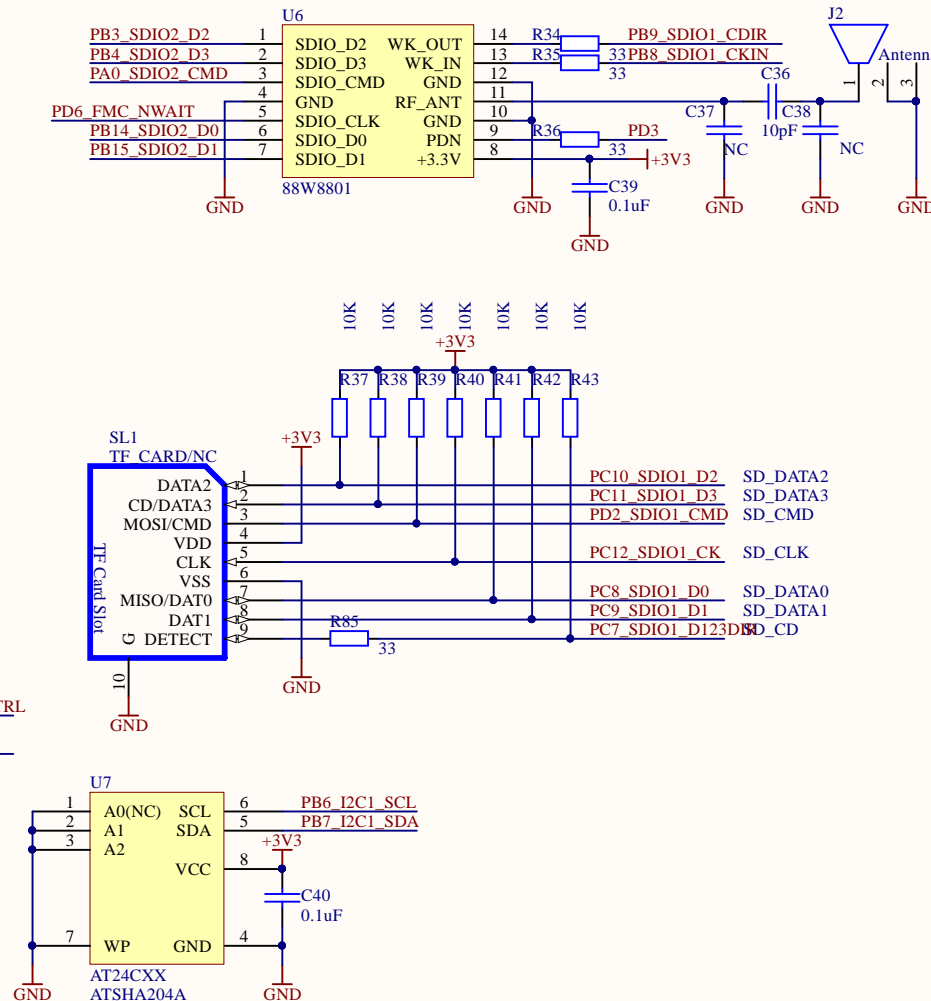
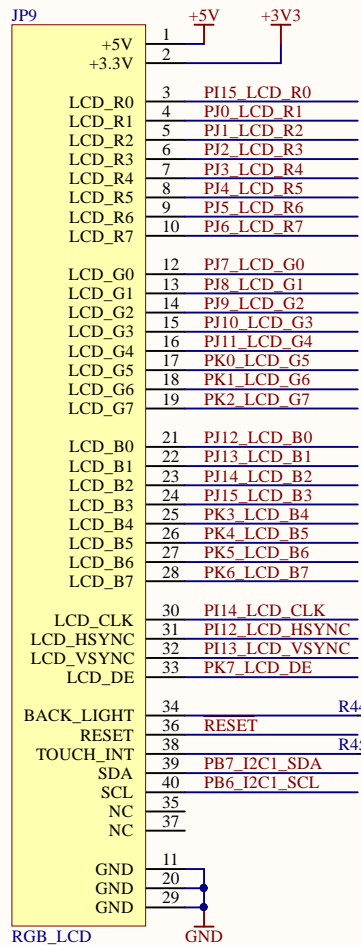


PH2_QSPI_BK2_IO0	J2
PH3_QSPI_BK2_IO1	J1
PH4_ULPI_NXT	N2
PH5_FMC_SDNWE	P2
PH6_FMC_SDNE1	P3
PH7_FMC_SDCKE1	P4
PH8_FMC_D16	T11
PH9_FMC_D17	T13
PH10_FMC_D18	R13
PH11_FMC_D19	P13
PH12_FMC_D20	P14
PH13_FMC_D21	R14
PH14_FMC_D22	D16
PH15_FMC_D23	B17
PH15_FMC_D23	B16
PI0_FMC_D24	A16
PI1_FMC_D25	A15
PI2_FMC_D26	B15
PI3_FMC_D27	C14
PI4_FMC_NBL2	A4
PI5_FMC_NBL3	A3
PI6_FMC_D28	A2
PI7_FMC_D29	B3
PI8_TOUCH_INT	E4
PI9_FMC_D30	E2
PI10_FMC_D31	F3
PI11_ULPI_DIR	F4
PI12_LCD_HSYNC	H1
PI13_LCD_VSYNC	H2
PI14_LCD_CLK	H3
PI15_LCD_R0	P5
PJ0_LCD_R1	N6
PJ1_LCD_R2	P6
PJ2_LCD_R3	T6
PJ3_LCD_R4	U6
PJ4_LCD_R5	U7
PJ5_LCD_R6	R12
PJ6_LCD_R7	N15
PJ7_LCD_G0	N14
PJ8_LCD_G1	N13
PJ9_LCD_G2	M14
PJ10_LCD_G3	L14
PJ11_LCD_G4	K14
PJ12_LCD_B0	D11
PJ13_LCD_B1	E10
PJ14_LCD_B2	D10
PJ15_LCD_B3	B10
PK0_LCD_G5	J14
PK1_LCD_G6	J15
PK2_LCD_G7	H17
PK3_LCD_B4	C8
PK4_LCD_B5	B8
PK5_LCD_B6	A8
PK6_LCD_B7	C7
PK7_LCD_DE	D7

PH0/OSC_IN	J2
PH1/OSC_OUT	J1
PH2/LPTIM1_IN2/QUADSPI_BK2_IO0/SAI2_SCK_B/ETH_MII_CRS/FMC_SDCKE0/LCD_R0/ADC3_INP13	N2
PH3/QUADSPI_BK2_IO1/SAI2_MCK_B/ETH_MII_COL/FMC_SDNE0/LCD_R1/ADC3_INP14/ADC3_INN13	P2
PH4/I2C2_SCL/LCD_G5/OTG_HS_ULPI_NXT/LCD_G4/ADC3_INP15/ADC3_INN14	P3
PH5/I2C2_SDA/SPI5_NSS/FMC_SDNWE/ADC3_INP16/ADC3_INN15	P4
PH6/TIM12_CH1/I2C2_SMBA/SPI5_SCK/ETH_MII_RXD2/FMC_SDNE1/DCMI_D8	T11
PH7/I2C3_SCL/SPI5_MISO/ETH_MII_RXD3/FMC_SDCKE1/DCMI_D9	T13
PH8/TIM5_ETR/I2C3_SDA/FMC_D16/DCMI_HSYNC/LCD_R2	R13
PH9/TIM12_CH2/I2C3_SMBA/FMC_D17/DCMI_D0/LCD_R3	P13
PH10/TIM5_CH1/I2C4_SMBA/FMC_D18/DCMI_D1/LCD_R4	P14
PH11/TIM5_CH2/I2C4_SCL/FMC_D19/DCMI_D2/LCD_R5	R14
PH12/TIM5_CH3/I2C4_SDA/FMC_D20/DCMI_D3/LCD_R6	D16
PH13/TIM8_CH1N/UART4_TX/CAN1_TX/FMC_D21/LCD_G2	B17
PH14/TIM8_CH2N/UART4_RX/CAN1_RX/FMC_D22/DCMI_D4/LCD_G3	B16
PH15/TIM8_CH3N/CAN1_TXFD/FMC_D23/DCMI_D11/LCD_G4	
PI0/TIM5_CH4/SPI2_NSS/I2S2_WS/CAN1_RXFD/FMC_D24/DCMI_D13/LCD_G5	A16
PI1/TIM8_BKIN2/SPI2_SCK/I2S2_CK/TIM8_BKIN2_COMP1/TIM8_BKIN2_COMP2/FMC_D25/DCMI_D8/LCD_G6	A15
PI2/TIM8_CH4/SPI2_MISO/I2S2_SDI/FMC_D26/DCMI_D9/LCD_G7	B15
PI3/TIM8_ETR/SPI2_MOSI/I2S2_SDO/FMC_D27/DCMI_D10	C14
PI4/TIM8_BKIN/SAI2_MCK_A/TIM8_BKIN_COMP1/TIM8_BKIN_COMP2/FMC_NBL2/DCMI_D5/LCD_B4	A4
PI5/TIM8_CH1/SAI2_SCK_A/FMC_NBL3/DCMI_VSYNC/LCD_B5	A3
PI6/TIM8_CH2/SAI2_SD_A/FMC_D28/DCMI_D6/LCD_B6	A2
PI7/TIM8_CH3/SAI2_FS_A/FMC_D29/DCMI_D7/LCD_B7	B3
PI8/RTC_TAMP_2/RTC_TS/WKUP3	E4
PI9/UART4_RX/CAN1_RX/FMC_D30/LCD_VSYNC	E2
PI10/CAN1_RXFD/ETH_MII_RX_ER/FMC_D31/LCD_HSYNC	F3
PI11/LCD_G6/OTG_HS_ULPI_DIR/WKUP4	F4
PI12/ETH_TX_ER/LCD_HSYNC	H1
PI13/LCD_VSYNC	H2
PI14/LCD_CLK	H3
PI15/LCD_G2/LCD_R0	P5
PJ0/LCD_R7/LCD_R1	N6
PJ1/LCD_R2	P6
PJ2/LCD_R3	T6
PJ3/LCD_R4	U6
PJ4/LCD_R5	U7
PJ5/LCD_R6	R12
PJ6/TIM8_CH2/LCD_R7	N15
PJ7/TIM8_CH2N/LCD_G0	N14
PJ8/TIM1_CH3N/TIM8_CH1/UART8_TX/LCD_G1	N13
PJ9/TIM1_CH3/TIM8_CH1N/UART8_RX/LCD_G2	M14
PJ10/TIM1_CH2N/TIM8_CH2/SPI5_MOSI/LCD_G3	L14
PJ11/TIM1_CH2/TIM8_CH2N/SPI5_MISO/LCD_G4	K14
PJ12/LCD_G3/LCD_B0	D11
PJ13/LCD_B4/LCD_B1	E10
PJ14/LCD_B2	D10
PJ15/LCD_B3	B10
PK0/TIM1_CH1N/TIM8_CH3/SPI5_SCK/LCD_G5	J14
PK1/TIM1_CH1/TIM8_CH3N/SPI5_NSS/LCD_G6	J15
PK2/TIM1_BKIN/TIM8_BKIN/TIM8_BKIN_COMP1/TIM8_BKIN_COMP2/TIM1_BKIN_COMP1/TIM1_BKIN_COMP2/LCD_G7	H17
PK3/LCD_B4	C8
PK4/LCD_B5	B8
PK5/LCD_B6	A8
PK6/LCD_B7	C7
PK7/LCD_DE	D7

STM32H747XIH6

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Size	Number	Revision
A4		
Date:	2019-10-23	Sheet of
File:	E:\STM32\LCD_SDIO.sch	Drawn By:

A

B

C

D

A

B

C

D

STM32H743II核心板
对应的管脚定义

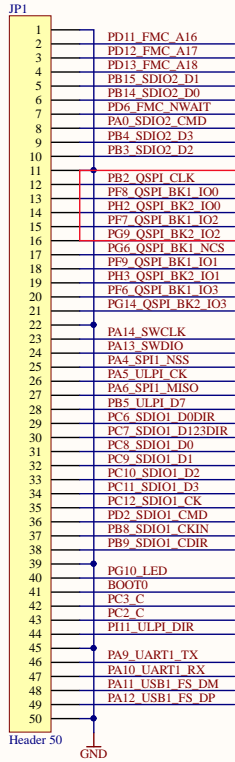
FMC_A16
FMC_A17
FMC_A18
SDIO2_D1
SDIO2_D0
SDIO2_CLK
SDIO2_CMD
SDIO2_D3
SDIO2_D2

QSPI_CLK
QSPI_BK1_IO0
QSPI_BK2_IO0
QSPI_BK1_IO2
QSPI_BK2_IO2
QSPI_BK1_NCS
QSPI_BK1_IO1
QSPI_BK2_IO1
QSPI_BK1_IO3
QSPI_BK2_IO3

SWCLK
SWDIO
SPI1_NSS
SPI1_SCK
SPI1_MISO
SPI1_MOSI
SDIO1_D0_DIR
SDIO1_D123_DIR
SDIO1_D0
SDIO1_D1
SDIO1_D2
SDIO1_D3
SDIO1_CK
SDIO1_CMD
SDIO1_CKIN
SDIO1_CDIR

LED
BOOT0
PC3_C
PC2_C
SD_DETECT

UART1_TX
UART1_RX
USB1_FS_DM
USB1_FS_DP



PB14/PB15也是USB线走差分
SDIO2-D[0:3]走等长
208MHz
到U6等长, 到排针等长

需要修改PCB文件, 已经修改
原来16脚的PB2移到J2脚
原来12-15脚的管脚整体往下移动一个单位

QSPI BK1/2 IO[0:3] Same length
133MHz
到U10等长, 到排针等长

SDIO1-D[0:3] Same length
208MHz
到SL1等长, 到排针等长

USB 差分线

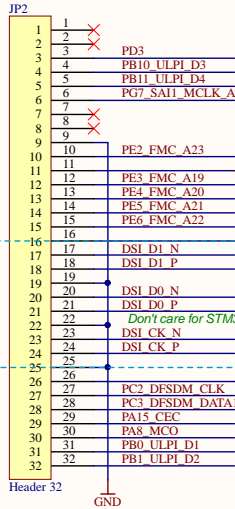
STM32H743II核心板
对应的管脚定义

PD3
PB5
PB7
SAI1_MCLK_A

SAI1_MCLK_A

SAI1_SD_B
SAI1_FS_A
SAI1_SCK_A
SAI1_SD_A

DFSDM_CKOUT
DFSDM_DATAIN1
HDMI_CEC
MCO
PG3
PB10



+5V

VBAT

500MHz
差分线

VREF+

STM32H743II核心板
对应的管脚定义

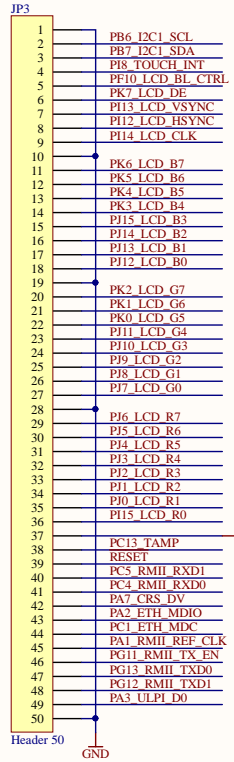
I2C_SCL
I2C_SDA
TP_INT
LCD_BL
LCD_DE
LCD_VSYNC
LCD_HSYNC
LCD_CLK

LCD_B7
LCD_B6
LCD_B5
LCD_B4
LCD_B3
LCD_B2
LCD_B1
LCD_B0

LCD_G7
LCD_G6
LCD_G5
LCD_G4
LCD_G3
LCD_G2
LCD_G1
LCD_G0

LCD_R7
LCD_R6
LCD_R5
LCD_R4
LCD_R3
LCD_R2
LCD_R1
LCD_R0

PC13
nRESET
RMII_RXD1
RMII_RXD0
RMII_CRS_DV
ETH_MDIO
ETH_MDC
RMII_REF_CLK
RMII_TX_EN
RMII_TXD0
RMII_TXD1
PA3

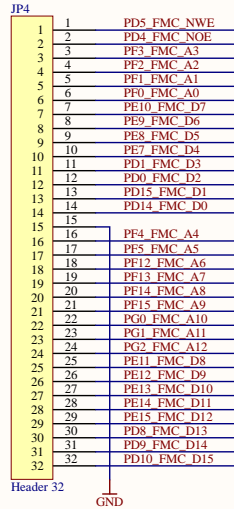


+3V3

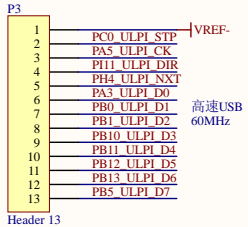
STM32H743II核心板
对应的管脚定义

CAN1_TXFD
CAN1_RXFD
FMC_A3
FMC_A2
FMC_A1
FMC_A0
FMC_D7
FMC_D6
FMC_D5
FMC_D4
FMC_D3
FMC_D2
FMC_D1
FMC_D0

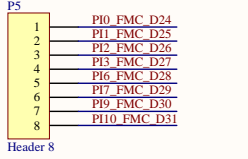
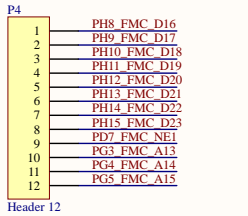
FMC_A4
FMC_A5
FMC_A6
FMC_A7
FMC_A8
FMC_A9
FMC_A10
FMC_A11
FMC_A12
FMC_D8
FMC_D9
FMC_D10
FMC_D11
FMC_D12
FMC_D13
FMC_D14
FMC_D15



数据总线到SDRAM及NOR Flash 等长



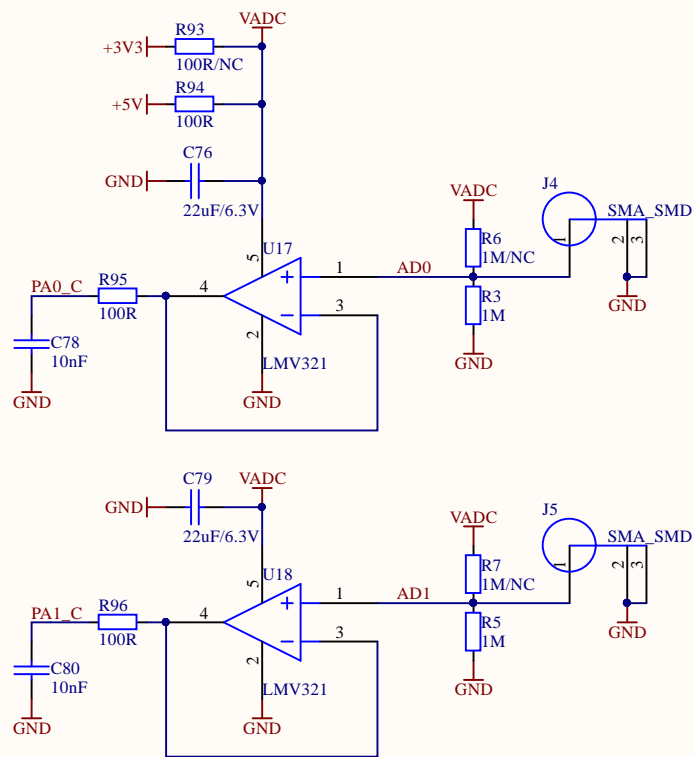
高速USB
60MHz



STM32H743II核心板
未有引出到排针上的管脚

PG15_FMC_SDNCAS
PG8_FMC_SDCLKA
PH5_FMC_SDNWE
PH6_FMC_SDNE1
PH7_FMC_SDCKE1
PH11_FMC_SDNRAS
PE0_FMC_NBL0
PE1_FMC_NBL1
PI4_FMC_NBL2
PI5_FMC_NBL3

Title		
Size	Number	Revision
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Date:	2019-10-23	Sheet of
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Title		
Size A4	Number	Revision
Date:	2019-10-23	Sheet of
File:	E:\STM32\..\Analog.sch	Drawn By:

U12A

SPI1_NSS
SPI1_SCK
SPI1_MISO

PA0_SDIO2_CMD N5
PA1_RMII_REF_CLK N4
PA2_ETH_MDIO N3
PA3_ULPI_D0 U2
PA4_SPI1_NSS U3
PA5_ULPI_CK T3
PA6_SPI1_MISO R3
PA7_CRD_DV R5
PA8_MCO E15
PA9_UART1_TX D15
PA10_UART1_RX D14
PA11_USB1_FS_DM E17
PA12_USB1_FS_DP E16
PA13_SWDIO C15
PA14_SWCLK B14
PA15_CEC A14

PA0/WKUP/TIM2_CH1/TIM2_ETR/TIM5_CH1/TIM8_ETR/TIM15_BKIN/USART2_CTS_NSS/UART4_TX/SDMMC2_CMD/SAI2_SD_B/ETH_MII_CRS/ADC1_INP16/WKUP0
PA1/TIM2_CH2/TIM5_CH2/LPTIM3_OUT/TIM15_CH1N/USART2_RTS/UART4_RX/QUADSPI_BK1_IO3/SAI2_MCK_B/ETH_MII_RX_CLK/ETH_RMII_REF_CLK/LCD_R2/ADC1_INP17/ADC1_INN16
PA2/TIM2_CH3/TIM5_CH3/LPTIM4_OUT/TIM15_CH1/USART2_TX/SAI2_SCK_B/ETH_MDIO/MDIOS_MDIO/LCD_R1/ADC12_INP14/WKUP1
PA3/TIM2_CH4/TIM5_CH4/LPTIM5_OUT/TIM15_CH2/USART2_RX/LCD_B2/OTG_HS_ULPI_D0/ETH_MII_COL/LCD_B5/ADC12_IN15
PA4/TIM5_ETR/SPI1_NSS/I2S1_WS/SPI3_NSS/I2S3_WS/USART2_SCK/SPI6_NSS/OTG_HS_SOF/DCMI_HSYNCLCD_VSYNCLCD_INP18/DAC1_OUT1
PA5/TIM2_CH1/TIM2_ETR/TIM8_CH1N/SPI1_SCK/I2S1_CK/SPI6_SCK/OTG_HS_ULPI_CK/LCD_R4/ADC12_INP19/ADC12_INN18/DAC1_OUT2
PA6/TIM1_BKIN/TIM3_CH1/TIM8_BKIN/SPI1_MISO/I2S1_SDI/SPI6_MISO/TIM13_CH1/TIM8_BKIN_COMP1/TIM8_BKIN_COMP2/MDIOS_MDC/TIM1_BKIN_COMP1/TIM1_BKIN_COMP2/DCMI_PIXCLK/LCD_G2/ADC12_INP3
PA7/TIM1_CH1N/TIM3_CH2/TIM8_CH1N/SPI1_MOSI/I2S1_SDO/SPI6_MOSI/TIM14_CH1/ETH_MII_RX_DV/ETH_RMII_CRS_DV/FMC_SDNWE/ADC12_INP7/ADC12_INN3/OPAMP1_VINM
PA8/MCO1/TIM1_CH1/HRTIM_CHB2/TIM8_BKIN2/I2C3_SCL/USART1_CK/OTG_FS_SOF/UART7_RX/TIM8_BKIN2_COMP1/TIM8_BKIN2_COMP2/LCD_B3/LCD_R6
PA9/TIM1_CH2/HRTIM_CHC1/LPUART1_TX/I2C3_SMBA/SPI2_SCK/I2S2_CK/USART1_TX/CAN1_RXFD/ETH_TX_ER/DCMI_D0/LCD_R5/OTG_FS_VBUS
PA10/TIM1_CH3/HRTIM_CHC2/LPUART1_RX/USART1_RX/CAN1_TXFD/OTG_FS_ID/MDIOS_MDIO/LCD_B4/DCMI_D1/LCD_B1
PA11/TIM1_CH4/HRTIM_CHD1/LPUART1_CTS/SPI2_NSS/I2S2_WS/UART4_RX/USART1_CTS_NSS/CAN1_RX/OTG_FS_DM/LCD_R4
PA12/TIM1_ETR/HRTIM_CHD2/LPUART1_RTS/SPI2_SCK/I2S2_CK/UART4_TX/USART1_RTS/SAI2_FS_B/CAN1_TX/OTG_FS_DP/LCD_R5
PA13/JTMS-SWDIO
PA14/JTCK-SWCLK
PA15/JTD1/TIM2_CH1/TIM2_ETR/HRTIM_FLT1/HDMI_CEC/SPI1_NSS/I2S1_WS/SPI3_NSS/I2S3_WS/SPI6_NSS/UART4_RTS/UART7_TX

SPI1_MOSI

PB0_ULPI_D1 U5
PB1_ULPI_D2 T5
PB2_QSPI_CLK R6
PB3_SDIO2_D2 C6
PB4_SDIO2_D3 B7
PB5_ULPI_D7 A5
PB6_I2C1_SCL B5
PB7_I2C1_SDA C5
PB8_SDIO1_CKIN D5
PB9_SDIO1_CDIR D4
PB10_ULPI_D3 P11
PB11_ULPI_D4 P12
PB12_ULPI_D5 T14
PB13_ULPI_D6 U14
PB14_SDIO2_D0 U15
PB15_SDIO2_D1 T15

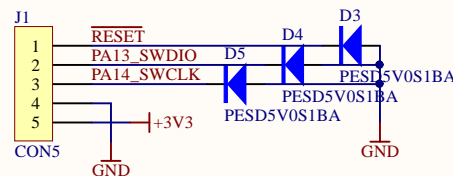
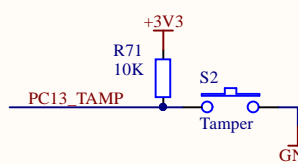
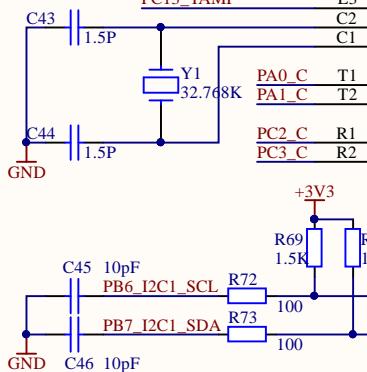
PB0/TIM1_CH2N/TIM3_CH3/TIM8_CH2N/DFSDM_CKOUT/UART4_CTS/LCD_R3/OTG_HS_ULPI_D1/ETH_MII_RXD2/LCD_G1/ADC12_INP9/ADC12_INN5/OPAMP1_VINP/COMP1_INP
PB1/TIM1_CH3N/TIM3_CH4/TIM8_CH3N/DFSDM_DATIN1/LCD_R6/OTG_HS_ULPI_D2/ETH_MII_RXD3/LCD_G0/ADC12_INP5/COMP1_INM
PB2/SAI1_D1/DFSDM_CKIN1/SAI1_SD_A/SPI3_MOSI/I2S3_SDO/SAI4_SD_A/QUADSPI_CLK/SAI4_D1/ETH_TX_ER/COMP1_INP
PB3/JTD0/TRACESWO/TIM2_CH2/HRTIM_FLT4/SPI1_SCK/I2S1_CK/SPI3_SCK/I2S3_CK/SPI6_SCK/SDMMC2_D2/UART7_RX
PB4/JTTRST/TIM16_BKIN/TIM3_CH1/HRTIM_EEV6/SPI1_MISO/I2S1_SDI/SPI3_MISO/I2S3_SDI/SPI2_NSS/I2S2_WS/SPI6_MISO/SDMMC2_D3/UART7_TX
PB5/TIM17_BKIN/TIM3_CH2/HRTIM_EEV7/I2C1_SMBA/SPI1_MOSI/I2S1_SDO/I2C4_SMBA/SPI3_MOSI/I2S3_SDO/SPI6_MOSI/CAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/FMC_SDCKE1/DCMI_D10/UART5_RX
PB6/TIM16_CH1N/TIM4_CH1/HRTIM_EEV8/I2C1_SCL/HDMI_CEC/I2C4_SCL/USART1_TX/LPUART1_TX/CAN2_TX/QUADSPI_BK1_NCS/DFSDM_DATIN5/FMC_SDNE1/DCMI_D5/UART5_TX
PB7/TIM17_CH1N/TIM4_CH2/HRTIM_EEV9/I2C1_SDA/I2C4_SDA/USART1_RX/LPUART1_RX/CAN2_TXFD/DFSDM_CKIN5/FMC_NL/DCMI_VSYNCPVD_IN
PB8/TIM16_CH1/TIM4_CH3/DFSDM_CKIN7/I2C1_SCL/I2C4_SCL/SDMMC1_CKIN/UART4_RX/CAN1_RX/SDMMC2_D4/ETH_MII_TXD3/SDMMC1_D4/DCMI_D6/LCD_B6
PB9/TIM17_CH1/TIM4_CH4/DFSDM_DATIN7/I2C1_SDA/SPI2_NSS/I2S2_WS/I2C4_SDA/SDMMC1_CDIR/UART4_TX/CAN1_TX/SDMMC2_D5/I2C4_SMBA/SDMMC1_D5/DCMI_D7/LCD_B7
PB10/TIM2_CH3/HRTIM_SCOUT/I2C2_SCL/SPI2_SCK/I2S2_CK/DFSDM_DATIN7/USART3_TX/QUADSPI_BK1_NCS/OTG_HS_ULPI_D3/ETH_MII_RX_ER/LCD_G4
PB11/TIM2_CH4/HRTIM_SCIN/LPTIM2_ETR/I2C2_SDA/DFSDM_CKIN7/USART3_RX/OTG_HS_ULPI_D4/ETH_MII_TX_EN/ETH_RMII_TX_EN/LCD_G5
PB12/TIM1_BKIN/I2C2_SMBA/SPI2_NSS/I2S2_WS/DFSDM_DATIN1/USART3_CK/CAN2_RX/OTG_HS_ULPI_D5/ETH_MII_TXD0/ETH_RMII_TXD0/OTG_HS_ID/TIM1_BKIN_COMP1/TIM1_BKIN_COMP2/UART5_RX
PB13/TIM1_CH1N/LPTIM2_OUT/SPI2_SCK/I2S2_CK/DFSDM_CKIN1/USART3_CTS_NSS/CAN2_TX/OTG_HS_ULPI_D6/ETH_MII_TXD1/ETH_RMII_TXD1/UART5_TX/OTG_HS_VBUS
PB14/TIM1_CH2N/TIM12_CH1/TIM8_CH2N/USART1_TX/SPI2_MISO/I2S2_SDI/DFSDM_DATIN2/USART3_RTS/UART4_RTS/SDMMC2_D0/OTG_HS_DM
PB15/RTC_REFIN/TIM1_CH3N/TIM12_CH2/TIM8_CH3N/USART1_RX/SPI2_MOSI/I2S2_SDO/DFSDM_CKIN2/UART4_CTS/SDMMC2_D1/OTG_HS_DP

PCI_PDM1_D1

PC0_ULPI_STP L2
PC1_ETH_MDC M2
PC2_DFSDM_CLK M3
PC3_DFSDM_DATA1 M4
PC4_RMII_RXD0 T4
PC5_RMII_RXD1 U4
PC6_SDIO1_D0DIR F14
PC7_SDIO1_D123DIR F13
PC8_SDIO1_D0 E13
PC9_SDIO1_D1 E14
PC10_SDIO1_D2 A13
PC11_SDIO1_D3 B13
PC12_SDIO1_CK C12
PC13_TAMP E3

PC0/DFSDM_CKIN0/DFSDM_DATIN4/SAI2_FS_B/OTG_HS_ULPI_STP/FMC_SDNWE/LCD_R5/ADC123_INP10
PC1/SAI1_D1/DFSDM_DATIN0/DFSDM_CKIN4/SPI2_MOSI/I2S2_SDO/SAI1_SD_A/SAI4_SD_A/SDMMC2_CK/SAI4_D1/ETH_MDC/MDIOS_MDC/ADC123_INP11/ADC123_INN10/RTC_TAMP_3/WKUP5
PC2/DFSDM_CKIN1/SPI2_MISO/I2S2_SDI/DFSDM_CKOUT/OTG_HS_ULPI_DIR/ETH_MII_TXD2/FMC_SDNE0/ADC123_INP12/ADC123_INN11
PC3/DFSDM_DATIN1/SPI2_SDO/OTG_HS_ULPI_NXT/ETH_MII_TX_CLK/FMC_SDCKE0/ADC12_INP13/ADC12_INN12
PC4/DFSDM_CKIN2/I2S1_MCK/SPDIFRX_IN2/ETH_MII_RXD0/ETH_RMII_RXD0/FMC_SDNE0/ADC12_INP4/OPAMP1_VOUT/COMP1_INM
PC5/SAI1_D3/DFSDM_DATIN2/SPDIFRX_IN3/SAI4_D3/ETH_MII_RXD1/ETH_RMII_RXD1/FMC_SDCKE0/COMP1_OUT/ADC12_INP8/ADC12_INN4/OPAMP1_VINM
PC6/HRTIM_CHA1/TIM3_CH1/TIM8_CH1/DFSDM_CKIN3/I2S2_MCK/USART6_TX/SDMMC1_D0DIR/FMC_NWAIT/SDMMC2_D6/SDMMC1_D6/DCMI_D0/LCD_HSYNCLCD_INP18/DAC1_OUT1
PC7/HRTIM_CHA2/TIM3_CH2/TIM8_CH2/DFSDM_DATIN3/I2S3_MCK/USART6_RX/SDMMC1_D123DIR/FMC_NE1/SDMMC2_D7/SWPMI_TX/SDMMC1_D7/DCMI_D1/LCD_G6
PC8/HRTIM_CHB1/TIM3_CH3/TIM8_CH3/USART6_CK/UART5_RTS/FMC_NE2/FMC_NCE/SWPMI_RX/SDMMC1_D0/DCMI_D2
PC9/MCO2/TIM3_CH4/TIM8_CH4/I2C3_SDA/I2S_CKIN/UART5_CTS/QUADSPI_BK1_IO0/LCD_G3/SWPMI_SUSPEND/SDMMC1_D1/DCMI_D3/LCD_B2
PC10/HRTIM_EEV1/DFSDM_CKIN5/SPI3_SCK/I2S3_CK/USART3_TX/UART4_TX/QUADSPI_BK1_IO1/SDMMC1_D2/DCMI_D8/LCD_R2
PC11/HRTIM_FLT2/DFSDM_DATIN5/SPI3_MISO/I2S3_SDI/USART3_RX/UART4_RX/QUADSPI_BK2_NCS/SDMMC1_D3/DCMI_D4
PC12/HRTIM_EEV2/SPI3_MOSI/I2S3_SDO/USART3_CK/UART5_TX/SDMMC1_CK/DCMI_D9
PC13/RTC_TAMP_1/RTC_TS/WKUP2
PC14-OSC32_IN
PC15-OSC32_OUT

STM32H747XIH6



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