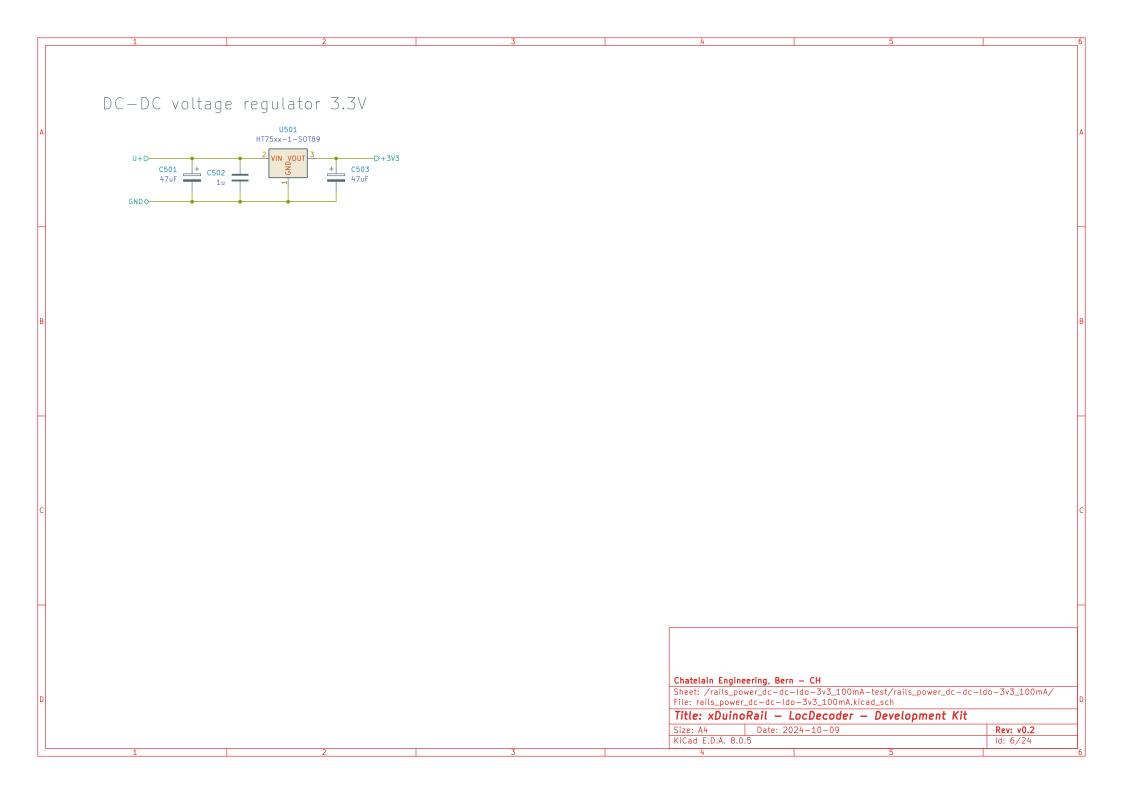
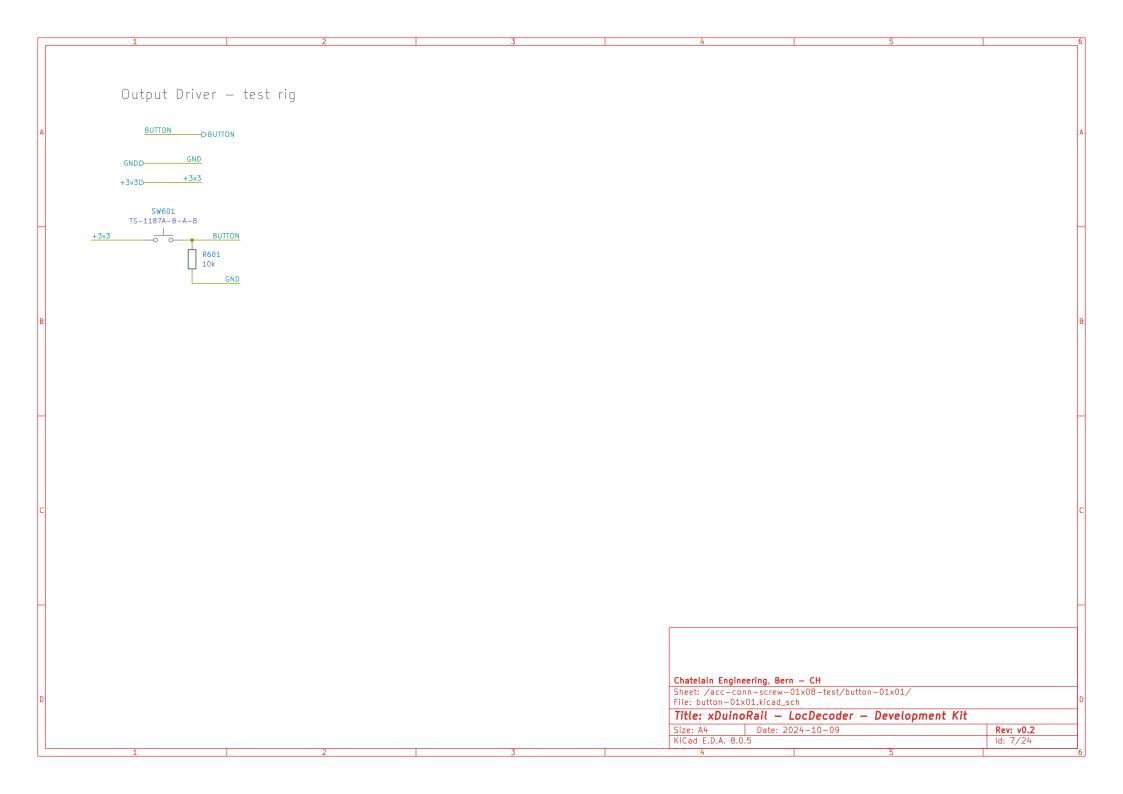


# Bridge rectifier medium AC[0..1]D D302 SS14 PWR\_FLAG F301 SRF1206P150 PWR\_FLAG D303 Chatelain Engineering, Bern — CH Sheet: /rails\_power\_ac-dc-bridge-medium-x/ File: rails\_power\_ac-dc-bridge-medium-x.kicad\_sch Title: xDuinoRail - LocDecoder - Development Kit Size: A4 Date: 2024-10-09 Rev: v0.2 KiCad E.D.A. 8.0.5 ld: 4/24

Output Driver FUNC[0..1]D FUNC[0..1] AMP\_FUNC[0..1] DAMP\_FUNC[0..1] GND GNDD-U+D-U401 ULN2003A FUNC0 AMP\_FUNCO FUNC1 GND Chatelain Engineering, Bern — CH
Sheet: /acc-switch-driver-dual-test/acc-switch-driver1/ File: acc-switch-driver.kicad\_sch Title: xDuinoRail — LocDecoder — Development Kit Size: A4 Date: 2024-10-09 Rev: v0.2 KiCad E.D.A. 8.0.5 ld: 5/24



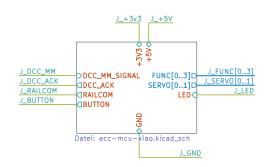


DCC\_MM\_SIGNALD\_\_DCC\_MM\_SIGNAL DCC\_ACK DCC\_ACK< RAILCOM RAILCOM -BUTTON BUTTOND-

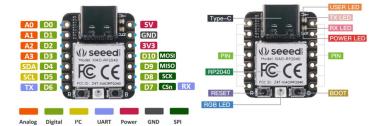
FUNC[0..3] DFUNC[0..3]

### XIAO Header (Decoder Pinout) - test rig









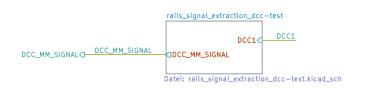
Chatelain Engineering, Bern — CH

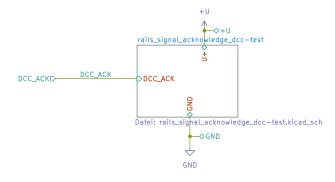
Sheet: /acc-mcu-xiao-test/ File: acc-mcu-xiao-test.kicad\_sch

Title: xDuinoRail - LocDecoder - Development Kit

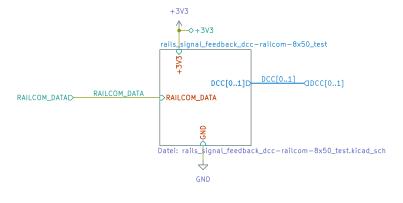
Size: A4 Date: 2024-10-09 Rev: v0.2 KiCad E.D.A. 8.0.5 ld: 8/24

### DCC/RailCom Up- & Downlinks









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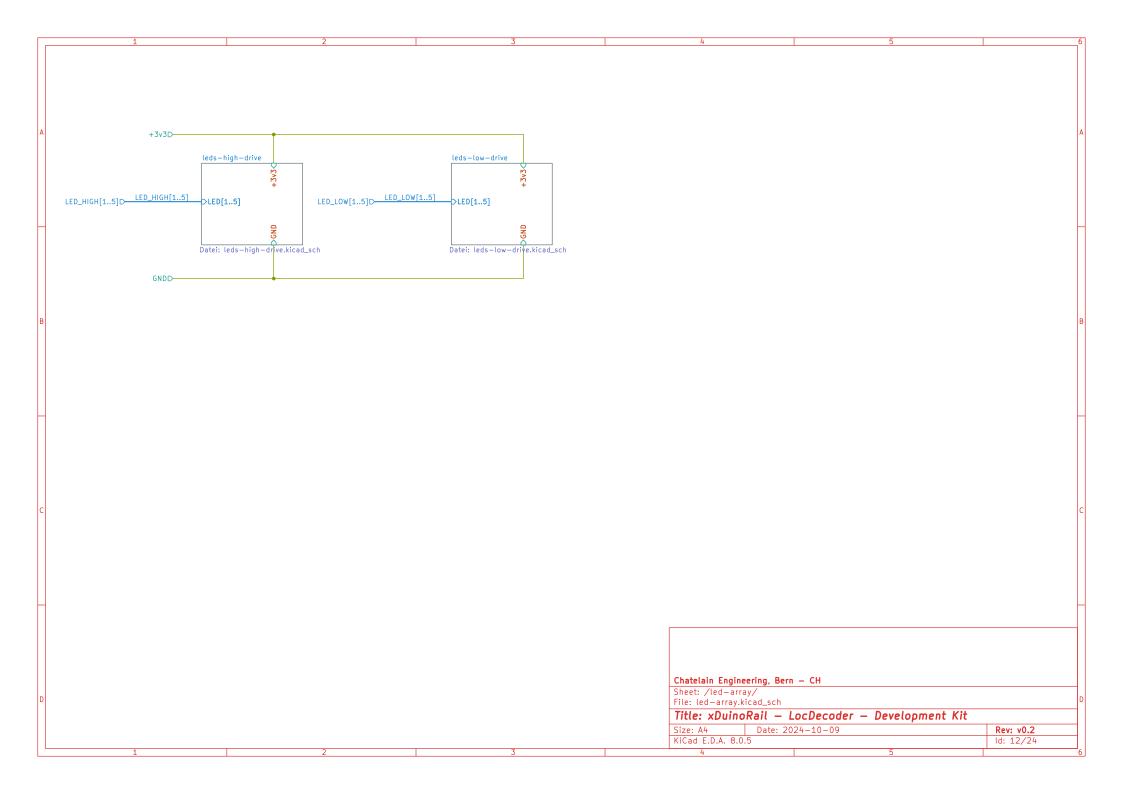
Sheet: /rails\_signal\_combined-test/ File: rails\_signal\_combined-test.kicad\_sch

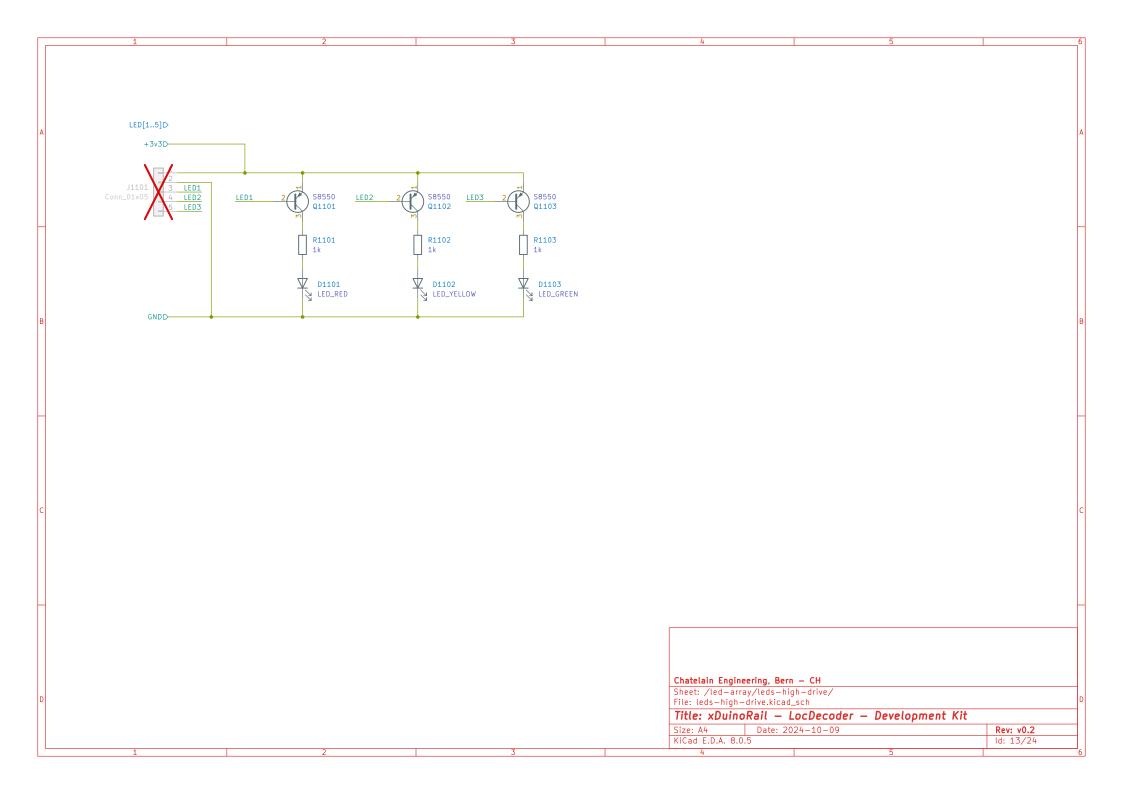
Title: xDuinoRail — LocDecoder — Development Kit

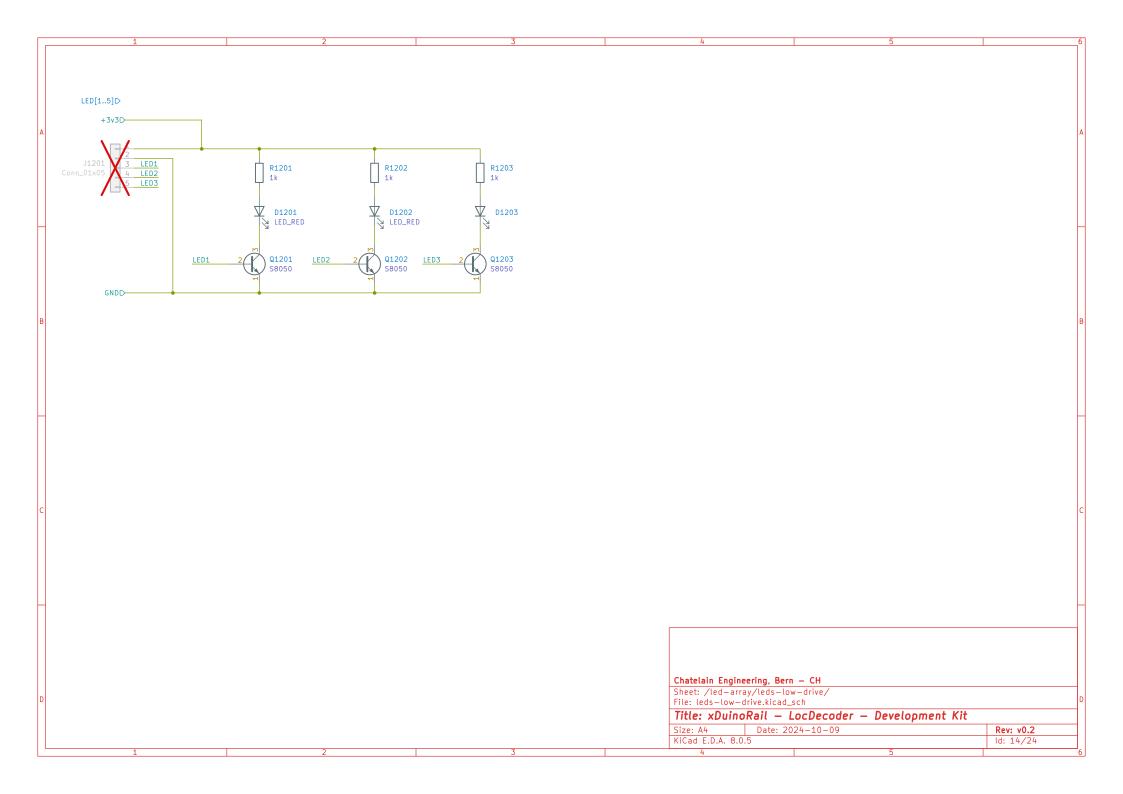
 Size: A4
 Date: 2024-10-09
 Rev: v0.2

 KiCad E.D.A. 8.0.5
 Id: 9/24







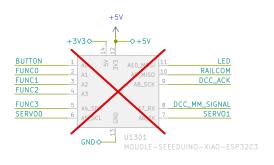


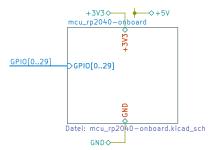
DCC\_MM\_SIGNALD
DCC\_ACK
DCC\_ACKC
RAILCOMC
BUTTON
BUTTON

FUNC[0..3]
SERVO[0..1]
DED

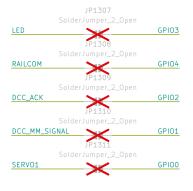
GLED

### XIAO Header (Decoder Pinout)









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Sheet: /acc-mcu-xiao-test/acc-mcu-xiao/

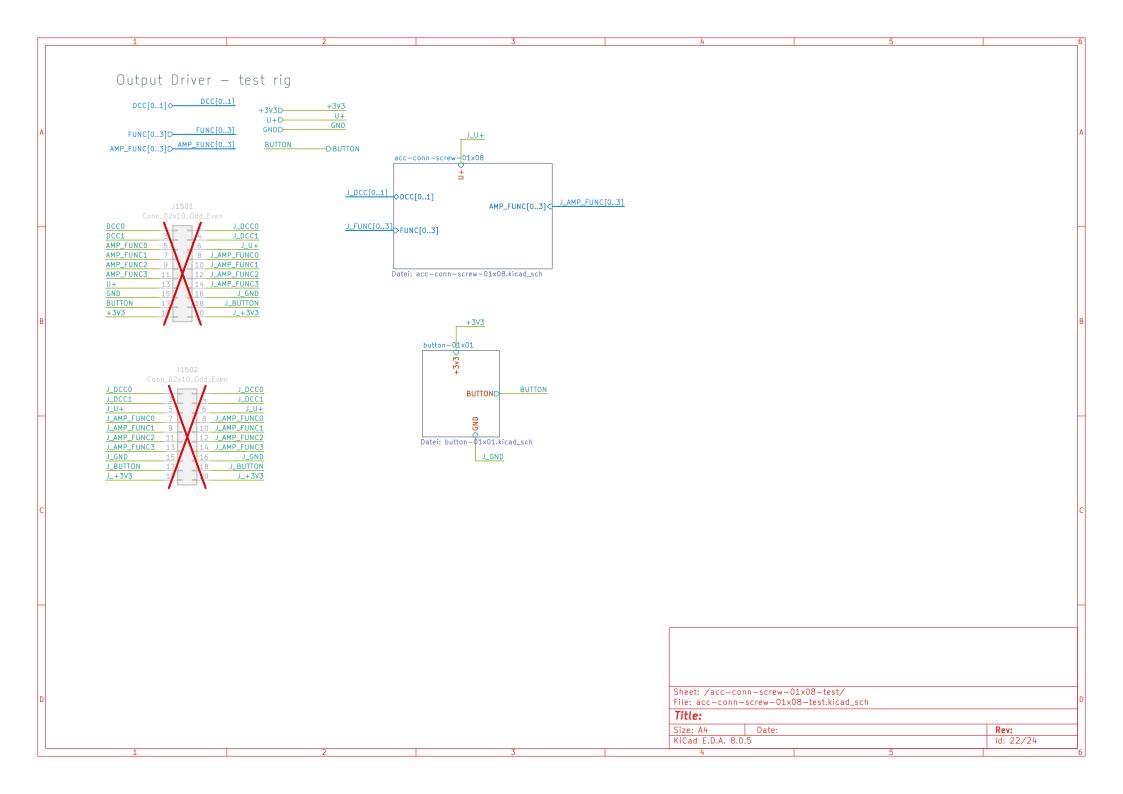
File: acc-mcu-xiao.kicad\_sch

Title: xDuinoRail - LocDecoder - Development Kit

 Size: A4
 Date: 2024-10-09
 Rev: v0.2

 KiCad E.D.A. 8.0.5
 Id: 18/24

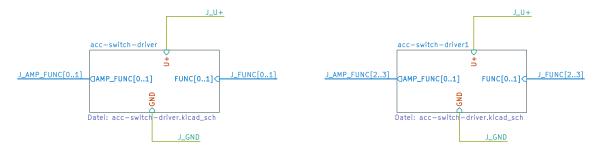
# Resistor (Use ONLY with on MCU pin protection) rails-signal-extraction\_dcc DCC\_MM\_SIGNAL DCC\_MM\_SIGNAL J\_DCC\_MM\_SIGNAL DCC\_MM\_SIGNALD J\_DCC1 Datei: rails-signal-extraction\_dcc.kicad\_sch J\_DCC\_MM\_SIGNAL Chatelain Engineering, Bern — CH Sheet: /rails\_signal\_combined-test/rails\_signal\_extraction\_dcc-test/ File: rails\_signal\_extraction\_dcc-test.kicad\_sch Title: xDuinoRail - LocDecoder - Development Kit Size: A4 Date: 2024-10-09 Rev: v0.2 KiCad E.D.A. 8.0.5 ld: 21/24





FUNC[0..3] FUNC[0..3] AMP\_FUNC[0..3] DAMP\_FUNC[0..3]

GNDD GND



J1601 Conn_02x10_0dd_Even					J1602 Conn_02x10_0dd_Even			
J_GND	_02x.		JUU_E\ ■ <b>1</b>	J_GND	J_GND	_02x1		GND
J_U+	3		4	J_U+	J_U+	3	4	U+
J_FUNC0	5		6	J_FUNC0	J_FUNC0	5	6	FUNC0
J_FUNC1	7	1	8	J_FUNC1	J_FUNC1	7	8	FUNC1
J_FUNC2	9	M	10	J_FUNC2	J_FUNC2	9	10	FUNC2
J_FUNC3	11	X	12	J_FUNC3	J_FUNC3	11	12	FUNC3
J_AMP_FUNC0	13	71	14	J_AMP_FUNCO	J_AMP_FUNC0	13	14	AMP_FUNC0
J_AMP_FUNC1	15	/	16	J_AMP_FUNC1	J_AMP_FUNC1	15	16	AMP_FUNC1
J_AMP_FUNC2	17		18	J_AMP_FUNC2	J_AMP_FUNC2	17	18	AMP_FUNC2
J_AMP_FUNC3	19		10	J_AMP_FUNC3	J_AMP_FUNC3	11	70	AMP_FUNC3
		_	_			_/=		

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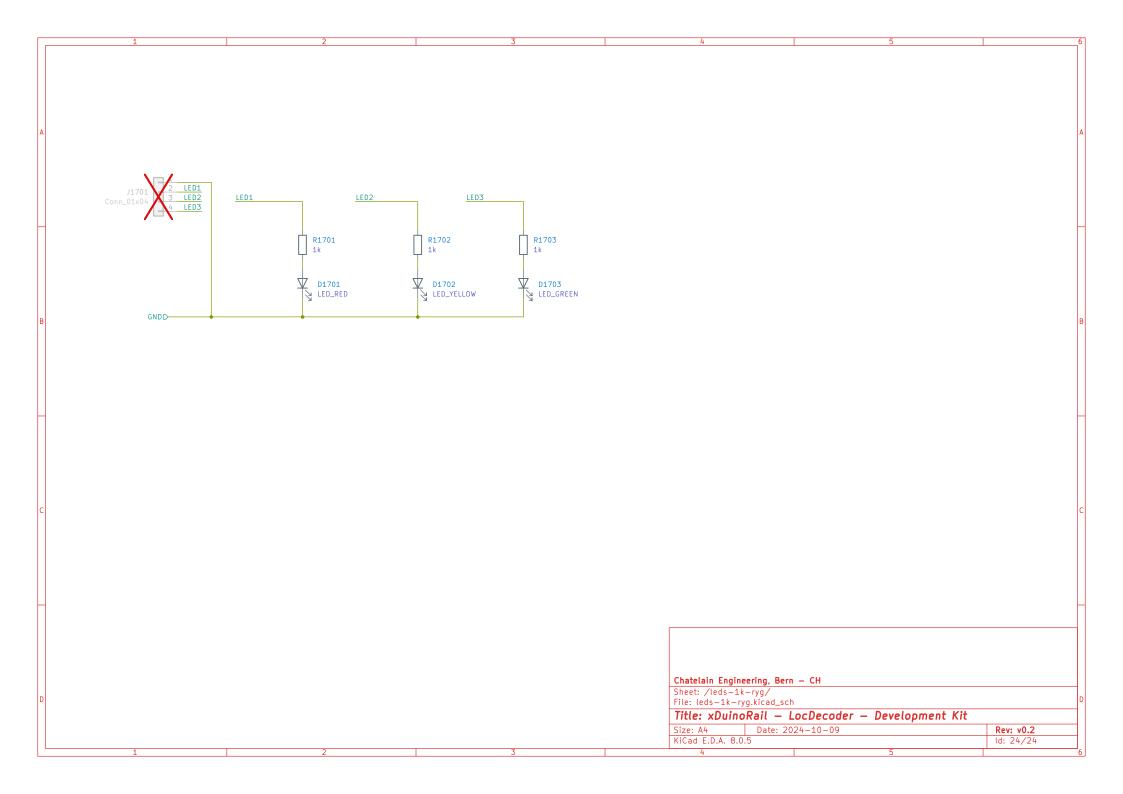
Sheet: /acc-switch-driver-dual-test/ File: acc-switch-driver-dual-test.kicad\_sch

Title: xDuinoRail — LocDecoder — Development Kit

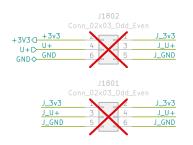
 Size: A4
 Date: 2024–10–09
 Rev: v0.2

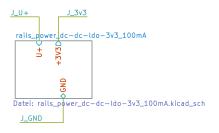
 KiCad E.D.A. 8.0.5
 Id: 24/24

2



## DC-DC voltage regulator 3.3V - test rig





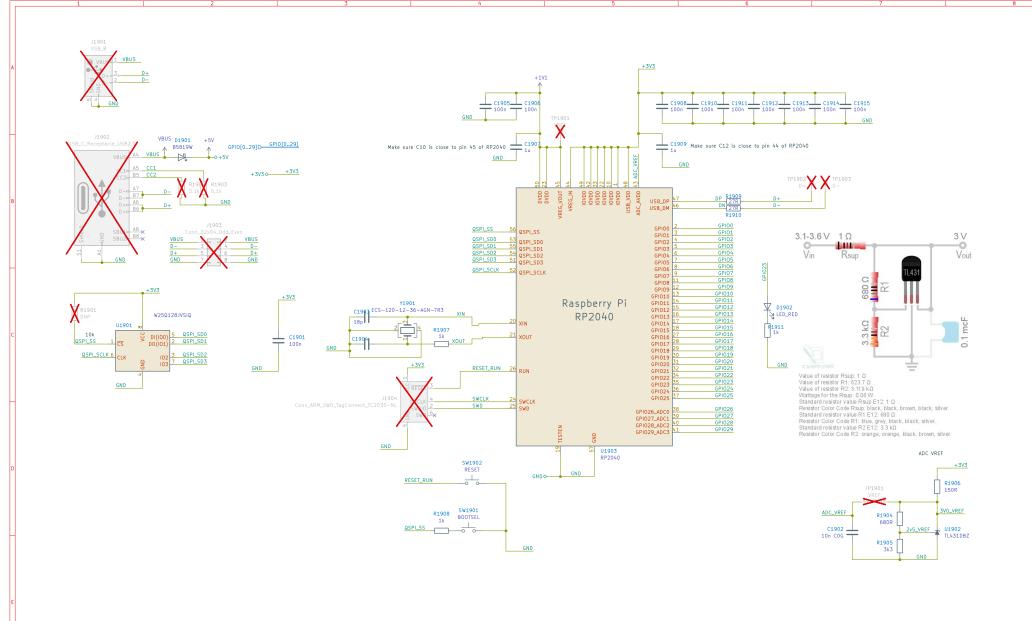
Chatelain Engineering, Bern — CH

Sheet: /rails\_power\_dc-dc-ldo-3v3\_100mA-test/ File: rails\_power\_dc-dc-ldo-3v3\_100mA-test.kicad\_sch

Title: xDuinoRail — LocDecoder — Development Kit

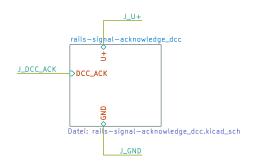
 Size: A4
 Date: 2024-10-09
 Rev: v0.2

 KiCad E.D.A. 8.0.5
 Id: 25/24



Output Driver FUNC[0..1]D FUNC[0..1] AMP\_FUNC[0..1] DAMP\_FUNC[0..1] GND GNDD-U+D-U2001 ULN2003A FUNC0 AMP\_FUNCO FUNC1 GND Chatelain Engineering, Bern — CH
Sheet: /acc-switch-driver-dual-test/acc-switch-driver/ File: acc-switch-driver.kicad\_sch Title: xDuinoRail — LocDecoder — Development Kit Size: A4 Date: 2024-10-09 Rev: v0.2 KiCad E.D.A. 8.0.5 ld: #/24







	Conn_02x03_0dd_Eve	n
J_DCC_ACK		J_DCC_ACK
J_U+	4 3	J_U+
J_GND	6 5	J_GND

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Sheet: /rails\_signal\_combined-test/rails\_signal\_acknowledge\_dcc-test/ File: rails\_signal\_acknowledge\_dcc-test.kicad\_sch

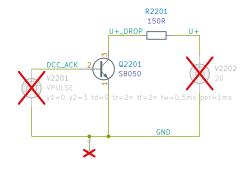
Title: xDuinoRail — LocDecoder — Development Kit

 Size: A4
 Date: 2024-10-09
 Rev: v0.2

 KiCad E.D.A. 8.0.5
 Id: #/24







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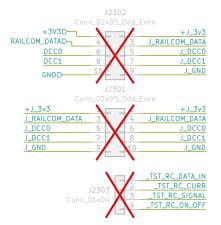
Title:

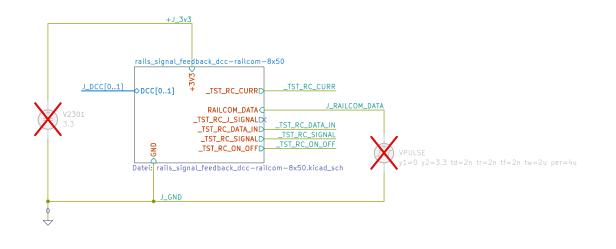
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 Rev:

 KiCad E.D.A. 8.0.5
 Id: #/24

### Testlayout — RailCom feedback generator







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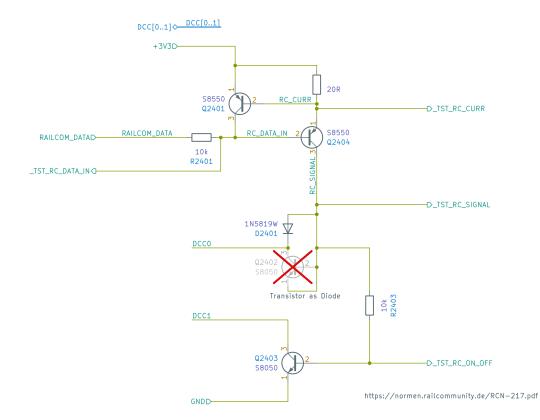
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Title: xDuinoRail — LocDecoder — Development Kit

 Size: A4
 Date: 2024–10–09
 Rev: v0.2

 KiCad E.D.A. 8.0.5
 Id: #/24

### RailCom feedback generator



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Sheet: /rails\_signal\_combined-test/rails\_signal\_feedback\_dcc-railcom-8x50\_test/rails\_signal\_feedbacFile: rails\_signal\_feedback\_dcc-railcom-8x50.kicad\_sch

Title: xDuinoRail — LocDecoder — Development Kit

 Size: A4
 Date: 2024-10-09
 Rev: v0.2

 KiCad E.D.A. 8.0.5
 Id: #/24