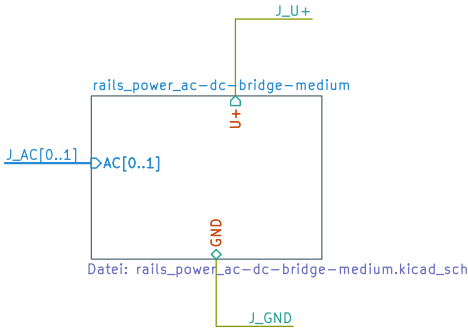
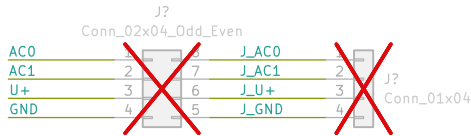
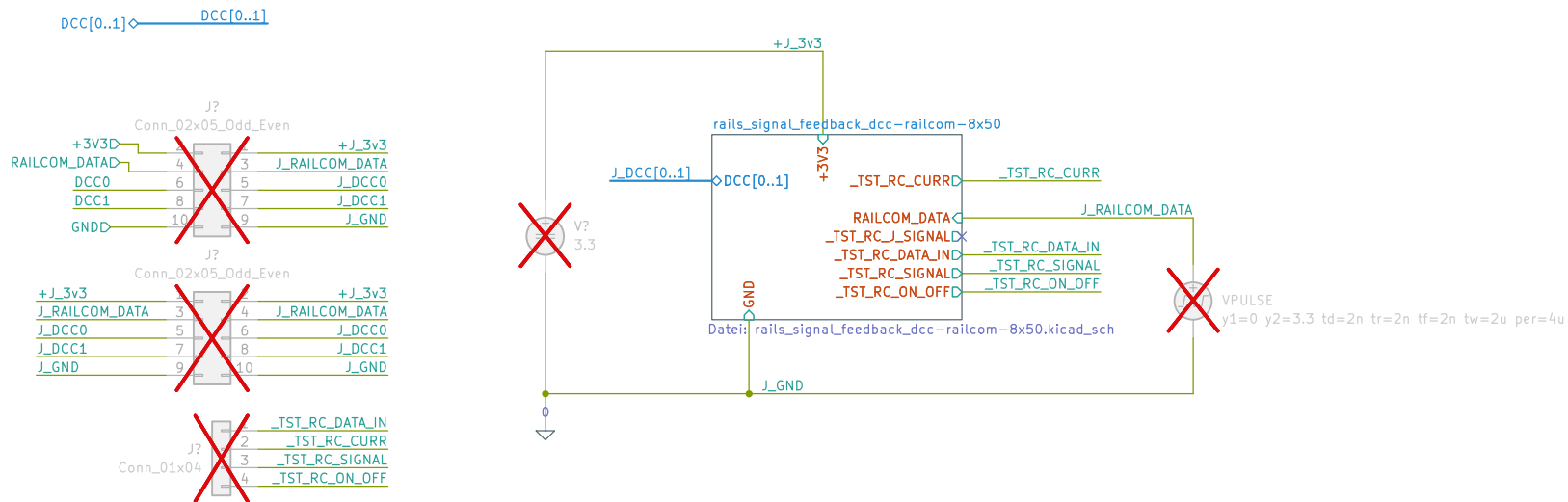


Bridge rectifier medium



# Testlayout – RailCom feedback generator



Chatelain Engineering, Bern – CH

Sheet: /rails\_signal\_feedback\_dcc-railcom-8x50\_test/

File: rails\_signal\_feedback\_dcc-railcom-8x50\_test.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

Size: A4

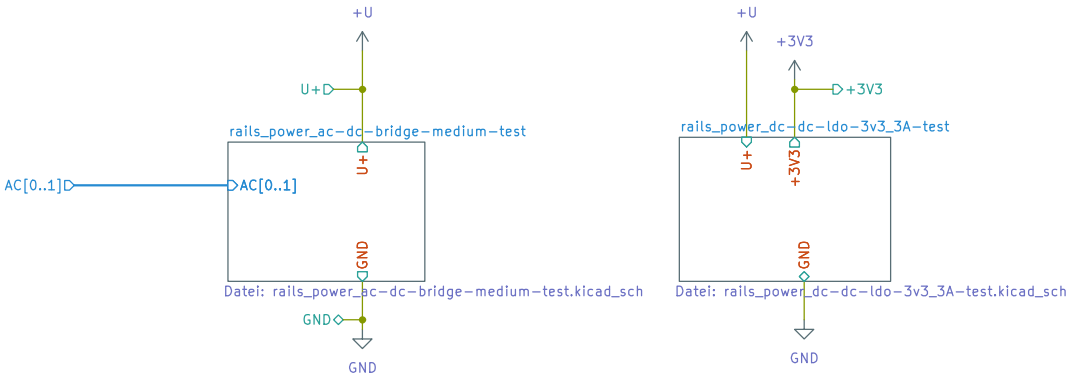
Date: 2024-10-09

Rev: v0.2

KiCad E.D.A. 8.0.5

Id: 3/27

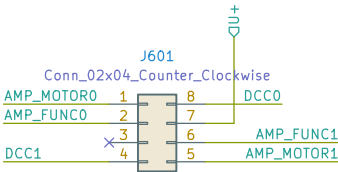
Bridge rectifier medium with LDO 3v3 / 100mA



# Bus interfaces

DCC[0..1]◇  
AMP\_MOTOR[0..1]▷  
AMP\_FUNC[0..1]▷

8-pin (NEM652)



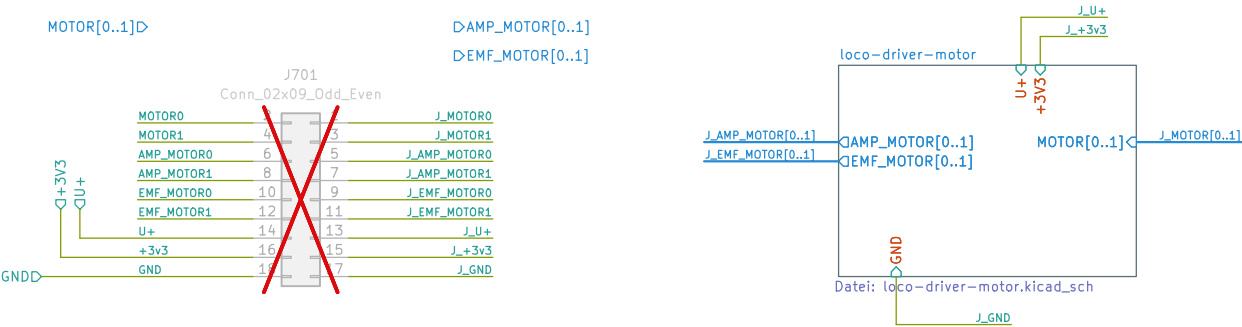
Chatelain Engineering, Bern – CH

Sheet: /loco-adapter-nem652-8-pin/  
File: loco-adapter-nem652-8-pin.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

Size: A4	Date: 2024-10-09	Rev: v0.2
KiCad E.D.A. 8.0.5		Id: 5/27

Motor Driver – test rig



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Sheet: /loco-driver-motor-test/  
File: loco-driver-motor-test.kicad\_sch

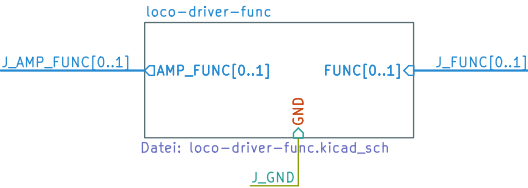
**Title: xDuinoRail – LocDecoder – Development Kit**

Size: A4  
KiCad E.D.A. 8.0.5

Date: 2024-10-09

Rev: v0.2  
Id: 6/27

Function output driver – test rig



J802  
Conn\_02x05\_Odd\_Even

FUNC0	3	4	J_FUNC0
FUNC1	5	6	J_FUNC1
AMP_FUNC0	7	8	J_AMP_FUNC0
AMP_FUNC1	9	10	J_AMP_FUNC1
GND			J_GND

J?  
Conn\_02x05\_Odd\_Even

J_FUNC0	3	4	J_FUNC0
J_FUNC1	5	6	J_FUNC1
J_AMP_FUNC0	7	8	J_AMP_FUNC0
J_AMP_FUNC1	9	10	J_AMP_FUNC1
J_GND			J_GND

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Sheet: /loco-driver-func-test/  
File: loco-driver-func-test.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

Size: A4

Date: 2024-10-09

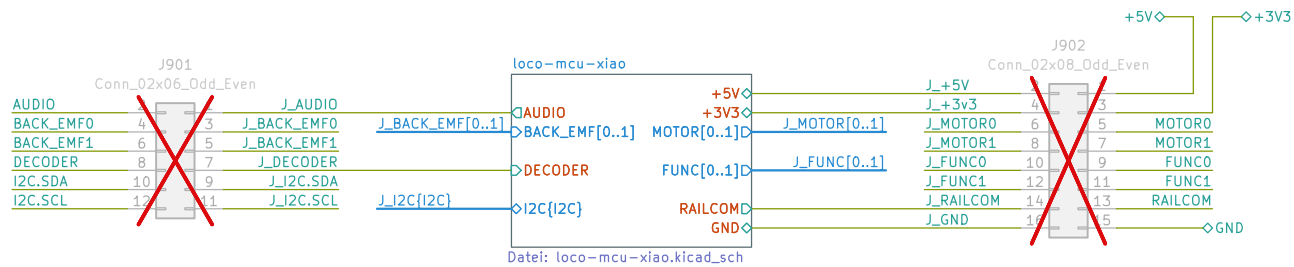
Rev: v0.2

KiCad E.D.A. 8.0.5

Id: 7/27



## XIAO Header (Decoder Pinout) – test rig



Chatelain Engineering, Bern – CH

Sheet: /loco-mcu-xiao-test/  
File: loco-mcu-xiao-test.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

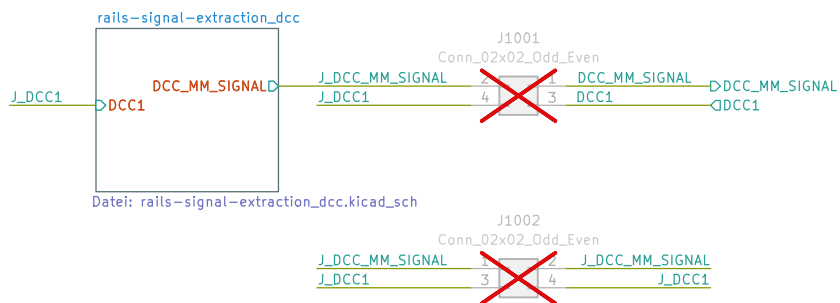
Size: A4  
KiCad E.D.A. 8.0.5

Date: 2024-10-09

Rev: v0.2  
Id: 8/27



Resistor (Use ONLY with on MCU pin protection)



Chatelain Engineering, Bern – CH

Sheet: /rails\_signalExtraction\_dcc-test/

File: rails\_signalExtraction\_dcc-test.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

Size: A4

Date: 2024-10-09

Rev: v0.2

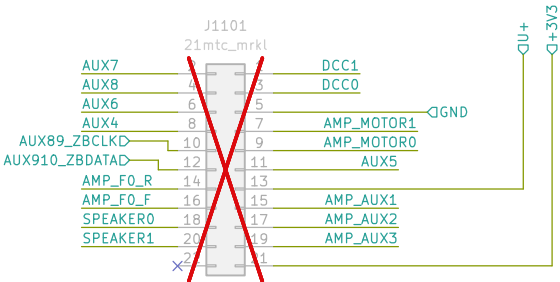
KiCad E.D.A. 8.0.5

Id: 9/27

# Bus interfaces



# 21mtc header (Märklin)



<https://normen.railcommunity.de/RCN-121.pdf>

Chatelain Engineering, Bern – CH

Sheet: /loco-adapter-maerklin-21mtc/  
File: loco-adapter-maerklin-21mtc.kicad\_sch

Title: xDuinoRail – LocDecoder – Development Kit

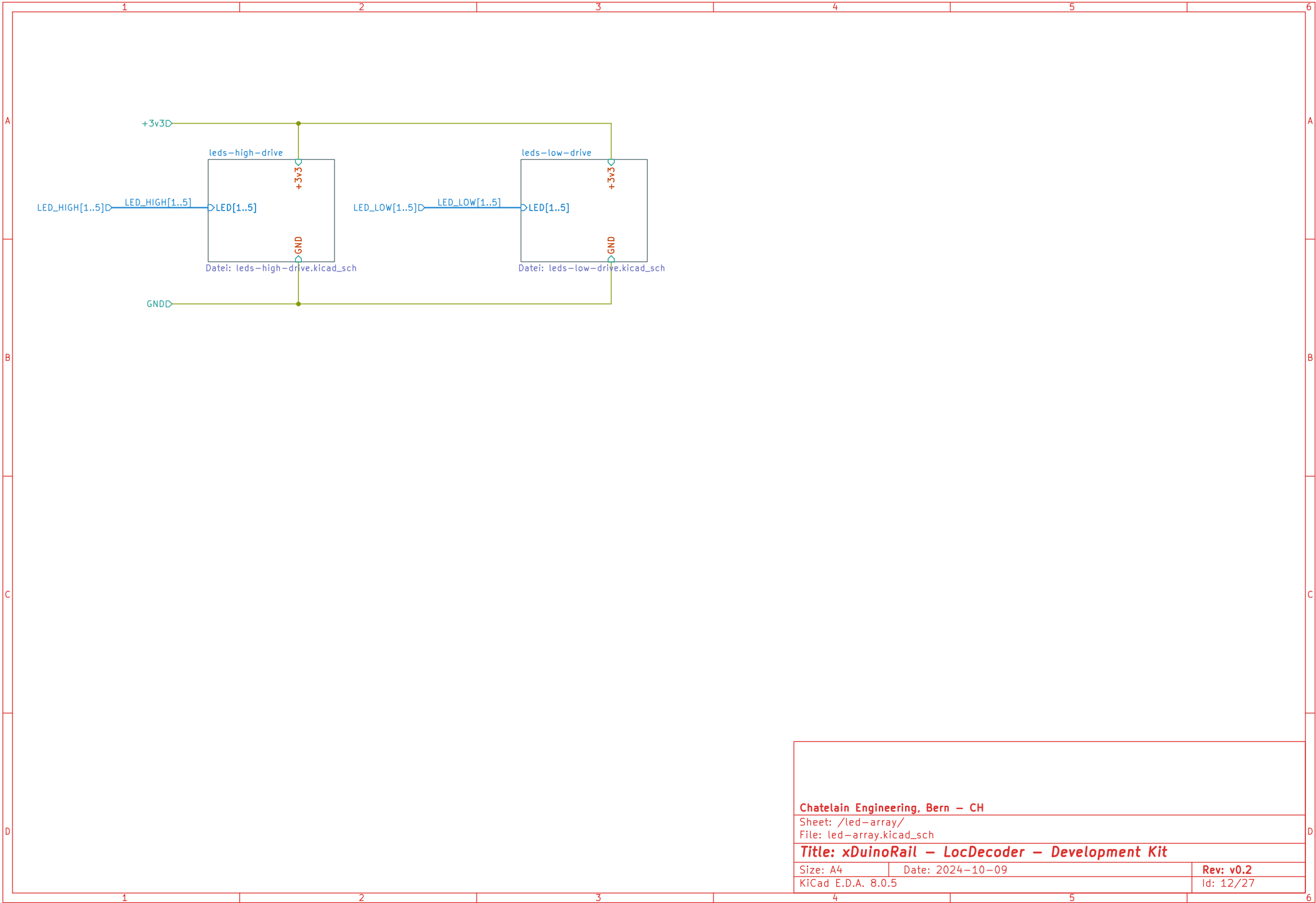
Size: A4

Date: 2024-10-09

Rev: v0.2

KiCad E.D.A. 8.0.5

Id: 11/27



Chatelain Engineering, Bern – CH

Sheet: /led-array/  
File: led-array.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

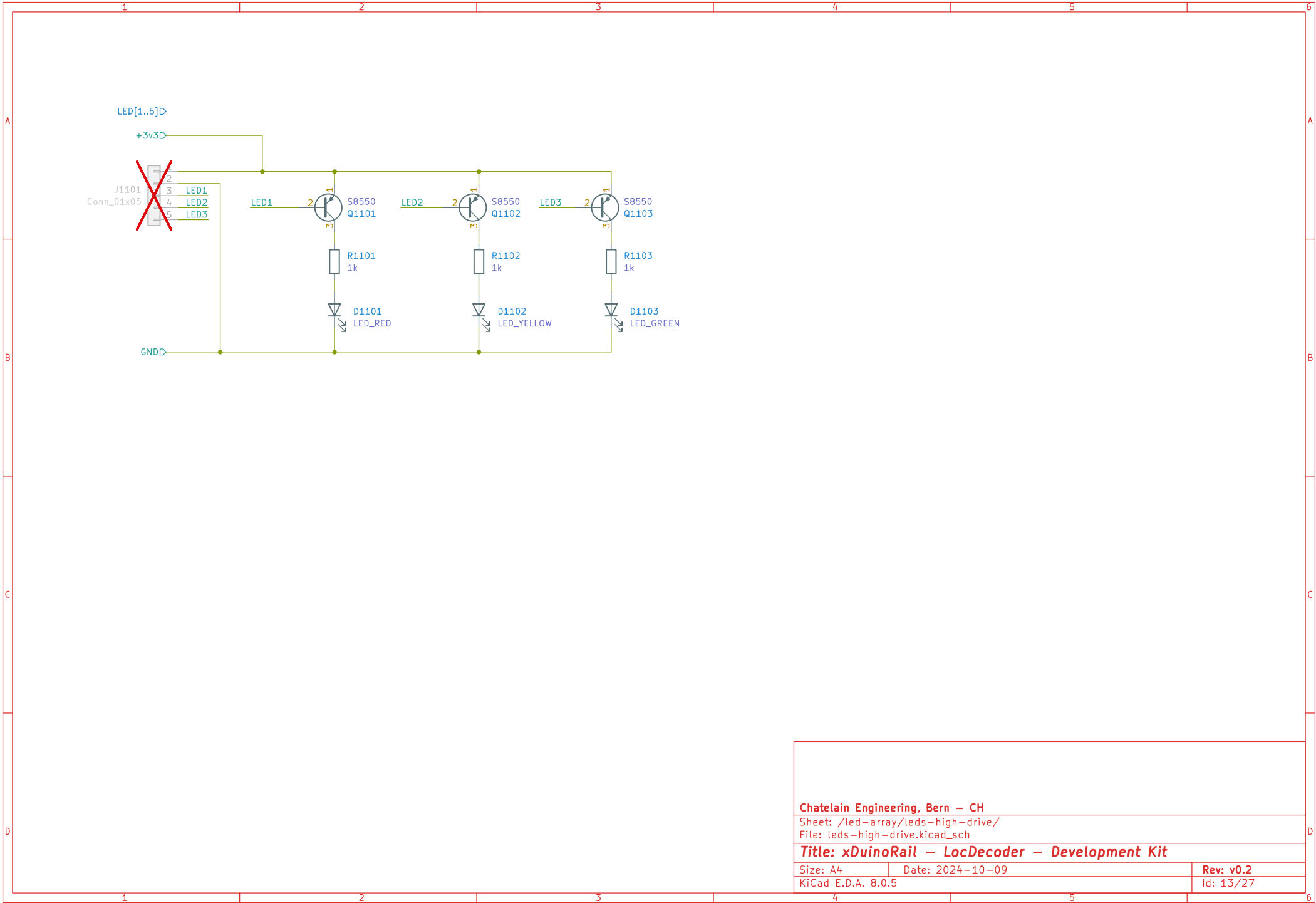
Size: A4

Date: 2024-10-09

Rev: v0.2

KiCad E.D.A. 8.0.5

Id: 12/27



Chatelain Engineering, Bern – CH

Sheet: /led-array/leds-high-drive/  
File: leds-high-drive.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

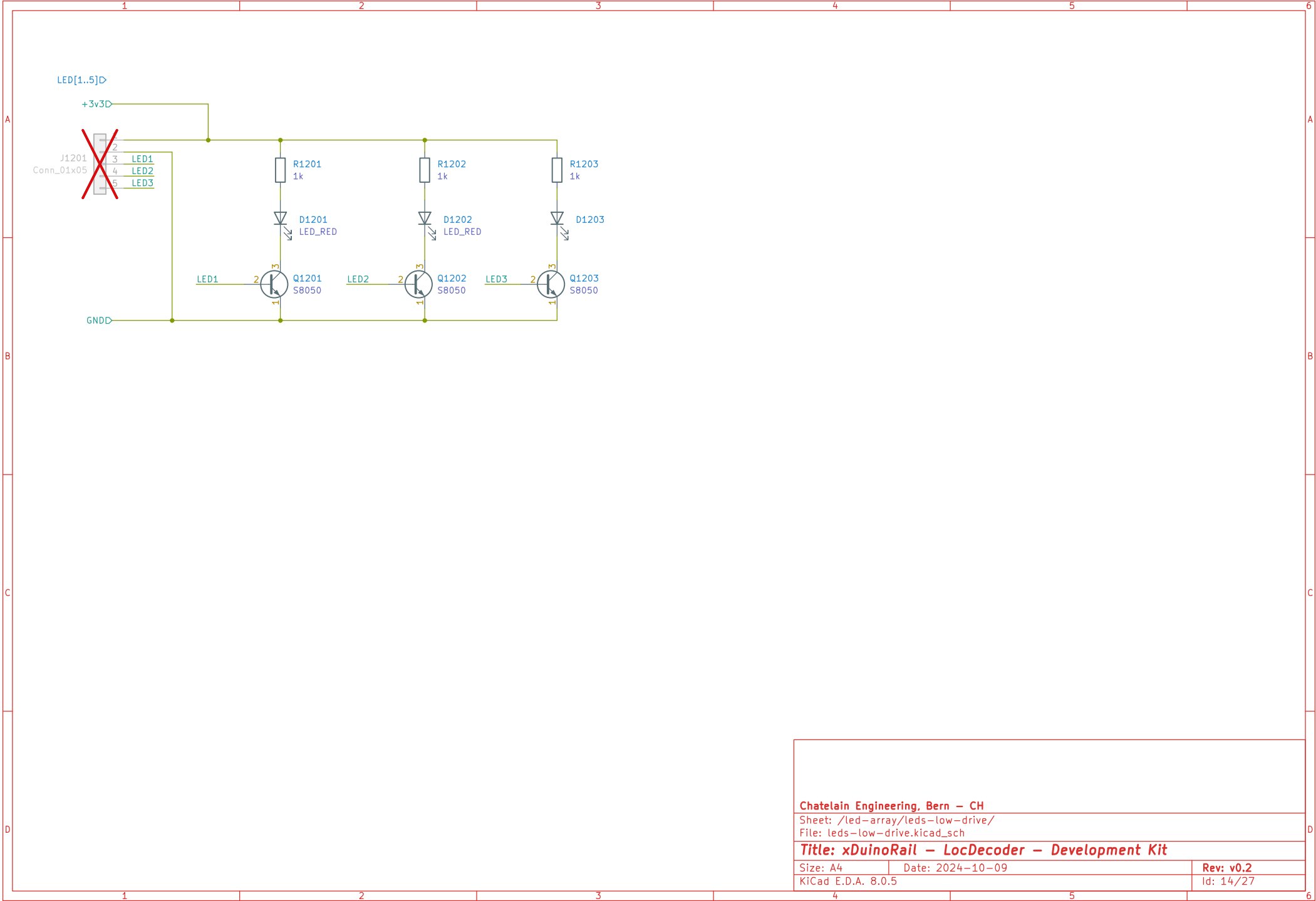
Size: A4

Date: 2024-10-09

Rev: v0.2

KiCad E.D.A. 8.0.5

Id: 13/27



Chatelain Engineering, Bern – CH

Sheet: /led-array/leds-low-drive/  
File: leds-low-drive.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

Size: A4

Date: 2024-10-09

Rev: v0.2

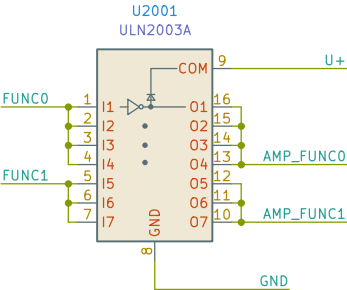
KiCad E.D.A. 8.0.5

Id: 14/27

Output Driver

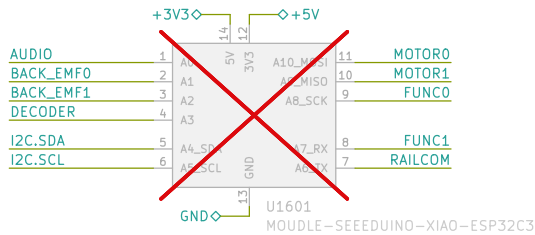
FUNC[0..1] → FUNC[0..1]    AMP\_FUNC[0..1] → AMP\_FUNC[0..1]

GND → GND  
U+ → U+





XIAO Header (Decoder Pinout)



Chatelain Engineering, Bern – CH

Sheet: /loco-mcu-xiao-test/loco-mcu-xiao/

File: loco-mcu-xiao.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

Size: A4

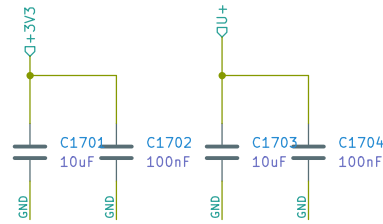
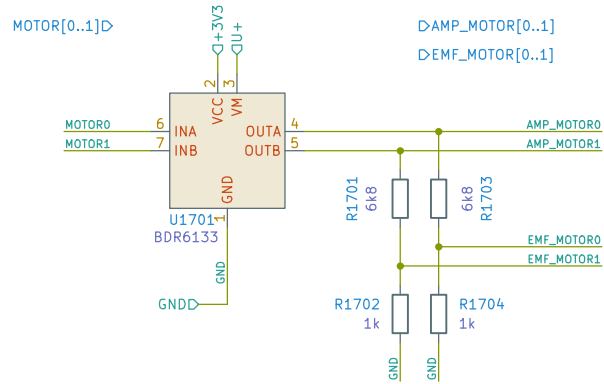
Date: 2024-10-09

Rev: v0.2

KiCad E.D.A. 8.0.5

Id: 18/27

## Motor Driver



Chatelain Engineering, Bern – CH

Sheet: /loco-driver-motor-test/loco-driver-motor/  
File: loco-driver-motor.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

Size: A4

Date: 2024-10-09

Rev: v0.2

KiCad E.D.A. 8.0.5

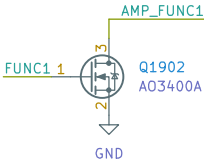
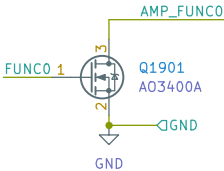
Id: 19/27





Output Driver

FUNC[0..1] → FUNC[0..1]    AMP\_FUNC[0..1] → AMP\_FUNC[0..1]

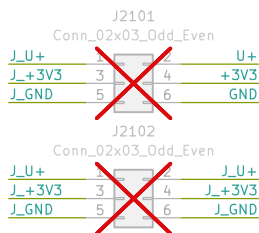
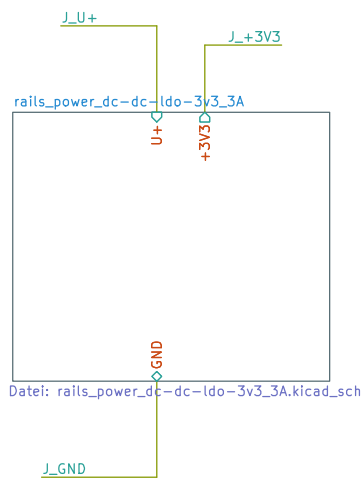


Chatelain Engineering, Bern – CH

Sheet: /loco-driver-func-test/loco-driver-func/  
File: loco-driver-func.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

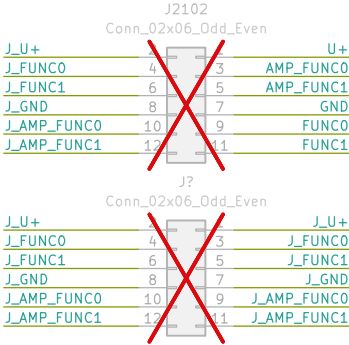
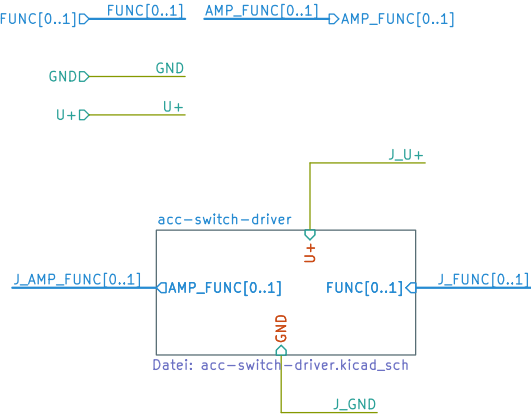
Size: A4	Date: 2024-10-09	Rev: v0.2
KiCad E.D.A. 8.0.5		Id: 21/27



Sheet: /rails\_power\_ac-dc-medium-3v3\_100mA/rails\_power\_dc-dc-ldo-3v3\_3A-test/  
File: rails\_power\_dc-dc-ldo-3v3\_3A-test.kicad\_sch

Title:		
Size: A4	Date:	
KiCad E.D.A. 8.0.5	Rev: Id: 24/27	

Output Driver – test rig



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Sheet: /acc-switch-driver-test/  
File: acc-switch-driver-test.kicad\_sch

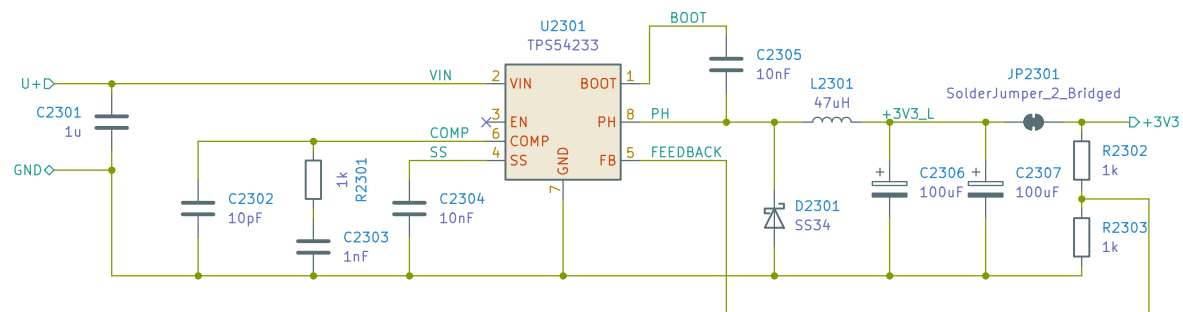
Title: xDuinoRail – LocDecoder – Development Kit

Size: A4  
KiCad E.D.A. 8.0.5

Date: 2024-10-09

Rev: v0.2  
Id: 24/27

# DC-DC buck convertor 3.3V



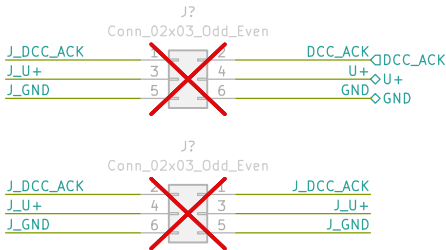
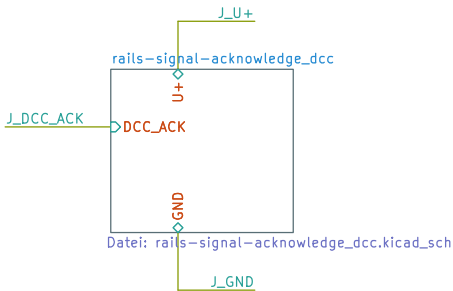
Chatelain Engineering, Bern – CH

Sheet: /rails\_power\_ac-dc-medium-3v3\_100mA/rails\_power\_dc-dc-lto-3v3\_3A-test/rails\_power\_dc-dc-lto-3v3\_3A.kicad\_sch  
File: rails\_power\_dc-dc-lto-3v3\_3A.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

Size: A4	Date: 2024-10-09	Rev: v0.2
KiCad E.D.A. 8.0.5		Id: 25/27

DCC ACK Signal for programming



Chatelain Engineering, Bern – CH

Sheet: /rails\_signalAcknowledge\_dcc-test/  
File: rails\_signalAcknowledge\_dcc-test.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

Size: A4

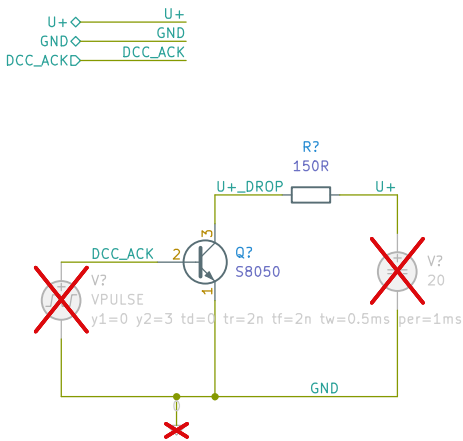
Date: 2024-10-09

Rev: v0.2

KiCad E.D.A. 8.0.5

Id: 26/27

Resistor (Use ONLY with on MCU pin protection)



Sheet: /rails\_signalAcknowledge\_dcc-test/rails-signal-acknowledge\_dcc/  
File: rails-signal-acknowledge\_dcc.kicad\_sch

**Title:**

Size: A4

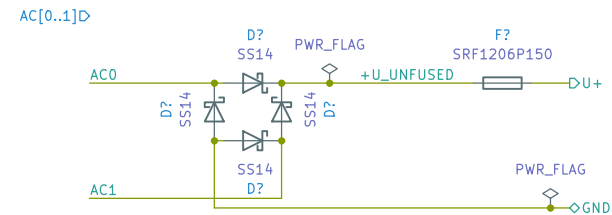
Date:

**Rev:**

KiCad E.D.A. 8.0.5

Id: 23/27

Bridge rectifier medium



Chatelain Engineering, Bern – CH

Sheet: /rails\_power\_ac-dc-medium-3v3\_100mA/rails\_power\_ac-dc-bridge-medium-test/rails\_power\_ac-dc-bridge-medium.kicad\_sch

Title: xDuinoRail – LocDecoder – Development Kit

Size: A4

Date: 2024-10-09

Rev: v0.2

KiCad E.D.A. 8.0.5

Id: 24/27

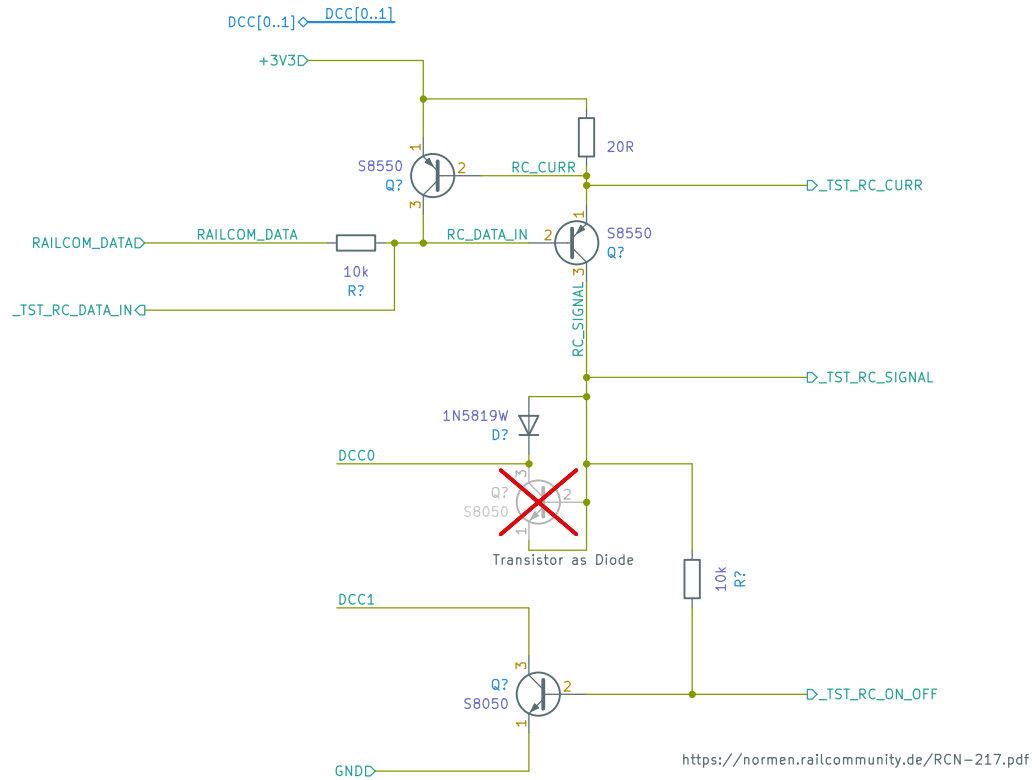


Resistor (Use ONLY with on MCU pin protection)



Sheet: /rails_signal_extraction_dcc-test/rails-signal-extraction_dcc/ File: rails-signal-extraction_dcc.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.5	Id: 25/27	

# RailCom feedback generator



Chatelain Engineering, Bern – CH

Sheet: /rails\_signal\_feedback\_dcc-railcom-8x50\_test/rails\_signal\_feedback\_dcc-railcom-8x50/  
File: rails\_signal\_feedback\_dcc-railcom-8x50.kicad\_sch

**Title: xDuinoRail – LocDecoder – Development Kit**

Size: A4	Date: 2024-10-09	Rev: v0.2
KiCad E.D.A. 8.0.5		Id: 26/27

