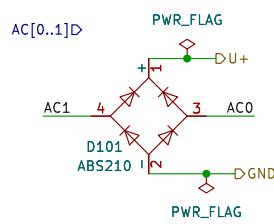


Bridge rectifier 3XS



Chatelain Engineering, Bern – CH

Sheet: /rails_power_ac_dc_bridge_medium_3s/
File: rails_power_ac_dc_bridge_medium_3s.kicad_sch

Title: xDuinoRail – RailcomDecoder – Development Kit "Hermes"

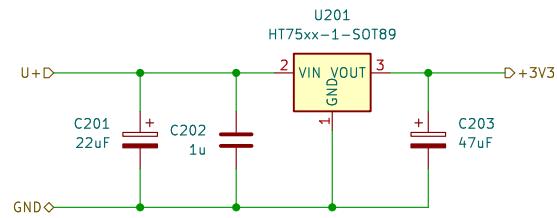
Size: A4 Date: 2026-01-11

KiCad E.D.A. 8.0.8

Rev: v1.0a

Id: 2/11

DC-DC voltage regulator 3.3V



Chatelain Engineering, Bern – CH

Sheet: /rails_power_dc_dc_ldo_3v3_100mA/
File: rails_power_dc_dc_ldo_3v3_100mA.kicad_sch

Title: xDuinoRail – RailcomDecoder – Development Kit "Hermes"

Size: A4 Date: 2026-01-11

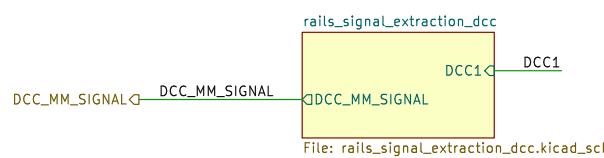
KiCad E.D.A. 8.0.8

Rev: v1.0a

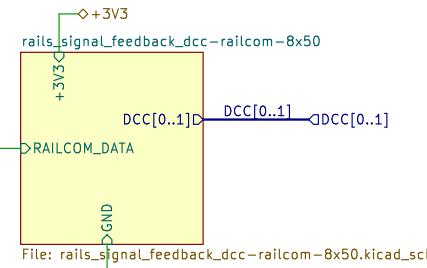
Id: 3/11

DCC/RailCom Up- & Downlinks

A

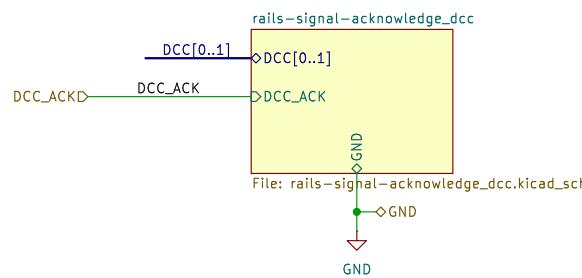


RAILCOM_DATA



File: rails_signal_feedback_dcc_railcom_8x50.kicad_sch

B



C

D

1 2 3 4 5 6

Resistor (Use ONLY with on MCU pin protection)



A

A

B

B

C

C

D

D

Chatelain Engineering, Bern – CH

Sheet: /rails_signal_combined_medium/rails_signal_extraction_dcc/
File: rails_signal_extraction_dcc.kicad_sch

Title: xDuinoRail – RailcomDecoder – Development Kit "Hermes"

Size: A4 Date: 2026-01-11

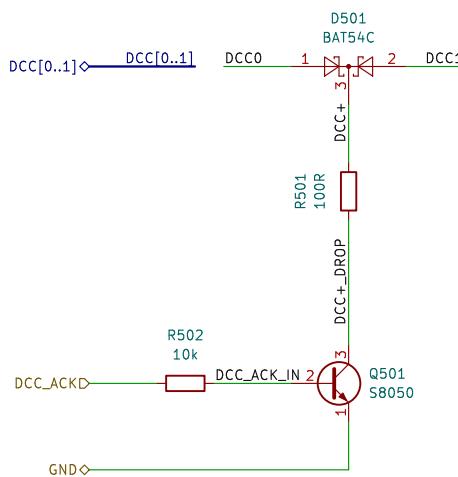
KiCad E.D.A. 8.0.8

Rev: v1.0a

Id: 5/11

1 2 3 4 5 6

DCC-ACK



Chatelain Engineering, Bern – CH

Sheet: /rails_signal_combined-medium/rails-signal-acknowledge_dcc/
File: rails-signal-acknowledge_dcc.kicad_sch

Title: xDuinoRail – RailcomDecoder – Development Kit "Hermes"

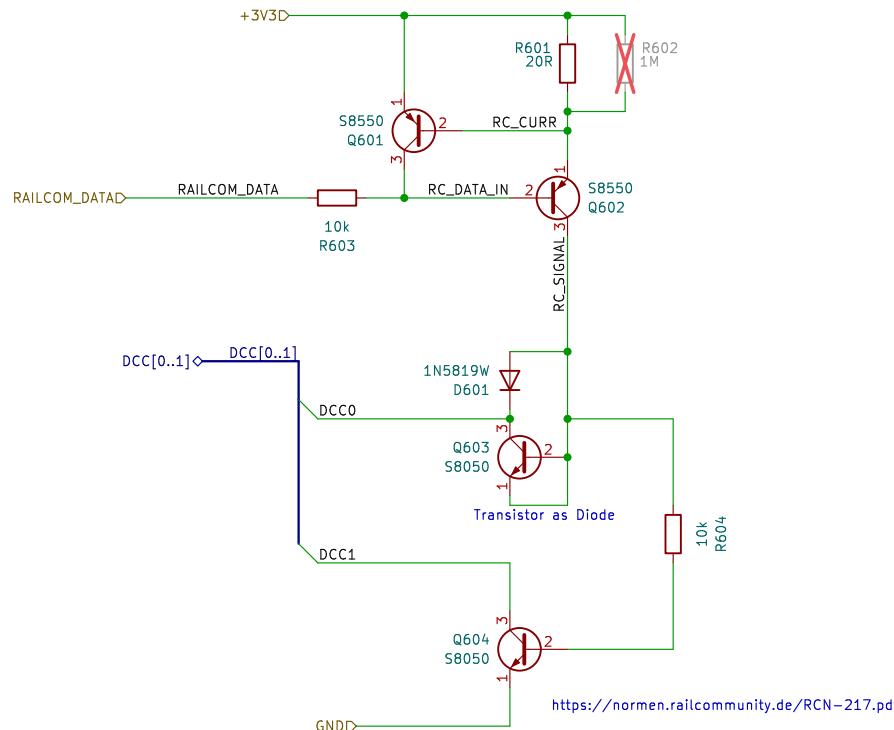
Size: A4 Date: 2026-01-11

KiCad E.D.A. 8.0.8

Rev: v1.0a

Id: 6/11

RailCom feedback generator



<https://normen.railcommunity.de/RCN-217.pdf>

Chatelain Engineering, Bern – CH

Sheet: /rails_signal_combined-medium/rails_signal_feedback_dcc-railcom-8x50/
File: rails_signal_feedback_dcc-railcom-8x50.kicad_sch

Title: xDuinoRail – RailcomDecoder – Development Kit "Hermes"

Size: A4 Date: 2026-01-11

KiCad E.D.A. 8.0.8

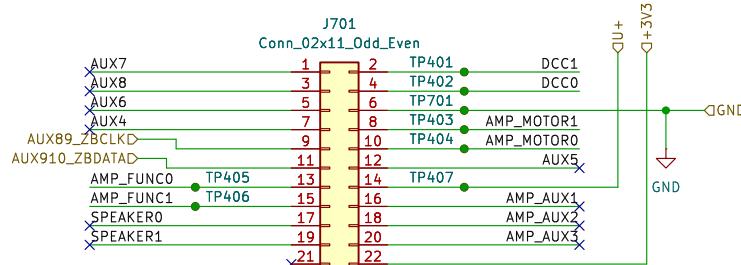
Rev: v1.0a

Id: 7/11

Bus interfaces

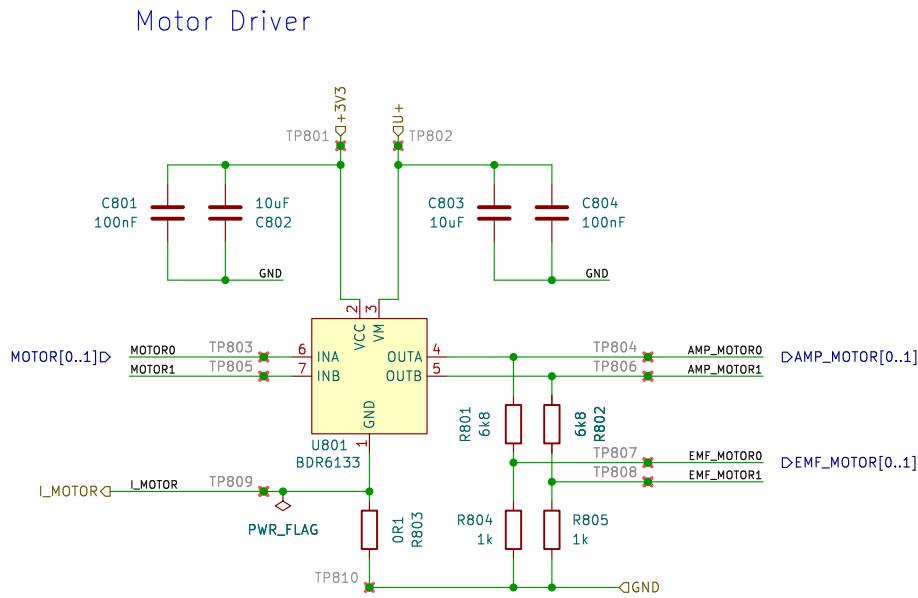
DCC[0..1]◊
AMP_MOTOR[0..1]◊
SPEAKER[0..1]◊
AMP_FUNC[0..1]◊
AUX[1..8]◊
AMP_AUX[1..7]◊

21mtc header (NEM660)



<https://normen.railcommunity.de/RCN-121.pdf>

Hallsensor 1	1	22		Schiene rechts
Hallsensor 2	2	21		Schiene links
Aux 6 (L)	3	20		DC (-) Funktionen GND
Aux 4 (L)	4	19		Motor rechts (+)
Zugbus Clock	5	18		Motor links (-)
Zugbus Data	6	17		Aux 5 (L) / Motor (3)
Licht hinten	7	16		DC (+) Funktionen
Licht vorne	8	15		Aux. 1
Lautsprecher 2	9	14		Aux. 2
Lautsprecher 1	10	13		Aux. 3 (L)
Verdrehschutz	11	12		Vcc +5V Prozessor



Chatelain Engineering, Bern - CH

Sheet: /loco-driver-motor/

File: loco-driver-motor.kicad_sch

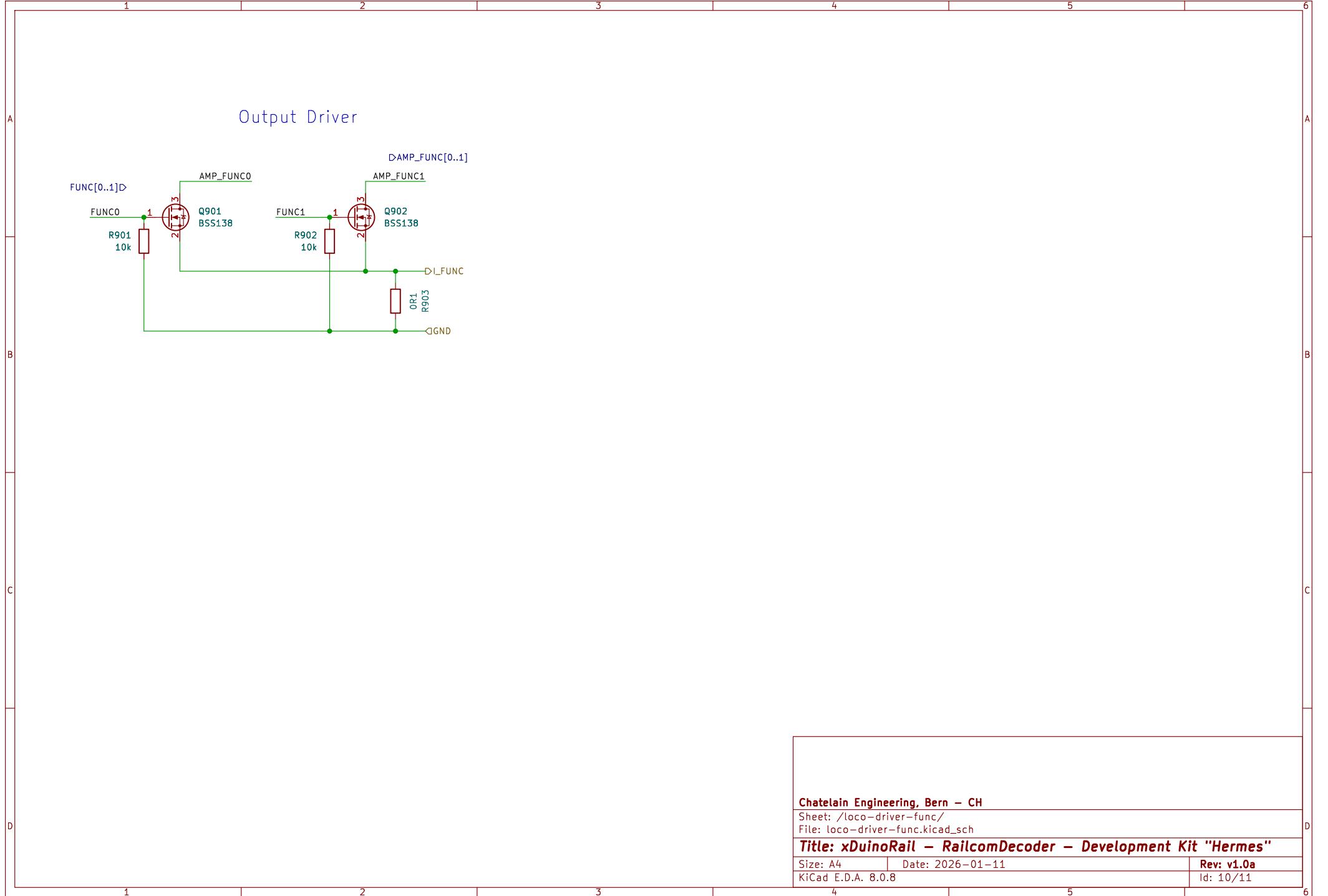
Title: xDuinoRail – RailcomDecoder – Development Kit "Hermes"

Size: A4 Date: 2026-01-11

Rev: v1.0a

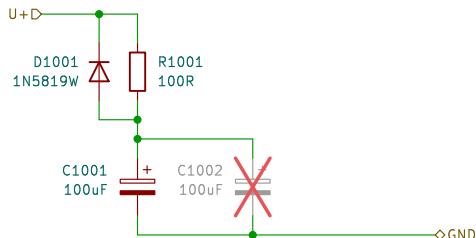
Id: 9/11

1 2 3 4 5 6



1 2 3 4 5 6

DC storage capacitors



Chatelain Engineering, Bern – CH

Sheet: /rails_power-buffer/

File: rails_power-buffer.kicad_sch

Title: xDuinoRail – RailcomDecoder – Development Kit "Hermes"

Size: A4 Date: 2026-01-11

KiCad E.D.A. 8.0.8

Rev: v1.0a

Id: 11/11

