

Exam - Computer Architecture Unit I [06/02/2025] (B)

Surname: _____ Name: _____

Student ID Number (Matricola): _____

DSA Students should solve only the first 4 exercises (grade will be scaled accordingly)

Exercise 1 (7 points) Design a sequential circuit with two inputs x_1, x_0 , that encode the characters T, O, S as shown on the table on the right. The circuit has 2 outputs z_1 and z_0 . The circuit outputs $z_1=1$ when it receives the sequence TOST, and $z_0=1$ when it receives the sequence TOT. Overlaps are allowed. Draw the FSM diagram, the state transition table, and the circuit.

x_1, x_0	character
00	T
01	O
1-	S

Exercise 2 (4 points) Design a circuit that computes how many days are in a given month. The month is specified by a 4-bit input $a_3a_2a_1a_0$. For example, if the inputs are (0001), the month is January, and if the inputs are (1100), the month is December. The output of the circuit $Y_1 Y_0$, must be equal to 00 only when the month specified on the input has 31 days, they must be equal to 01 when the input month has 30 days, and they must be equal to 10 when the input month has 28 days. The outputs must be equal to 11 in the remaining cases. Write down the truth table, and the minimal SOP and POS forms.

days	a_3	a_2	a_1	a_0	Y_1	Y_0
//	0	0	0	0	1	1
31	0	0	0	1	0	0
28	0	0	1	0	1	0
31	0	0	1	1	0	0
30	0	1	0	0	0	1
31	0	1	0	1	0	0
30	0	1	1	0	0	1
31	0	1	1	1	0	0
28	1	0	0	0	0	0
30	1	0	0	1	0	1
31	1	0	1	0	0	0
30	1	0	1	1	0	1
31	1	1	0	0	0	0
//	1	1	0	1	1	1
//	1	1	1	0	1	1
//	1	1	1	1	1	1

Y_1	a_3a_2	00	01	11	10
00	00	1	0	0	0
01	01	0	0	1	0
11	11	0	0	1	0
10	10	1	0	1	0

$$SOP = \bar{a}_3 \bar{a}_2 \bar{a}_0 + a_3 a_2 a_0 + a_3 a_2 a_1$$

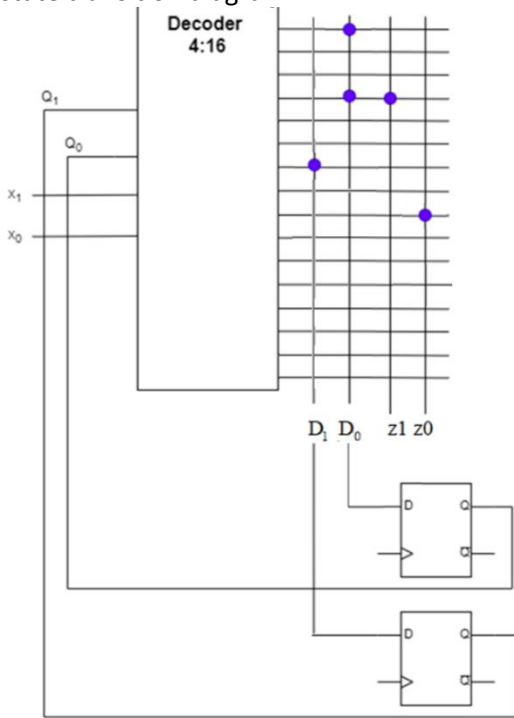
$$POS = (a_3 + \bar{a}_0)(a_3 + \bar{a}_2)(\bar{a}_3 + a_2)(a_3 + a_1 + a_0)$$

Y_0	a_3a_2	00	01	11	10
00	00	1	1	0	0
01	01	0	0	1	1
11	11	0	0	1	1
10	10	0	1	1	0

$$SOP = \bar{a}_3 \bar{a}_1 \bar{a}_0 + a_2 a_1 \bar{a}_0 + a_3 a_0$$

$$POS = (a_3 + \bar{a}_0)(\bar{a}_3 + a_1 + a_0)(a_2 + \bar{a}_1 + a_0)$$

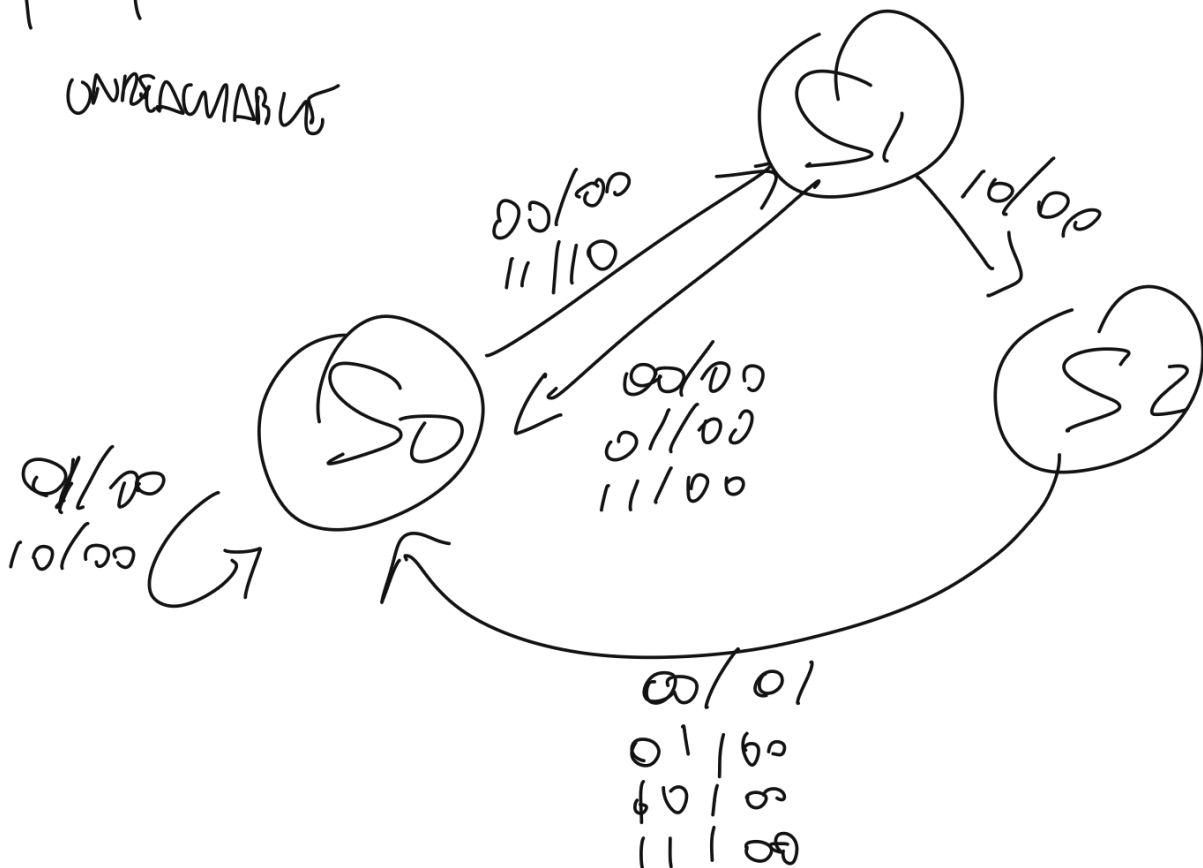
Exercise 3 (5 points) Analyze the sequential circuit in the figure below. Write down the next state table and the state transition diagram.



Q_1	Q_0	x_1	x_0	D_1	D_0	z_1	z_0
0	0	0	0	0	1	0	0
0	0	0	1	0	0	0	0
0	0	1	0	0	0	0	0
0	0	1	1	0	1	1	0
0	1	0	0	0	0	0	0
0	1	0	1	0	0	0	0
0	1	1	0	1	0	0	0
0	1	1	1	0	0	0	0
1	0	0	0	0	0	0	1
1	0	0	1	0	0	0	0
1	0	1	0	0	0	0	0
1	0	1	1	0	0	0	0
1	1	0	0	0	0	0	0
1	1	0	1	0	0	0	0
1	1	1	0	0	0	0	0
1	1	1	1	0	0	0	0

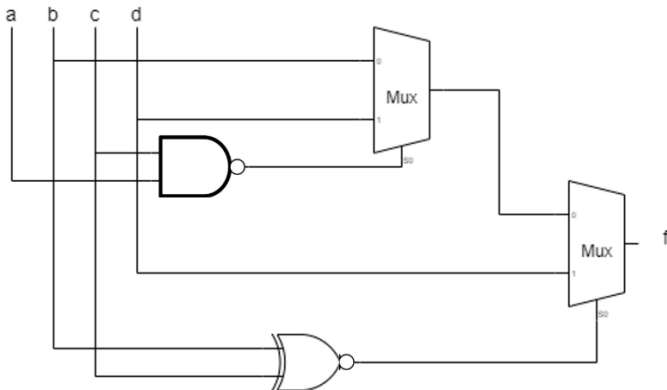
Q_1	Q_0	S
0	0	S_0
0	1	S_1
1	0	S_2
1	1	

11 UNREACHABLE



Exercise 4 (6 points)

- Consider the circuit depicted below and write down the boolean expression for the function f
- Transform the expression, using Boole's algebra axioms and theorems, to SOP form
- Write down the truth table for f
- Write down the minimal POS form for f



$$\begin{aligned}
 f &= (\overline{B \oplus C})D + (B \oplus C)[\overline{A}C D + A C B] = \\
 &= (\overline{B C} + \overline{B \overline{C}})D + (B \overline{C} + \overline{B} C)(\overline{A} + \overline{C})D + A B C = \\
 &= (\overline{B C} + \overline{B \overline{C}})D + (B \overline{C} + \overline{B} C)(\overline{A} D + \overline{C} D + A B C) =
 \end{aligned}$$

$$= (B + \overline{C})(\overline{B} + C)D + \overline{A} B \overline{C} D + \overline{A} \overline{B} C D + B \overline{C} D =$$

$$= B C D + \overline{B} \overline{C} D + \overline{A} B \overline{C} D + \overline{A} \overline{B} C D + B \overline{C} D =$$

$$= B D (C + \overline{A} \overline{C} + \overline{C}) + \overline{B} \overline{C} D + \overline{A} \overline{B} C D =$$

$$= B D + \overline{B} \overline{C} D + \overline{A} \overline{B} C D = D(B + \overline{B} \overline{C}) + \overline{A} \overline{B} C D =$$

$$= B D + \overline{C} D + \overline{A} \overline{B} C D = B D + D(\overline{C} + \overline{A} \overline{B} C) =$$

$$= B D + \overline{C} D + \overline{A} \overline{B} D = D(B + \overline{A} \overline{B}) + \overline{C} D =$$

$$= B D + \overline{A} D + \overline{C} D$$

A	B	C	D	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	0
1	0	1	1	0
1	1	0	0	0
1	1	0	1	1
1	1	1	0	0
1	1	1	1	1


f	AB	00	01	11	10
00	0	0	0	0	0
01	1	1	1	1	1
11	1	1	1	0	0
10	1	0	0	0	0

$$POS = (D)(\overline{A} + B + \overline{C})$$

Exercise 5 (4 points) Convert the base 10 number $X = -304$ in the IEEE 754 half-precision format and convert those 16 bits to a base-16 number. Then, convert the base-16 number $Y = 5A00$ to a binary string, and interpret that string as an IEEE 754 half-precision number. Compute $X+Y$ in IEEE 754 half-precision, and convert the resulting 16 bits to a base-16 number.

$$304_{10} = 10011000_2 = 1.0011000 \cdot 2^8$$

X $\left\{ \begin{array}{l} \text{Sign} = 1 \\ \text{b. exp.} = 8 + 15 = 23 = 1011_2 \\ m = 0011000000000000 \end{array} \right.$

$x =$ 

$V = 5A00 = 0101\ 1010\ 0000\ 0000$

Diagram illustrating the bit fields of the value 5A00 (hexadecimal) in IEEE 754 single-precision floating-point format:

- Sign: 0
- Exponent: 101 (3 bits)
- Mantissa: 10100000 (7 bits)

Annotation: $= 22 \rightarrow \text{exp} = 7$

$$Y = +2^7 \cdot 1.1 = 0.11 \cdot 2^8 = 11000000_2 = 192_{10}$$

1. 0011 $\xrightarrow{2^{\text{s}} \text{ CPL.}}$
$$\begin{array}{r} 10.1101 + \\ 00.1100 = \\ \hline 11.1001 \end{array}$$
 $\xrightarrow{2^{\text{s}} \text{ CPL.}}$ $00.0111 \cdot 2^8 =$

$$= 1.11 \cdot 2^6$$

b. $\exp = 64, 15 =$

21-10/01

$x + y =$

5 b.e.				m											
1	0	1	0	1	1	0	0	0	0	0	0	0	0	0	0
D				7				0				0			

Exercise 6 (4 points) Given the expression $f = (\bar{x} + \overline{y(y + \bar{z}\bar{w})}) \oplus (\bar{x} + zw)$ simplify it and bring it to normal SOP form. Write the canonical form for f and implement f using NAND only.

CANONICAL: $xyzw + x\bar{y}\bar{z}\bar{w} + x\bar{y}\bar{z}w + x\bar{y}z\bar{w}$

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