Stream Ciphers: Striving for Randomness

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1 Introduction

Random numbers are an important component of modern cryptography. In the case of encryption, they reduce redundancies in encrypted plaintexts, resulting in more secure transmission. [1, p. 107] The underlying issue is the difficulty of generating random numbers. In his paper *Various Techniques Used in Connection With Random Digits*, John von Neumann already warned in 1951 of this non-trivial task: "Any one who considers arithmetical methods of producing random digits is, of course, in a state of sin." [2, p. 36] A cryptographic system that addresses this challenge is stream ciphers. The generation of random numbers has a central function in this concept. Stream ciphers are used if one symbol is to be encrypted or decrypted per time unit while using minimum resources. [3, p. 191]

This paper presents efforts to approximate randomness with stream ciphers. In particular, the difficulty of this attempt will be illustrated.

Initially, the first chapter introduces the central idea of stream ciphers. In the second chapter, the most traditional approach for constructing stream ciphers with shift registers is discussed mathematically. Expanding on this, the third chapter will highlight the difficulties associated with this approach. Efforts to improve this idea and alternative solutions are analyzed in the fourth chapter. Finally, a recent competition is presented in the fifth chapter, aimed at finding out the best of all participating stream ciphers in order to eliminate the previous difficulties.

2 The Idea of Stream Ciphers

Stream ciphers are one of two major symmetric encryption methods. They serve a different purpose than block ciphers, the second symmetric encryption method. The primary intent is to encrypt and decrypt messages approximately synchronously between the sender and receiver of a message. To achieve this, the message is not first divided into blocks and pre-processed before it is ciphered, as is the case with block ciphers. Instead, stream ciphers encrypt or decrypt the plaintext or ciphertext directly. [4, p. 223]

A prerequisite for this is a theoretically infinite, ideally true binary random sequence. To encrypt the data, the sender combines the plaintext bitwise exclusive-or (XOR) with the random sequence. The recipient decrypts the ciphertext by also combining it bitwise exclusive-or with the same random sequence used by the sender.

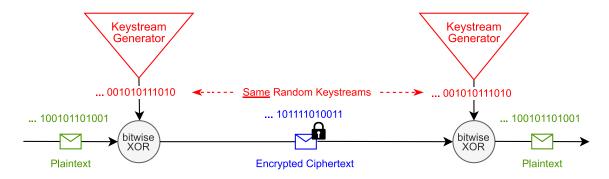


Figure 1: Stream cipher as a symmetric encryption method, based on [4, p. 232]

This only works if both communication partners have the same random sequence. This is a characteristic of symmetric encryption. [5, pp. 319-320] This problem could be trivially solved by the sender first generating a true random sequence of sufficient length and transmitting it to the recipient. However, since this key itself can be interpreted as a plaintext stream, the same problem is faced again.

2.1 "True Pseudorandomness"

The central question that arises is: How can truly random numbers be generated by means of a computer, based on a short key? [6, p. 53] Without starting a philosophical discussion, it is necessary to define what characterizes a truly random bit sequence.

This can be described using an analogy to a Laplacean experiment, like the sequences of fair coin tosses: Within this sequence, the values 0 and 1 each occur with probability 0.5. In addition, there is no way to derive information about the rest of the sequence from knowledge of an arbitrarily long initial piece of the sequence. To predict the next bit, an attacker must thus have no better chance of success than 0.5. To achieve equal distribution of the bits, the experiment must be conducted theoretically for a long time. Combining a given a message M with this truly random sequence XOR results in a truly random ciphertext C. Based on this information, a so-called *perfect cipher system* is characterized by the fact that the a priori probability P(M) is equal to the a posteriori probability $P(M \mid C)$ resulting in $P(M \mid C) = P(M)$. [7, pp. 52-23]

The real question is in fact: Can a computer generate numbers that only look truly random? Due to the determinism of a PC, which can be represented as a finite state machine, truly random numbers can never be generated. It is only possible to generate deterministic, so-called *pseudorandom numbers* (PRN). After the input of one or more initialization numbers, a *pseudorandom number generator* (PRNG) generates this pseudorandom number sequence. A deterministic inner state is used for this purpose. [7, pp. 195-196] In the following chapters it will be shown that a stream cipher can be interpreted as a PRNG according to these criteria. As can be seen in Figure 1, a stream cipher has this inner state of the PRNG. Initially, it is filled by the short key that both communication partners possess. A stream cipher uses the key to generate and output the pseudorandom *keystream* or *running-key* bitwise or bytewise. [4, p. 233]

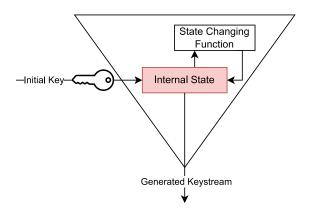


Figure 2: A stream cipher generates the keystream with the help of the initial key, using an inner state. Based on [4, p. 234]

Optimally, this generated bit sequence should have the same statistical properties as truly random bits. To estimate this statistical quality, a series of statistical tests exist. Pioneering for the evaluation of pseudorandom numbers were the *Golomb-postulates* formulated in the 1960s by the American mathematician Solomon W. Golomb. [8, p. 43] In order not to go beyond the scope of this paper, the focus is not on this topic.

Ultimately, it should not be computationally feasible for an attacker to determine the prospective sequence. No matter how many bits of the keystream one has, the probability of guessing the next bit correctly should not be better than half.

3 Linear Feedback Shift Registers (LFSRs)

The underlying idea of stream ciphers is the generation of a pseudorandom bit stream. An important tool for this are so called *linear feedback shift registers (LFSRs)*. These kind of shift registers are pseudorandom bit generators. This is by no means a new theory. Solomon Golomb wrote in 1967 in his book *Shift Register Sequences* that the approach to generate pseudorandom sequences using LFSRs had been researched for two decades. [8, p. 2] Today, this idea has been developed further, resulting in many, partially theoretical applications that use shift registers. One reason for its popularity is its mathematical interpretability which is discussed in more detail in this chapter.

3.1 Generating Periodic Numbers

A register is a logical unit that has a certain number of memory cells, each of which stores one bit of information. A set with k of these cells forms a register. In the literature, no uniform term is used for these atomic elements of a register. Synonymous terms for the memory cells are, for example, memory elements and stages [9, p. 81], delay elements [10, pp. 186-187], tubes [8, p. 27] or, more broadly, bit sequence [4, p. 429] or bit vector [7, p. 198]. This paper uses the term memory cell as defined by Nigel P. Smart. [11, p. 227]

The crucial factor of a *feedback shift register* (FSR) is the connection of the memory cells via a feedback function R. After a clock signal, the register shifts the contents of each cell to the next. In the process, one bit is shifted out at one end of the register. At the other end, a memory cell is freed. The sequence of these shifted out bits forms the sequence generated by the FSR. The new bit in the cleared memory cell at the other end is calculated by the function R depending on the other bits. The shift direction differs in literature. In Smart and Schneier, for example, the shift is to the right [11, p. 227][4, p. 429] and in Lidl and Niederreiter to the left, which is adopted in this paper. The memory cell at the end where the content is shifted out has the least significant index 0. The cell for which a new bit is calculated per clock signal has index k-1. [10, pp. 186-187].

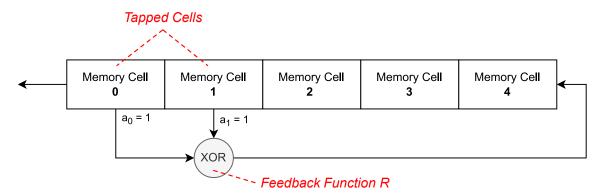


Figure 3: Basic concept of an LFSR of length k = 5. The linear XOR feedback function R combines the values of the tapped memory cells 0 and 1. Based on [4, p. 430]

The characteristic aspect of a *linear feedback shift register* (LFSR) is that the new bit k-1 is calculated by a linear feedback function. That means, certain memory cells calculate the new value of the freed cell with the binary addition XOR. [9, p. 82] These memory cells whose values are used for this calculation are called *tapped* [11, p. 227]. Analogous to LFSRs, nonlinear FSRs use feedback functions, with nonlinear elements, such as binary multiplication. This concept is introduced in chapter **TODO**. Further details can be found in Lidl and Niederreiter, 1986, page 187.

In literature, LFSRs are described with different mathematical approaches. Common approaches use linear algebra, polynomial algebra and the theory of finite fields [10, pp. 186ff.][11, pp. 228ff.]. Less frequently, approaches with formal power series are chosen [6, pp. 53ff.][12, pp. 201ff.]. In this paper, the first three approaches are investigated further.

The sequence generated by an LFSR can be described as this relation:

$$s_{n+k} = a_{k-1}s_{n+k-1} + a_{k-2}s_{n+k-2} + \ldots + a_0s_n + a$$
 for $n = 0, 1, \ldots$

The variable s_{n+k} represents the value of the new calculated bit after n clock signals, which changes state of the LFSR. The values $a, a_0, a_1, \ldots, a_{k-1}$ indicate the tapped memory cells. The constant a will be explained later. If a memory cell a_i is tapped, $a_i = 1$, if not $a_i = 0$. For an LFSR, a, a_i and s_i are elements of the finite field \mathbb{F}_2 . This means they are elements of the Galois field of order p = 2, where p is a prime number. This corresponds to residue field $\mathbb{Z}/2\mathbb{Z}$. [13, p. 48] Initially, the LFSR of length k is assigned the values $s_0, \ldots, s_{k-1} \in \mathbb{F}_2$. The generated sequence $s_0, s_1, \ldots, s_{k-1}, s_k, \ldots, s_{k+n}$ is thus determined by the initial values. [10, pp. 186-187]

Figure 4 shows an LFSR of length k=5 with the initial state: $s_0=1, s_1=1, s_2=0, s_3=1$ and $s_4=1$. The memory cells 0 and 1 are tapped, resulting in $a_0=1$ and $a_1=1$ with the remaining $a_i=0$. After a clock signal, the binary addition $a_0s_0+a_1s_1=1\cdot 1+1\cdot 1=0$ generates the new bit $s_{k-1+1}=s_{4+1}=s_5$, which is inserted in the freed cell after the shift

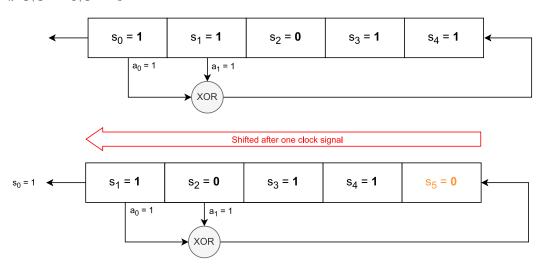


Figure 4: Workflow of an LFSR: After a clock signal, a new bit is calculated and the values of the memory cells are shifted to the left.

The sequence of shifted-out bits generated by an LFSR of length k is called a k-th-order linear recurring sequence. A distinction is made between homogenous k-th-order linear recurring sequences when a=0 and inhomogeneous k-th-order linear recurring sequence when $a\neq 0$. The last case corresponds to the addition of a constant value in \mathbb{F}_2 to the equation above. [10, p. 186] Since LFSRs are usually constructed to produce homogenous k-th-order linear recurring sequences, the case $a\neq 0$ is not considered further here.

As the name suggests, it is characteristic of k-th-order linear recurring sequences that they will repeat. A sequence is said to be periodic if it will repeat after $r \in \mathbb{N}$ generated bits, such that $s_{n+r} = s_n$ for all $n \in \mathbb{N}_0$, where r denotes the period of the sequence. This periodic behavior can possibly occur after $n_0 \in \mathbb{N}_0$ non-periodic bits, such that: $s_{n+r} = s_n$ for all $n \geq n_0$. In this case the sequence is referred to as n_0 as n_0 as n_0 as n_0 . Any n_0 and n_0 as n_0 are n_0 and n_0 as n_0 and n_0 and n_0 as n_0 and n_0 and n_0 as n_0 and n_0 as n_0 and n_0 as n_0 and n_0 and n_0 as n_0 and n_0 and n_0 and n_0 as n_0 and n_0 and n_0 as n_0 and n_0 and n_0 and n_0 and n_0 and n_0 and n_0 are n_0 and n_0 and n_0 and n_0 and n_0 are n_0 and n_0 and n_0 as n_0 and n_0 and n_0 are n_0 and n_0 and n_0 and n_0 and n_0 are n_0 and n_0 and n_0 and n_0 are n_0 and n_0 and n_0 and n_0 and n_0 are n_0 and n_0 and n_0 are n_0 and n_0 and n_0 are n_0 and n_0 are n_0 and n_0 and n_0 are n_0 and n_0 and n_0 are n_0 and n_0 are n_0 and n_0 and n_0 are n_0 and n_0 are n_0 and n_0 are n_0 and n_0 and n_0 are n_0 and n_0

most the value $r \le 2^k - 1$. This can be explained since a register of length k can only hold $2^k - 1$ non-zero states. To avoid the trivial zero period, an LFSR of length k must not be initialized with the zero sequence $s_0 = s_1 = \ldots = s_{k-1} = 0$. [10, p. 189]

Figure 5 shows how the LFSR of length k=5 from the previous example generates a sequence of period r=3. The register returns to the initial state after 3 clock signals. The preperiod is $n_0=0$.

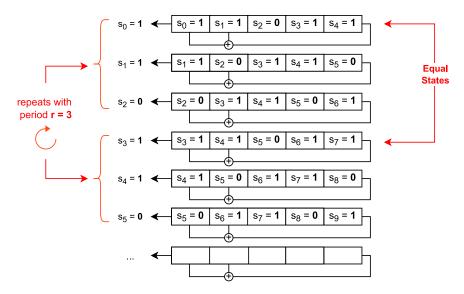


Figure 5: Periodic behavior of an LFSR. Once the state of an LFSR occurs a second time, the generated sequence will repeat.

Depending on the chosen values for $s_0, \ldots, s_{k-1} \in \mathbb{F}_2$ a different sequence with a possibly different period can be obtained. This can be illustrated by the following example: For each period, a corresponding state diagram is plotted. State i in the diagram corresponds to a inner state of the LFSR in decimal notation. This state is formed by the contents of the memory cells 0 to k-1 at a point in time. A state transition is created by applying the feedback function R. This representation is adapted from Nigel P. Smart in [11, pp. 230]. Figure 6 shows all 32 states of the LFSR of length k=5 from the previous two examples.

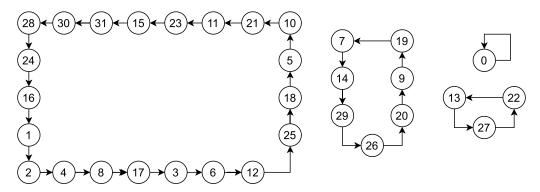


Figure 6: Representation of different periods of an LFSR as state machine diagrams. Based on [11, pp. 230-232]

3.2 m-sequences with Primitive Polynomials

Consequently, the ultimate goal is to generate a pseudorandom number with the largest possible period. One option of trial and error is to select other tapped memory cells and check if the period has improved. Additionally, the choice of initialization values is limited, since different values for the same register may produce sequences with different periods. Such an effort is unfavorable for a symmetric encryption system. The most favorable situation would be to always generate a sequence with the largest possible period for an LFSR, regardless of the initialization vector chosen.

To achieve this situation, a second way must be presented to describe homogeneous k-th-order linear recurring sequences: An LFSR of length k can be represented as the following regular $k \times k$ matrix A over \mathbb{F}_2 : the matrix elements e in the diagonal below the main diagonal are assigned 1: $e_{2,1}, e_{3,2}, \ldots, e_{k,k-1} = 1$. The values of the tapped memory cells a_i as defined above, are written in the k-th column in ascending order by index. [10, p. 191] For consistency, the notation of the matrix was adopted from Lidl and Niederreiter. Nigel P. Smart transposes the matrix [11, p. 218]. As a result, the matrix $A \in \mathbb{F}_2^{k \times k}$ has this form:

$$\begin{pmatrix} 0 & 0 & 0 & \dots & 0 & a_0 \\ 1 & 0 & 0 & \dots & 0 & a_1 \\ 0 & 1 & 0 & \dots & 0 & a_2 \\ \vdots & \vdots & \vdots & & \vdots & \vdots \\ 0 & 0 & 0 & \dots & 1 & a_{k-1} \end{pmatrix} \quad \text{if } k = 1, A \text{ is the } 1 \times 1 \text{ matrix } (a_0)$$

The crucial point is the examination of the characteristic polynomial f(x) of the matrix:

$$f(x) = det(xI - A) = x^{k} - a_{k-1}x^{k-1} - a_{k-2}x^{k-2} - \dots - a_0 \in \mathbb{F}_2[x]$$

In the case of an LFSR, this polynomial is an element of the polynomial ring $\mathbb{F}_2[x]$ over the finite field \mathbb{F}_2 . Therefore, the coefficients of the polynomial – meaning the tapped memory cells – are elements of the finite field \mathbb{F}_2 [13, pp. 18-19]. Therefore, the signs of the coefficients before the indeterminant x can be reversed:

$$f(x) = x^k + a_{k-1}x^{k-1} + a_{k-2}x^{k-2} + \dots + a_0 \in \mathbb{F}_2[x]$$

Only with the use of this characteristic polynomial it is possible to construct an LFSR which generates so-called *maximal period sequences* with elements of \mathbb{F}_2 . This refers to a sequence with the largest possible period and without preperiod, generated by an LFSR for any initial values except 0. These sequences are also denoted as *m-sequences*. Such sequences are generated by an LFSR of length k if the characteristic polynomial of the matrix A is a so-called *primitive polynomial* in $\mathbb{F}_2[x]$. The period for all initial values except the zero initialization is $2^k - 1$. [10, p. 201]

In the literature it is difficult to find a coherent and uniform explanation to primitive polynomials. For this paper the approach of Lidl and Niederreiter via finite fields was chosen.

Defining a primitive polynomial starts with the polynomial f in $\mathbb{F}_2[x]$ of degree $m \geq 1$. For an element α in \mathbb{F}_{2^m} it must hold that $f(\alpha) = 0$ is satisfied, whereby α is called the *root* of f. The finite field \mathbb{F}_{2^m} consists of the elements of $\mathbb{F}_2[x]$ modulus the polynomial f, resulting in $\mathbb{F}_{2^m} = \mathbb{F}_2[x]/f(x)$. [11, p.11] At the same time, f must be the so-called *minimal polynomial* of α : Besides the property

 $f(\alpha)=0$, it must not be possible to factor it into further polynomials in $\mathbb{F}_2[x]$ of degree greater 0. This property is comparable to that of a prime number. Moreover, if there is a polynomial g in $\mathbb{F}_2[x]$ for which also holds $g(\alpha)=0$, then g must divide the minimal polynomial f. The crucial characteristic of a minimal polynomial f is that it is the polynomial of minimal degree with $f(\alpha)=0$, where the leading coefficient must be 1. For this polynomial f to be finally a primitive polynomial, it must hold that α generates the multiplicative group $\mathbb{F}_{2^m}^{\times}$. This group is equal to \mathbb{F}_{2^m} without the 0. Thus $\langle \alpha \rangle = \mathbb{F}_{2^m}^{\times}$ must hold. [13, pp. 23, 31, 89]

Just as it is difficult to test whether a number is prime, it is complex to find out whether a polynomial is primitive. [4, p. 431] Therefore, in the following example, we will only show that a given primitive polynomial satisfies the properties above:

Given is the primitive polynomial $f(x)=x^4+x^1+1$ in $\mathbb{F}_2[x]$ of degree m=4. For $\alpha\in\mathbb{F}_{2^4}$ holds $f(\alpha)=0$. This is how the finite field $\mathbb{F}_{2^4}=\mathbb{F}_2[x]/(x^4+x^1+1)$ is composed:

It can be assumed that the polynomial f is a minimal polynomial. Now it will be shown that $\langle \alpha \rangle = \mathbb{F}_{2^m}^{\times}$, meaning that α generates the multiplicative group $\mathbb{F}_{2^m}^{\times}$.

$$\begin{array}{lll} \alpha^{1} = \alpha & \alpha^{11} = \alpha^{5}\alpha^{6} = (\alpha^{2} + \alpha)(\alpha^{2} + \alpha) \\ \alpha^{2} = \alpha^{2} & = \alpha^{5} + \alpha^{4} + \alpha^{4} + \alpha^{3} \\ \alpha^{3} = \alpha^{3} & = \alpha^{3} + \alpha^{2} + \alpha \\ \alpha^{4} = \alpha + 1 & \alpha^{12} = \alpha^{6}\alpha^{6} = (\alpha^{3} + \alpha^{2})(\alpha^{3} + \alpha^{2}) \\ \alpha^{5} = \alpha(\alpha^{4}) = \alpha(\alpha + 1) & = \alpha^{6} + 2\alpha^{5} + \alpha^{4} \\ = \alpha^{2} + \alpha & = \alpha^{3} + \alpha^{2} + \alpha + 1 \\ \alpha^{6} = \alpha^{2}\alpha^{4} = \alpha^{2}(\alpha + 1) & = \alpha^{6} + \alpha^{4} + \alpha^{3} + \alpha^{5} + \alpha^{3} + \alpha^{2} \\ \alpha^{7} = \alpha^{3}\alpha^{4} = \alpha^{3}(\alpha + 1) = \alpha^{4}\alpha^{3} & = \alpha^{6} + \alpha^{4} + \alpha^{3} + \alpha^{5} + \alpha^{3} + \alpha^{2} \\ = \alpha^{3} + \alpha + 1 & = \alpha^{6} + \alpha^{4} + \alpha^{3} + \alpha^{5} + \alpha^{3} + \alpha^{2} \\ = \alpha^{3} + \alpha^{2} + 1 & = \alpha^{3} + \alpha^{2} + 1 \\ \alpha^{8} = \alpha^{4}\alpha^{4} = (\alpha + 1)(\alpha + 1) & = \alpha^{6} + \alpha^{2} + 1 + 2\alpha^{3}\alpha + 2\alpha^{3} + 2\alpha \\ = \alpha^{2} + 1 & = \alpha^{6} + \alpha^{2} + 1 + 2\alpha^{3}\alpha + 2\alpha^{3} + 2\alpha \\ = \alpha^{3} + \alpha^{2} + \alpha^{2} + 1 & = \alpha^{3} + \alpha^{2} + \alpha^{2} + 1 \\ = \alpha^{3} + \alpha^{2} + \alpha^{2} + \alpha & = \alpha^{3} + \alpha^{2} + \alpha^{2} + 1 \\ = \alpha^{3} + \alpha^{2} +$$

Consequently, $f(x) = x^4 + x^1 + 1$ satisfies the properties of a primitive polynomial. The corresponding LFSR shown in figure 7 can be obtained with the help of the definition of the characteristic polynomial introduced previously: $f(x) = x^k + a_{k-1}x^{k-1} + a_{k-2}x^{k-2} + \ldots + a_0 \in \mathbb{F}_2[x]$

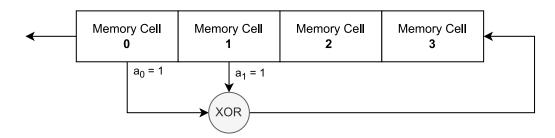


Figure 7: Structure of the LFSR which corresponds to the primitive polynomial $f(x) = x^4 + x^1 + 1$, based on [4, p. 430]

The corresponding state machine diagram for this LFSR of length k=4 is shown in figure 7. As can be seen, apart from the trivial zero period, only one period with maximum length $2^k-1=2^4-1=15$ exists. Thus, this LFSR, of which the characteristic polynomial is primitive, produces an m-sequence.

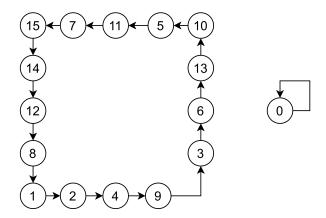


Figure 8: State machine diagram of the largest possible period of an LFSR whose characteristic polynomial is primitive. Based on [11, pp. 230-232]

Proof that such a primitive polynomial indeed generates an m-sequence is difficult to find in the literature. In many cases there are references to other authors or textbooks, without going into more detail or naming them. See, for example [11, p. 229]. I wanted to have found a coherent explanation in the literature to the question: "Why do primitive polynomials in $\mathbb{F}_2[x]$ of degree k generate sequences with maximum period $2^k - 1$?" For this paper, the example shown above should be sufficient.

In addition to having the longest possible period, m-sequences satisfy Solomon Golomb's randomness criteria mentioned initially [14, p. 2847]. Thus, m-sequences are statistically secure pseudorandom numbers. However, the fact that a statistically secure pseudorandom number, such as the m-sequence, does not necessarily equal a cryptographically secure number will be discussed in the next chapter.

4 Security of stream ciphers based on LFSRs

To evaluate the usability of encryption methods in real-world problems, multiple factors need to be analyzed, like the ease of implementation, performance and security. Based on the discussed technical realization, a pseudo-random bit stream can be generated. By applying the primitive polynomial to an LFSR, their output always has the largest possible period indifferent to their initial values of the memory cells. This allows for fast encryption of messages with unknown length [11, p. 181]. Further, the next bits of the keystream can be calculated in advance to improve processing speed [15, p. 3]. In computer hardware the LFSRs are efficiently implemented with shift registers [9]. These reasons established their wide usage in cryptographic contexts [16, p. 97]. For example, real-time audio and video data were encrypted with stream ciphers. Especially since there is no error propagation because if one bit of the stream differs from its correct value, the following bits are not defective [11, p. 181]. Nevertheless, the remaining main concern regarding stream ciphers is their security aspects.

4.1 Known-plaintext attack

In cryptanalysis, attacks can be categorized based on the data available to the adversary. Besides the ciphertext-only attacks and chosen-plaintext attacks, there is also the group of known-plaintext attacks. Here, the plaintext and its position of an encrypted sequence are laid open. Because of their linear nature, LFSR-based stream ciphers are prone to these known-plaintext attacks: Given the adversary has a segment of the encrypted message s and the corresponding plaintext p, the used keystream k can be reproduced by calculating $s_i \oplus p_i$. This is possible due to the mathematical laws of XOR like $b \oplus b = 0$ and $b \oplus b = b$. The method can be especially abused for metadata like header fields since their structure and content are mostly known [17, p. 359]. Figure 9 illustrates the principle behind a basic known-plaintext attack.

```
\begin{array}{ll} \textit{Given:} & s & = (s_0, s_1, ..., s_n) \in \mathbb{F}_2 & := \text{encrypted message} \\ & p & = (p_0, p_1, ..., p_n) \in \mathbb{F}_2 & := \text{plaintext} \\ & k & = (k_0, k_1, ..., k_n) \in \mathbb{F}_2 & := \text{keystream} \\ & f(p_i, k_i) & = k_i \oplus p_i = s_i & := \text{encryption function} \\ & \textit{Attack:} & s_i \oplus p_i = (k_i \oplus p_i) \oplus p_i = k_i \oplus 0 = k_i \end{array}
```

Figure 9: Basic known-plaintext attack on a stream cipher

If the period of a keystream is shorter than the gained segment of its plaintext, then the rest of the message can be decrypted [12, p. 9]. Therefore, a large period is necessary to diminish this threat [9, p. 83]. Even if it is not possible for the adversary to recreate the complete keystream period, the original data p can be replaced by malicious content p' of the same length. To demonstrate this, it is assumed that the position of the plaintext '10.000 $\ensuremath{\in}$ ' and its corresponding encrypted message s is known. The first digit is now replaced by a '9' in Figure 10. This is a clear security concern.

```
Sender (sends s):

p = 00000001_{(2)} = 1_{(10)}

k = 101111110_{(2)}

s = k \oplus p = 101111110 \oplus 00000001 = 10110101

Adversary (receives s, knows p and sends s'):

p' = 00001010_{(2)} = 9_{(10)}

k \oplus p' = s \oplus p \oplus p'

= 10110101 \oplus 000000001 \oplus 00001010

= 10110100 \oplus 00001010 = 101111110 = s'

Receiver (receives s'):

k \oplus s' = 101111110 \oplus 101110101 = 00001001 = 9_{(10)}
```

Figure 10: Replacing original data with modified text in a known-plaintext attack

4.2 Linear complexity and the Berlekamp-Massey algorithm

Besides the period of a sequence, linear complexity is also used as an indicator for the cryptographic usefulness of this sequence.

Definition: The linear complexity L(s) of a finite binary sequence s is equal to the length and therefore degree of the shortest LFSR to generate s [11, p. 233]. L follows the properties [18, pp. 20-21]:

 $\begin{array}{lll} \bullet & s \text{ is the zero sequence with } (0,0,...,0) & \Leftrightarrow & L(s)=0 \\ \bullet & s \text{ is the zero sequence with } (0,0,...,0) & \Leftrightarrow & L(s)=0 \\ \bullet & s \text{ has length } n \text{ with format } (0,0,...,1) & \Leftrightarrow & L(s)=n \\ \bullet & s \text{ cannot be generated by an LFSR} & \Rightarrow & L(s)=\infty \\ \bullet & s \text{ is periodic with period } r & \Rightarrow & L(s)\leq r \\ \bullet & s \text{ is the one-periodic sequence of a primitive} & \Rightarrow & L(s)=n \\ \text{feedback polynomial with degree } n & \Rightarrow & L(s)=n \\ \end{array}$

The Berlekamp-Massey algorithm presented in the paper 'Shift-register synthesis and BCH decoding' can be used to calculate the linear complexity of a sequence and its corresponding shortest LFSR. Given a primitive polynomial has degree n and consequently, its generated period has linear complexity of L, if an adversary gains a sequence of the keystream $k \geq 2L$, the primitive polynomial can be determined [19, pp. 124-125]. The used LFSR can then be successfully asserted. So the linear complexity is directly connected to the required keystream sequence to crack the stream cipher.

Exploiting a known-plaintext attack, a finite sequence of the keystream can be obtained. This sequence can be used as input for the Berlekamp-Massey algorithm to try to recreate the LFSR generating the full period of the keystream [11, p. 232]. The algorithm has an efficient linear run time of O(n) for a sequence with length n. Its structure is displayed in Figure 11.

```
\begin{array}{ll} s & = (s_0, s_1, ..., s_n) \in \mathbb{F}_2 := \text{keystream sequence of the LFSR and input for the algorithm} \\ n & := \text{length of the input sequence} \\ i & := \text{current index of the input sequence} \\ i' & := \text{previous index since the last increment of the linear complexity} \\ C(x) & = 1 + c_1 x^1 + c_2 x^2 + ... + c_i x^i \pmod{2} \\ & := \text{feedback connection polynomial of the minimal LFSR generating } s \\ c_i & := \text{if tapped:} c_i = 1 \text{else} c_i = 0 \\ B(x) & := \text{previous connection polynomial since the last increment of the linear complexity} \\ L & := \text{linear complexity of the minimal LFSR} \\ d & := \text{discrepancy between the input and the output generated by } C(x) \\ \end{array}
```

Berlekamp-Massey(s):

```
\begin{split} n &= |s| \\ C(x) &= B(x) = 1 \\ L &= i = 0 \\ i' &= -1 \\ \text{while } i < n: \\ d &= s_i \oplus c_1 s_{i-1} \oplus c_2 s_{i-2} \oplus \ldots \oplus c_L s_{i-L} \\ \text{if } d &= 1: \\ C_{tmp}(x) &= C(x) \\ C(x) &= C(x) + (B(x) * x^{i-i'}) \\ \text{if } L &<= \frac{i}{2}: \\ L &= i + 1 - L \\ i' &= i \\ B(x) &= C_{tmp}(x) \\ i &= i + 1 \\ return(L, C(x)) \end{split}
```

Figure 11: Explanation and structure of the Berlekamp-Massey algorithm [19]

As a demonstration, the LFSR in Figure 8 with characteristic polynomial $G(x)=1+x+x^4$ is uniquely determined by inputting its bit sequence into the Berlekamp-Massey algorithm. A characteristic polynomial G(x) and connection polynomial C(x) both represent the structure of an LFSR. However, they differ in their written mathematical form. The formula $G(x)=x^L*C(\frac{1}{x})$ describes their relation to each other. The expected output for the demonstration can be calculated by reversing the above equation, resulting in $C(x)=1+x^3+x^4$. As input, the six-bit deciphered keystream s=110001 is used which corresponds to the initial state $s_{12}=12_{(10)}=1100$ of the LFSR. The state and action of each iteration of the loop over the input sequence are presented in Figure 12.

Current values of attributes		Result	ing attribute assignments
Input: $s = 110001$	n=6		
i=0 $i'=-1$		d	← 1
C(x) = 1		C(x)	$\leftarrow 1 + 1 * x^1 = 1 + x$
	,	L	$\leftarrow 1 i' \leftarrow 0 B(x) \leftarrow 1$
i=1 $i'=0$	L=1	d	$\leftarrow 0 \oplus 1 \odot 1 = 0$
C(x) = 1 + x	B(x) = 1		
i=2 $i'=0$	L=1	d	$\leftarrow 0 \oplus 1 \odot 1 \oplus 1 \odot 0 = 1$
C(x) = 1 + x	B(x) = 1	C(x)	$\leftarrow 1 + 1 * x^1 + (1 * x^{2-0}) = 1 + x + x^2$
		L	$\leftarrow 2 i' \leftarrow 2 B(x) \leftarrow 1 + x$
i=3 $i'=2$	L=2	d	$\leftarrow 0 \oplus 1 \odot 0 \oplus 1 \odot 1 \oplus 1 \odot 0 = 1$
$C(x) = 1 + x + x^2$	B(x) = 1 + x	C(x)	$\leftarrow 1 + x + x^2 + ((1+x) * x^{3-2})$
			$\leftarrow 1 + 2x + 2x^2 \pmod{2} = 1$
i=4 $i'=2$	L=2	d	$\leftarrow 0$
C(x) = 1	B(x) = 1 + x		
i=5 $i'=2$	L=2	d	← 1
C(x) = 1	B(x) = 1 + x	C(x)	$\leftarrow 1 + ((1+x) * x^{5-2}) = 1 + x^3 + x^4$
		L	$\leftarrow 5 i' \leftarrow 5 B(x) \leftarrow 1$
		Outpu	t: $C(x) = 1 + x^3 + x^4$ $L = 4$

Figure 12: Step-by-step execution of the Berlekamp-Massey algorithm

To again validate the gained connection polynomial, it is inserted into the equation $G(x) = x^L * C(\frac{1}{x}) = x^4 * (1 + \frac{1}{x^3} + \frac{1}{x^4}) = x^4 + x + 1$. The expected output was indeed computed correctly by the algorithm after six iterations. In the example, the adversary knew only six bits and recreated the LFSR successfully. In this case, she would have required at most 2 * L = 2 * 4 = 8 bits for the algorithm to determine the correct connection polynomial. This can also be achieved with an equation as long as the length of the LFSR is known. To calculate which memory cell c_i is tapped, the following formula can be used [11, p. 232]:

$$\begin{pmatrix} s_{L-1} & s_{L-2} & \dots & s_1 & s_0 \\ s_L & s_{L-1} & \dots & s_2 & s_1 \\ \dots & \dots & \dots & \dots & \dots \\ s_{2L-3} & s_{2L-4} & \dots & s_{L-1} & s_{L-2} \\ s_{2L-2} & s_{2L-3} & \dots & s_L & s_{L-1} \end{pmatrix} * \begin{pmatrix} c_1 \\ c_2 \\ \dots \\ c_{L-1} \\ c_L \end{pmatrix} = \begin{pmatrix} s_L \\ s_{L+1} \\ \dots \\ s_{2L-2} \\ s_{2L-1} \end{pmatrix}$$

Since the degree of a primitive polynomial is equal to its linear complexity, even for an LFSR with a period of $2^{512} - 1$ only 1024 bits of the keystream are required to crack the stream cipher. Thus pure LFSRs are of no value as cryptographic tools due to their linear behavior [11, p. 231].

5 Increasing the cryptographic qualities of LFSRs

In an attempt to increase the security of LFSRs, several adjustments to their basic structure can be made [16, p. 97]. The discussed exploits mainly abuse their linear nature. Therefore, LFSRs can be combined with nonlinear transformations, for example, the multiplication of two bits by an AND function, to diminish their weak points. In this section, a few concepts based on this idea are expanded.

5.1 Nonlinear Feedback Shift Registers (NLFSR)

Instead of combining the keystream and plaintext linearly, NLFSRs utilize a nonlinear feedback function. This approach seems secure since there are 2^{2^n} possible Boolean functions for n bits. However, it is mathematically proven that every bit of a keystream with period r can be correctly determined after at most r bits. For example, the algorithm by Boyar and Krawczyk recursively computes the whole keystream after n+m bits of plaintext, where n is the length of the NLFSR and m is the number of degrees of freedom of the feedback function. The required plaintext is generally much smaller than the period of the NLFSRs. There are even further restrictions that keep reducing their cryptographic values. To still guarantee the effective calculation of the nonlinear function, the amount of tapped cells is limited to a realistic sum. [20]

In this paper, the usage of pure NLFSRs is not further pursued because they cannot be securely used on their own. [16, p. 97]

5.2 Combining Linear Feedback Shift Registers with an output generator

Another solution is to combine the n output bits of multiple LFSRs based on a nonlinear function $f: \mathbb{F}_2^n \to \mathbb{F}_2$. A famous representative of this group is the *Geffe generator* which was developed by P.R. Geffe in 1973. It involves two LFSRs, whose outputs a and b are chained together by the nonlinear function $f(a_i,b_i,c_i)=k_i=a_i+c_ia_i+c_ib_i$, where c is produced by a third LFSR. The idea behind a Geffe generator is the selection of the keystream bit k_i based on c_i . If $c_i=0$ then a_i is returned, else b_i is chosen. [21] Thus, the combination component is implemented as a *multiplexer* whose index value is c_i [15, p. 19]. The three LFSRs can also be of different length. This structure can be seen in Figure 13.

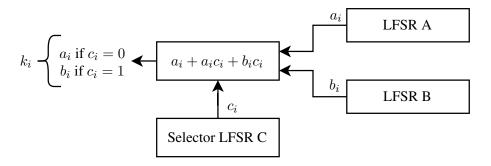


Figure 13: Graphic representation of a Geffe generator

This results in the following change in cryptographic metrics of the output sequence k: [11, p. 234]

Linear Complexity
$$L_k = L_A + (L_A * L_C) + (L_B * L_C)$$

Period $r_k = (2^{L_A} - 1) * (2^{L_B} - 1) * (2^{L_C} - 1)$

The increase in linear complexity and period improves the quality of the stream cipher. However, the Geffe generator has a statistical weakness that can be exploited. To prove this statement, its truth table is created in Figure 14.

a_i	b_i	c_i	k_i
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	1

Figure 14: Truth table of a Geffe generator

It can be deducted, that an output bit k_i has the probability $p = \frac{3}{4}$ of being equal to the bit a_i of LFSR A. The same can be seen for LFSR B. Consequently, the Geffe generator can be easily broken with a correlation attack [16, p. 104].

A correlation attack is a brute-force plaintext attack in which the correlation between the final output keystream and the generated sequence of one of the LFSRs is exploited. First, a random initial state for LFSR A is produced. The resulting output of the system based on this guessed state is compared to the actual output stream. If the state is correct, then they should be equal in approximately $\frac{3}{4}$ ths of their bits. If so, the next step is to calculate the initial state of LFSR B and afterwards repeated for LFSR C. The problem of estimating the initial states of all LFSRs at once is divided into determining only one at a time. This attack pattern is also called a divide-and-conquer attack. [15, p. 17] Without this approach the brute-force method requires roughly ' $2^{L_A+L_B+L_C}$ times the total number of possible connection polynomials' operations to guess the three initial states. The first part of the equation is reduced to $2^{L_A} + 2^{L_B} + 2^{L_C}$, due to the separated calculation of the initial state by the

divide-and-conquer pattern. The probability of guessing the correct states for all LFSRs amplifies drastically. [11, p. 235]

To measure the qualities of a cipher concerning correlation attacks, a new term was introduced. A function is m^{th} -order correlation-immune when any subset of m input bits are uncorrelated to the output bit [22, p. 777]. In the case of a Geffe generator, the function is of 1st-order correlation-immune, since only the sequence of LFSR C has no influence on the output bits. Interestingly, high linear complexity L of the combination function results in low correlation-immunity m [22, p. 779].

5.3 Extending nonlinear output generators with memory cells

To counter the undesired effect between linear complexity and correlation-immunity, the combination function can be expanded with a memory component to achieve maximum linear complexity and correlation-immunity at the same time [15, p. 17]. The addition of memory turns the system into a nonlinear finite state machine [23, p. 209]. The combination function f is modified to not only produce the output bit k but also the next state of the machine σ .

The *summation combiner* uses this principle and allows for maximum linear complexity and correlation-immunity [24, p. 261]. The generated bits from two LFSRs are added together every tact and the resulting carry σ is saved in an one-bit memory, similar to an integer addition. This method proves to be highly nonlinear. The general structure is illustrated in Figure 15. [25, p. 70]

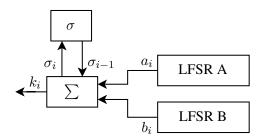


Figure 15: Basic overview of a summation combiner with two input LFSRs

Its combination functions are defined as:

$$f_k(a_i,b_i): k_i = a_i \oplus b_i \oplus \sigma_{i-1} := \text{keystream bit}$$

 $f_{\sigma}(a_i,b_i): \sigma_i = a_ib_i \oplus a_i\sigma_{i-1} \oplus b_i\sigma_{i-1} := \text{carry}$

The summation combiner accomplishes improvements in regards to the period r_k and linear complexity L_k of the produced keystream k. The period r_k is equal to $r_a * r_b$ for a integer addition of two sequences a and b with their corresponding periods r_a , r_b assuming $gcd(r_a, r_b) = 1$ [12, p. 220]. Also the gained linear complexity L_k is generally close to the period r_k with $L_k \le r_a * r_b$ [12, p. 225]. In terms of the correlation between the state and its output, it still shows a slight bias which can be exploited by a correlation attack. This bias can be decreased by adding more LFSRs as input for the integer addition and increasing its memory [25, pp. 81-82].

5.4 Clock-Controlled Linear Feedback Shift Registers

Until this point in the paper, all registers were shifted on every clock tick. A different idea for refining the security characteristics of LFSRs is changing their tick rate based on the output of another register. This introduces a nonlinear nature to the keystream. The *Stop-and-Go generator* for example has two LFSRs A and B with their output a_i and b_i . The clock signal c_i is AND-combined with b_i before being connected to the LFSR A, so that the register is only shifted if $b_i = 1$. As long as $(b_i, b_{i+1}, ..., b_{i+n}) = (0, 0, ..., 0)$ the generator produces the unchanged bit a_i . [26, pp. 89-90] Since the Stop-and-Go generator suffers from a high correlation between b_i and a switch in the bits of the keystream, it is prone to correlation attacks [27, p. 156]. An improvement to this concept is the *shrinking generator*. Instead of outputting the same bit if $b_i = 0$, the LFSR A is shifted regardless but nothing is added to the keystream. This restricts the threat of correlation attacks [27, p. 159]. Even though the input clock signal is not directly tampered with, the shrinking generator counts towards the group of the clock-controlled stream ciphers [15, p. 23]. In Figure 16 the general structures of both generators are shown.

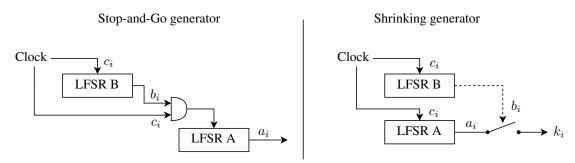


Figure 16: The Stop-and-Go [26, pp. 89-90] and shrinking generator [27, p. 159]

Let LFSR A have a maximal period of $r_A=2^{L_A}-1$ with linear complexity L_A and LFSR B has accordingly period $r_B=2^{L_B}-1$ with linear complexity L_B . On the condition that L_A and L_B are relative prime, meaning $gcd(L_A,L_B)=1$, the period r_k of the output keystream k is equal to $r_k=2^{L_B-1}*r_A=2^{L_B-1}*(2^{L_A}-1)$ [28, p. 25][27, p. 159]. Furthermore, the linear complexity of the output keystream L_k can be described as: $L_A*2^{L_B-2}< L_k \le L_A*2^{L_A-1}$ [28, p. 25]. Because the shrinking generator only sends bits if $b_i=1$, the time between the consecutive bits k_i and k_{i+1} can be measured. The stream of LFSR B can then be immediately reconstructed. These timing attacks are a specific variant of so called side-channel attacks. To lower the possibility of such attacks, dummy operations can be implemented or the actual bit b_i can be hidden by caching the output stream. [27, pp. 163-164]

The metrics to measure the cryptographic applicability of stream ciphers such as their period, linear complexity and correlation-immunity only indicate required properties to guarantee minimal security not their actual real-world usability [28, p. 24]. Over time a lot of stream ciphers have been cracked. For them to be reliable even today, more adjustments need to be made.

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