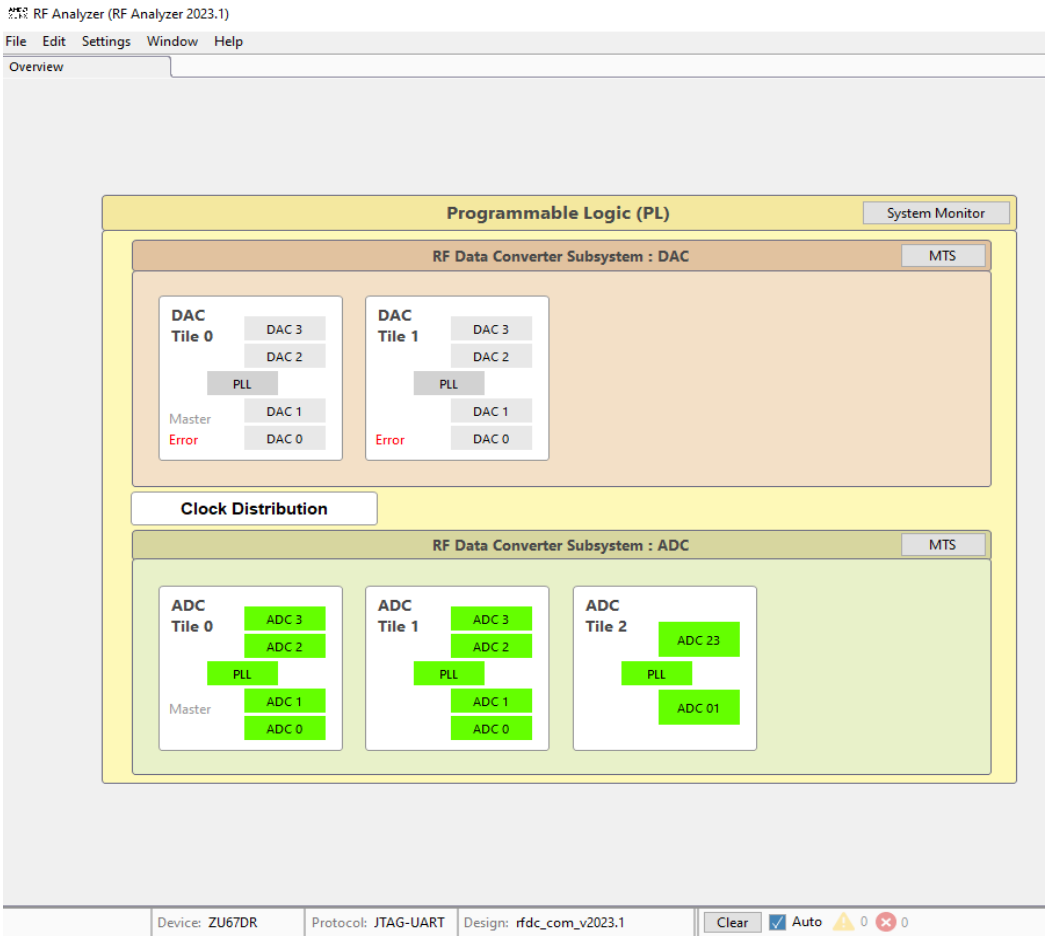
INITIAL STEPS

- Connect zcu670 to usb port on pc and turn on (black and red switch near microUSB connection)

- Open RF analyzer and connect to local server (target is on local machine). ZCU670 should show up under hardware.

- Click on the yellow folder icon near bitstream path to select bitstream. You want RF_Analyzer_67DR_32_mA.bit and click "Download Bitstream"

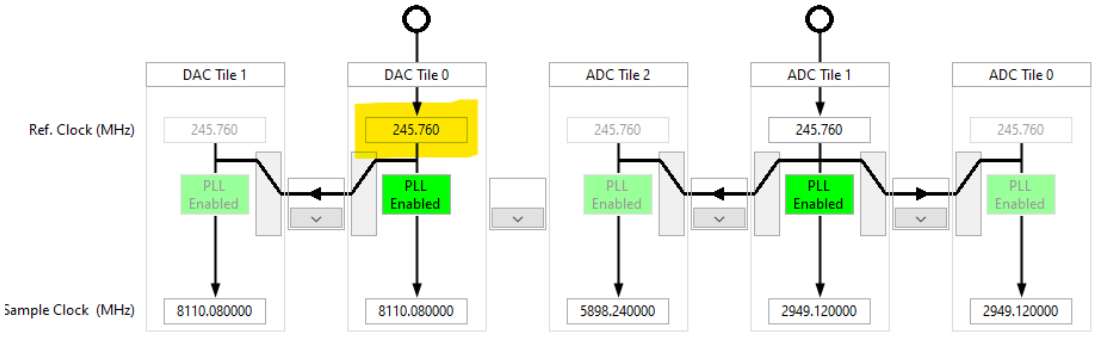
- Select xzcu67dr in the hardware window. Click "select target" next to the bitstream path.



this is what you should be seeing at this point.

- Notice there are errors on the two DACS. That's because the clocks are configured incorrectly. Click the "Clock Distribution" button to open the clock settings for the DAC/ADCs.

- Change the clock value of DAC tile to match the ADC tiles. It should be 245.760. Hit "Apply."



Tile	Sample Clock (MHz)	PLL	Ref. Clock (MHz)	Source Tile	Distribute Clock
DAC Tile 1	8110.080000	☑	245.760	DAC Tile 0	None
DAC Tile 0	8110.080000	☑	245.760	DAC Tile 0	Input Ref. Clock
ADC Tile 2	5898.240000	☑	245.760	ADC Tile 1	None
ADC Tile 1	2949.120000	☑	245.760	ADC Tile 1	Input Ref. Clock
ADC Tile 0	2949.120000	☑	245.760	ADC Tile 1	None

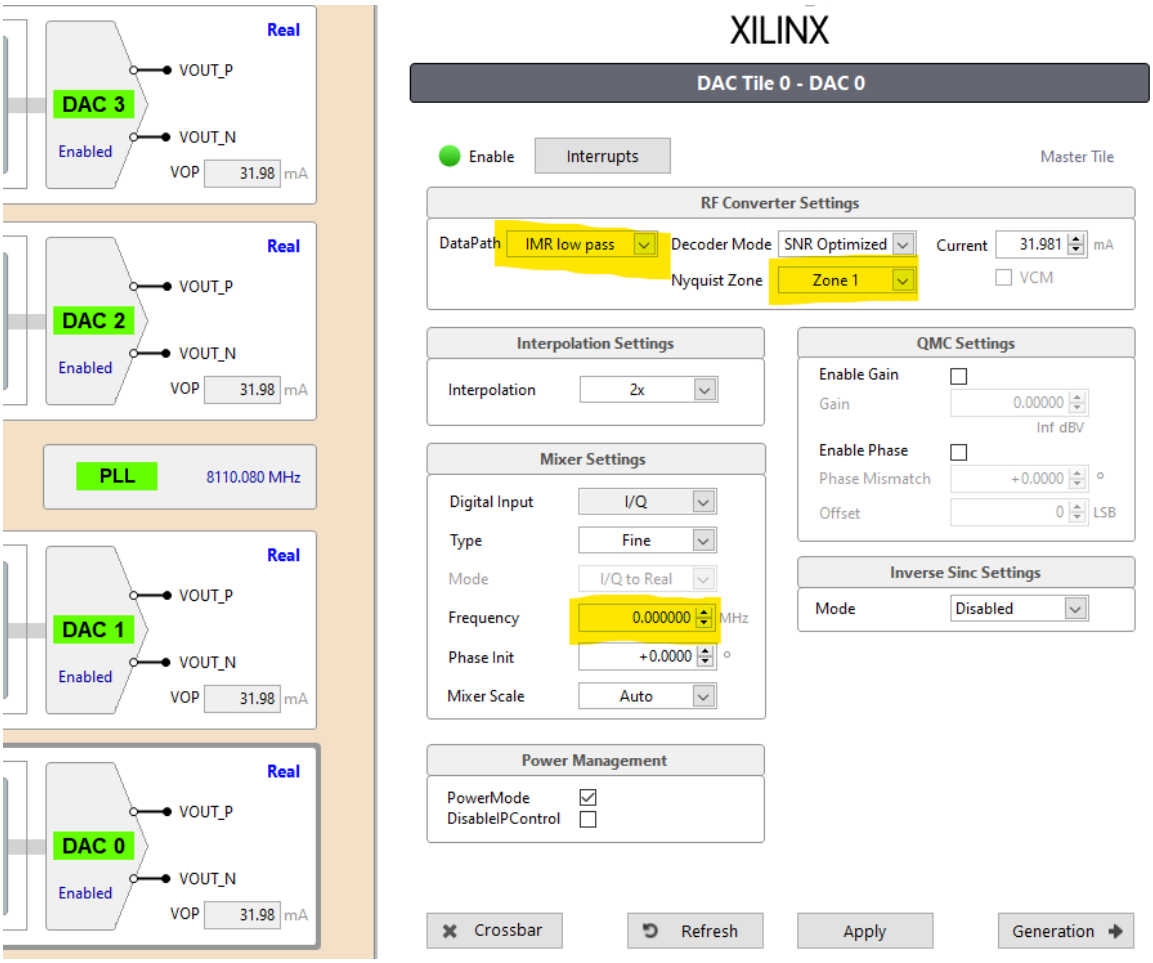
Refresh Apply

correct DAC clock values

HARDWARE SETUP

- Follow the Xilinxs procedure for board connections.

- Depending what DAC/ADCs you want to test, connect it accordingly.



In our case since we were transmitting at higher frequency, we selected Nyquist Zone to be 2 (unlike the photo).